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# SKY14151-350LF: 20 MHz-2.5 GHz High Power SP4T Switch With Decoder

## Applications

- GSM/WCDMA/EDGE datacards and handsets
- Mobile high power switching systems

## Features

- Broadband frequency range: 20 MHz to 2.5 GHz
- Low insertion loss: 0.40 dB @ 1 GHz, 0.45 dB @ 2 GHz with high isolation (29 dB @ 1 GHz)
- $V_{DD}$: 2.5 to 3.0 V for high power applications; can be used down to 1.8 V for low power applications
- High linearity IMD $<$ −111 dBm over phase
- Good harmonic performance $<$ −80 dBc @ 0.9 GHz
- Low voltage logic compatible (minimum $V_{HIGH}$ = 1.8 V)
- Small, QFN (16-pin, 3 x 3 mm) package (MSL1, 260 °C per JEDEC J-STD-020)

NEW ►

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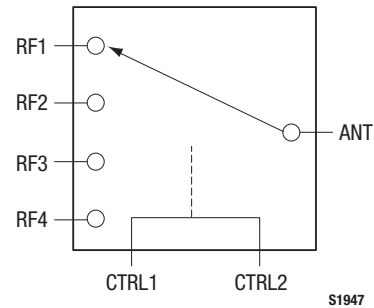


Figure 1. SKY14151-350LF Block Diagram

## Description

The SKY14151-350LF is a symmetrical, single-pole, four-throw (SP4T) switch. The device is designed for broadband, high power switching applications that demand high linearity and low insertion loss. This is a general purpose switch optimized for a variety of multimode applications such as GSM/WCDMA/EDGE.

The switch is manufactured using Skyworks state-of-the-art pHEMT process. The SKY14151-350LF features integrated logic that uses only two control lines for switch operation. The low current consumption makes the device suitable for battery-operated applications.

The SKY14151-350LF SP4T switch is provided in a compact Quad Flat No-Lead (QFN) 3 x 3 mm package. A functional block diagram is shown in Figure 1. The pin configuration and package are shown in Figure 2. Signal pin assignments and functional pin descriptions are provided in Table 1.
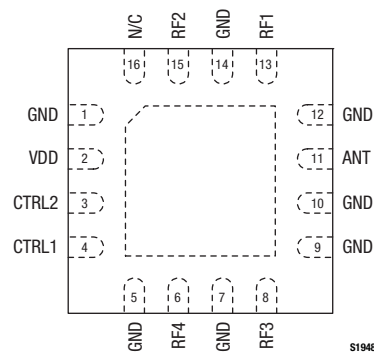


Figure 2. SKY14151-350LF Pinout – 16-Pin QFN (Top View)

## Table 1. SKY14151-350LF Signal Descriptions

| Pin # | Name | Description | Pin # | Name | Description |
|---|---|---|---|---|---|
| 1 | GND | Ground | 9 | GND | Ground |
| 2 | VDD | Supply voltage input. The voltage may be switched. The switching time must be no longer than the start-up time. | 10 | GND | Ground |
| 3 | CTRL2 | Control signal 2. The logic level applied to this pin, along with the logic level applied to pin 4, controls the state of the switch. | 11 | ANT | Antenna. This pin is connected directly and exclusively to pin 6, 8, 13, or 15 depending on the control voltage applied to pins 3 and 4. A DC blocking capacitor is required. |
| 4 | CTRL1 | Control signal 1. The logic level applied to this pin, along with the logic level applied to pin 3, controls the state of the switch. | 12 | GND | Ground |
| 5 | GND | Ground | 13 | RF1 | RF output 1. This pin is either connected directly to or is disconnected from pin 11, depending on the control voltage applied to pins 3 and 4. A DC blocking capacitor is required. |
| 6 | RF4 | RF output 4. This pin is either connected directly to or is disconnected from pin 11, depending on the control voltage applied to pins 3 and 4. A DC blocking capacitor is required. | 14 | GND | Ground |
| 7 | GND | Ground | 15 | RF2 | RF output 2. This pin is either connected directly to or is disconnected from pin 11, depending on the control voltage applied to pins 3 and 4. A DC blocking capacitor is required. |
| 8 | RF3 | RF output 3. This pin is either connected directly to or is disconnected from pin 11, depending on the control voltage applied to pins 3 and 4. A DC blocking capacitor is required. | 16 | N/C | No connection. |

## Table 2. SKY14151-350LF Absolute Maximum Ratings

| Parameter | Symbol | Minimum | Maximum | Units |
|---|---|---|---|---|
| Supply voltage | $V_{DD}$ | | 3 | V |
| Input power (20 MHz to 2.5 GHz, $V_{DD}$ = 2.5 to 3.0 V) | $P_{IN}$ | | +36 | dBm |
| Control voltage | CTRL1, CTRL2 | | 3 | V |
| Operating temperature | $T_{OP}$ | −40 | +85 | °C |
| Storage temperature | $T_{STG}$ | −50 | +100 | °C |

**Note:** Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

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**CAUTION**: *Although this device is designed to be as robust as possible, Electrostatic Discharge (ESD) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.*

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## Functional Description

The SKY14151-350LF is comprised of a CMOS decoder that enables two TTL-compatible DC lines to control four RF ports. The decoder is internally connected to a GaAs pHEMT RF switch. Depending on the logic voltage level applied to the control pins, the ANT pin is connected to one of four switched RF outputs (RF1, RF2, RF3, or RF4) by a low insertion loss path, while maintaining a high isolation path to the alternate port.

Startup time is defined as the time from when $V_{DD}$ is applied to when the switch is active. Once the startup time has passed, the control voltages CTRL1 and CTRL2 can be applied. RF power should not be applied during the startup time or damage to the device could result.

The recommended startup sequence is:

Step 1: Apply $V_{DD}$.

Step 2: Apply CTRL1 and CTRL2

Step 3: Apply RF input.

The device must be turned off in the reverse order.

When $V_{DD}$ is not applied, the device is considered off or inactive. All arms of the switch remain on in this state, creating a poor four-way power splitter. The return loss of all RF ports is very low in this state. RF should not be applied when $V_{DD}$ is not present and should only be used to conserve current.

## Electrical and Mechanical Specifications

The absolute maximum ratings of the SKY14151-350LF are provided in Table 2. Electrical specifications are provided in Table 3.

The state of the SKY14151-350LF is determined by the logic provided in Table 4.

Typical performance characteristics of the SKY14151-350LF are illustrated in Figures 3 through 10.

Figure 11 illustrates the test setup used to measure data for 3rd Order Intermodulation Distortion (IMD3) testing. Figure 12 illustrates the test setup used to measure triple beat ratio data.

**Table 3. SKY14151-350LF Electrical Specifications (1 of 3) (Note 1)**
**($V_{DD}$ = 2.65 V, CTRL1 = CTRL2 = $V_{DD}$, $T_{OP}$ = +25 °C, All Unused RF Ports are Terminated in a 50 $\Omega$ Load, Unless Otherwise Noted)**

| Parameter | Symbol | Test Condition | Min | Typical | Max | Units |
|---|---|---|---|---|---|---|
| *RF Specifications* | | | | | | |
| Insertion loss | IL | 0.02 to 1.0 GHz | | 0.40 | 0.50 | dB |
| | | 1.0 to 2.0 GHz | | 0.45 | 0.60 | dB |
| | | 2.0 to 2.1 GHz | | 0.50 | 0.60 | dB |
| Isolation | ISO | 0.02 to 1.0 GHz | 26 | 29 | | dB |
| | | 1.0 to 2.0 GHz | 19 | 24 | | dB |
| | | 2.0 to 2.1 GHz | 19 | 23 | | dB |
| Return loss | \|S11\| | 0.02 to 2.5 GHz, all RF ports, insertion loss state | | 15 | | dB |
| Second harmonic | 2fo | $f_{FUND}$ = 900 MHz, $P_{IN}$ = +34.5 dBm | −75 | −80 | | dBc |
| | | $f_{FUND}$ = 0.12 GHz, $P_{IN}$ = +10 dBm | −75 | −80 | | dBc |
| | | $f_{FUND}$ = 0.4 GHz, $P_{IN}$ = +10 dBm | −80 | −90 | | dBc |
| | | $f_{FUND}$ = 1.8 GHz, $P_{IN}$ = +31.5 dBm | −70 | −85 | | dBc |
| Third harmonic | 3fo | $f_{FUND}$ = 900 MHz, $P_{IN}$ = +34.5 dBm | −70 | −80 | | dBc |
| | | $f_{FUND}$ = 0.12 GHz, $P_{IN}$ = +10 dBm | −80 | −90 | | dBc |
| | | $f_{FUND}$ = 0.4 GHz, $P_{IN}$ = +10 dBm | −80 | −90 | | dBc |
| | | $f_{FUND}$ = 1.8 GHz, $P_{IN}$ = +31.5 dBm | −70 | −80 | | dBc |

**Table 3. SKY14151-350LF Electrical Specifications (2 of 3) (Note 1)**
**($V_{DD}$ = 2.65 V, CTRL1 = CTRL2 = $V_{DD}$, $T_{OP}$ = +25 °C, All Unused RF Ports are Terminated in a 50 Ω Load, Unless Otherwise Noted)**

| Parameter | Symbol | Test Condition | Min | Typical | Max | Units |
|---|---|---|---|---|---|---|
| *RF Specifications (continued)* | | | | | | |
| Input 0.1 dB compression point | P0.1dB | @ 48 MHz<br>@ 900 MHz<br>@ 1.8 GHz | <br>+37.0<br>+34.0 | +38.5<br>+38.0<br>+35.0 | | dBm<br>dBm<br>dBm |
| 3$^{rd}$ Order Intermodulation Distortion | IMD3 | $f_{FUND}$ = 1.95 GHz @ +20 dBm, $f_{BLK}$ = 1.76 GHz @ −15 dBm, $f_{RX}$ = 2.14 GHz, worst case over phase. See Figure 9. | | −111 | | dBm |
| Power handling under mismatch | | @ 900 MHz, VSWR = 20:1<br>@ 1.8 GHz, VSWR = 20:1 | | +34.5<br>+31.5 | | dBm<br>dBm |
| 2$^{nd}$ Order Input Intercept Point | IIP2 | $f_0$ = 836.61 MHz @ +26 dBm, $f_2$ = 1718.22 MHz @ −20 dBm, measured @ 881.61 MHz | | +113.5 | | dBm |
| Switching speed | | 10/90% RF rise/fall time | | 5 | | µs |
| Startup time | | Wait time required from when $V_{DD}$ is applied until control voltage can be applied | | 25 | | µs |
| Triple beat ratio (matched) | 3BR | $f_1$ = 1880 MHz, $P_1$ = +21.5 dBm, $f_2$ = 1881 MHz, $P_2$ = +21.5 dBm, $f_{JAMMER}$ = 1960 MHz, $P_{JAMMER}$ = −30 dBm, measured @ 1959 MHz and 1961 MHz for worst case, 50 Ω load | +81 | +82 | | dBc |
| | | $f_1$ = 891.025 MHz @ +14 dBm, $f_2$ = 892.025 MHz, $P_2$ = +14 dBm, $f_{JAMMER}$ = 836.0125 MHz, $P_{JAMMER}$ = −30 dBm, measured @ 835.0125 MHz and 837.0125 MHz for worst case, 50 Ω load | +81 | +82 | | dBc |
| | | $f_1$ = 825.995 MHz, $P_1$ = +21.5 dBm, $f_2$ = 826.995 MHz, $P_2$ = +21.5 dBm, $f_{JAMMER}$ = 870.995 MHz, $P_{JAMMER}$ = −30 dBm, measured @ 869.995 MHz and 871.995 MHz for worst case, 50 Ω load | +81 | +82 | | dBc |

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**Table 3. SKY14151-350LF Electrical Specifications (3 of 3) (Note 1)**
**($V_{DD}$ = 2.65 V, CTRL1 = CTRL2 = $V_{DD}$, $T_{OP}$ = +25 °C, All Unused RF Ports are Terminated in a 50 Ω Load, Unless Otherwise Noted)**

| Parameter | Symbol | Test Condition | Min | Typical | Max | Units |
|---|---|---|---|---|---|---|
| *RF Specifications (continued)* | | | | | | |
| Triple beat ratio (2:1 VSWR) | 3BR | f1 = 1880 MHz, P1 = +21.5 dBm, f2 = 1881 MHz, P2 = +21.5 dBm, $f_{JAMMER}$ = 1960 MHz, $P_{JAMMER}$ = −30 dBm, measured @ 1959 MHz and 1961 MHz for worst case over phase, 2:1 VSWR load | +81 | +82 | | dBc |
| | | f1 = 891.025 MHz, P1 = +14 dBm, f2 = 892.025 MHz, P2 = +14 dBm, $f_{JAMMER}$ = 836.0125 MHz, $P_{JAMMER}$ = −30 dBm, measured @ 835.0125 MHz and 837.0125 MHz for worst case over phase, 2:1 VSWR load | +81 | +82 | | dBc |
| | | f1 = 825.995 MHz, P1 = +21.5 dBm, f2 = 826.995 MHz, P2 = +21.5 dBm, $f_{JAMMER}$ = 870.995 MHz, $P_{JAMMER}$ = −30 dBm, measured @ 869.995 MHz and 871.995 MHz for worst case over phase, 2:1 VSWR load | +81 | +82 | | dBc |
| *DC/Control Specifications* | | | | | | |
| Switched supply voltage (Note 2) | $V_{DD}$ | | 1.8 | 2.0 | 3.00 | V |
| Switched supply current | $I_{DD}$ | | | 0.3 | | mA |
| Control voltage: High Low | CTRL1, CTRL2 | | 1.80 (Note 3) | 2.65 0 | 3.00 0.3 | V V |
| Control current: High Low | $I_{CTL}$ | | | 0.5 0.5 | | μA μA |

**Note 1**: Performance is guaranteed only under the conditions listed in this Table.

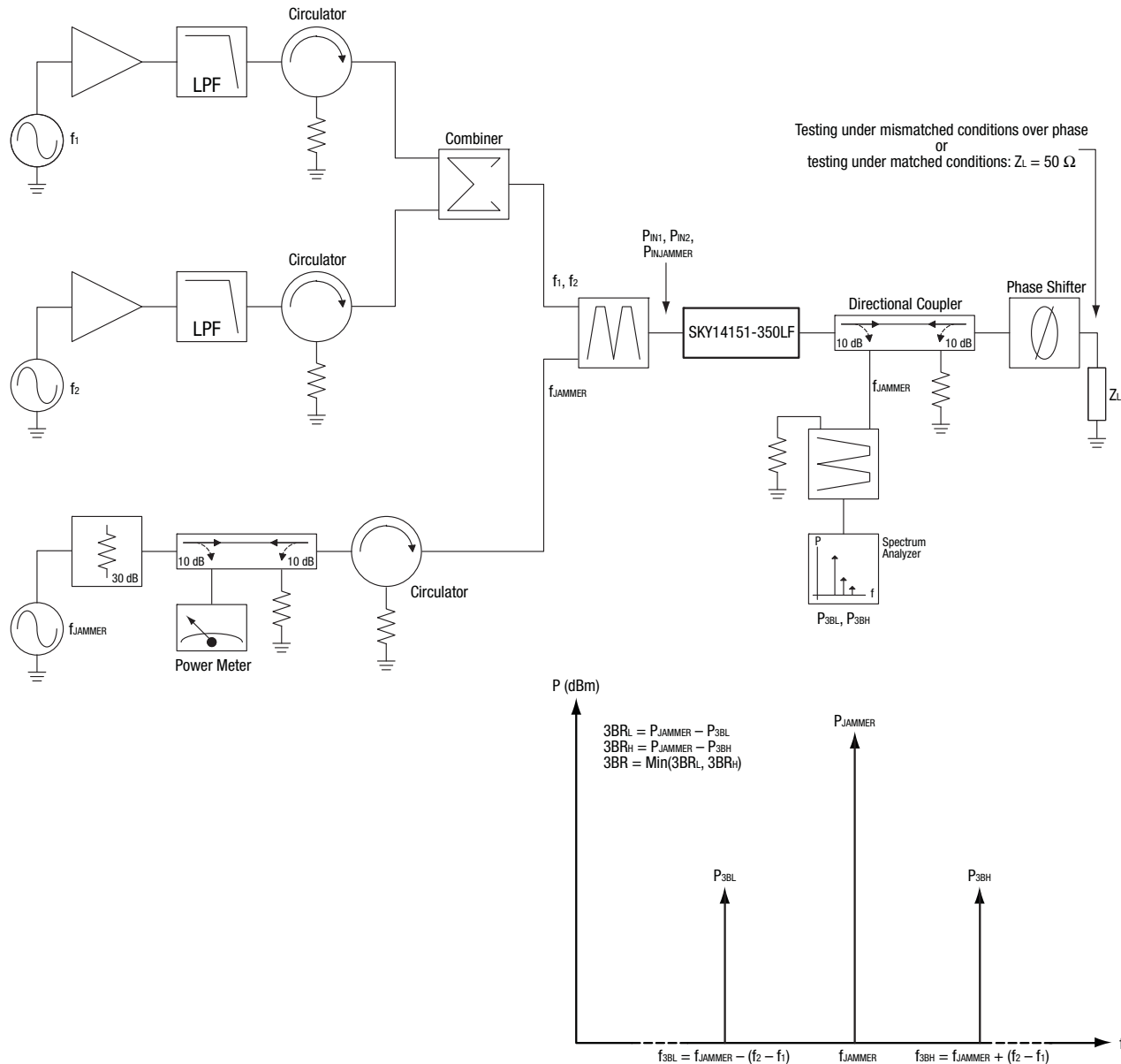**Note 2:** For best receiver sensitivity, $V_{DD}$ is recommended to be between 1.8 and 2.0 V with CTRL1/2 (high) no larger than $V_{DD}$.

**Note 3**: For 1.8 V control, $V_{DD}$ must be ≤ 2.65 V.

**Table 4. SKY14151-350LF Truth Table**

| State | CTRL1 (Pin 4) | CTRL2 (Pin 3) | RF Path |
|-------|---------------|---------------|---------|
| 1 | V$_{LOW}$ | V$_{LOW}$ | ANT to RF1 |
| 2 | V$_{LOW}$ | V$_{HIGH}$ | ANT to RF2 |
| 3 | V$_{HIGH}$ | V$_{LOW}$ | ANT to RF3 |
| 4 | V$_{HIGH}$ | V$_{HIGH}$ | ANT to RF4 |

**Note**: V$_{HIGH}$ = 1.8 V to V$_{DD}$
V$_{LOW}$ = 0 to 0.3 V
Any state other than described in this Table places the switch into an undefined state.

## Typical Performance Characteristics

**(V$_{DD}$ = 2.5 to 3.0 V, CTRL1 = CTRL2 = V$_{DD}$, T$_{OP}$ = +25 °C, All Unused RF Ports are Terminated in a 50 Ω Load, Unless Otherwise Noted)**
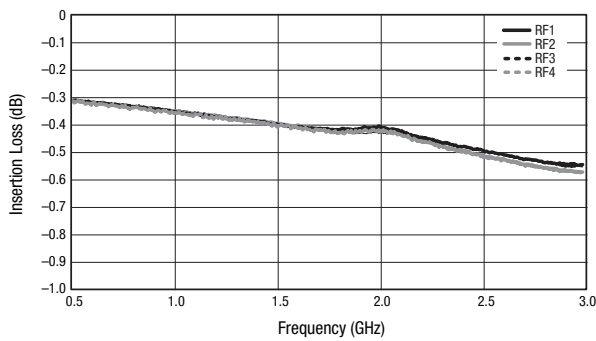


**Figure 3. Insertion Loss vs Frequency**
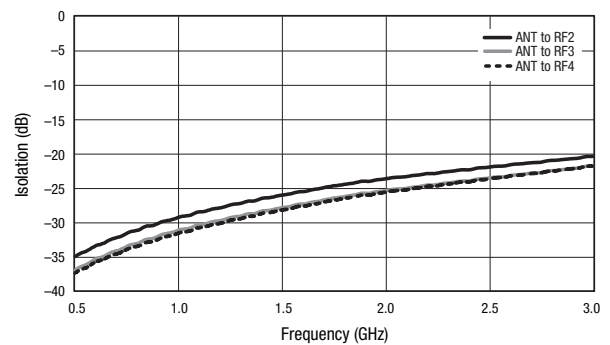


**Figure 4. Isolation vs Frequency
(ANT to RF1 Insertion Loss State)**
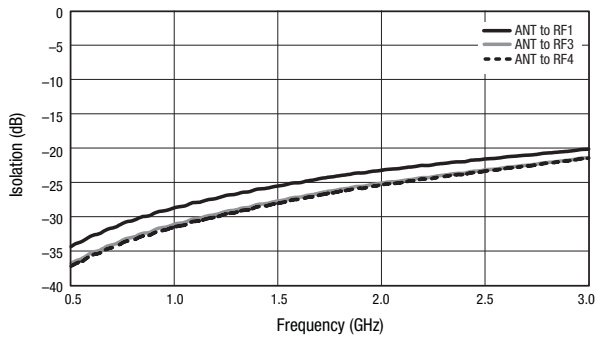


**Figure 5. Isolation vs Frequency
(ANT to RF2 Insertion Loss State)**
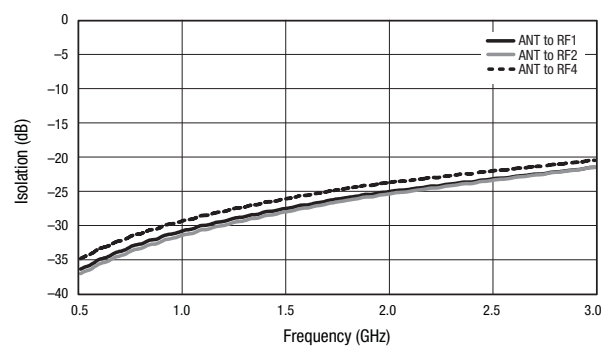


**Figure 6. Isolation vs Frequency
(ANT to RF3 Insertion Loss State)**

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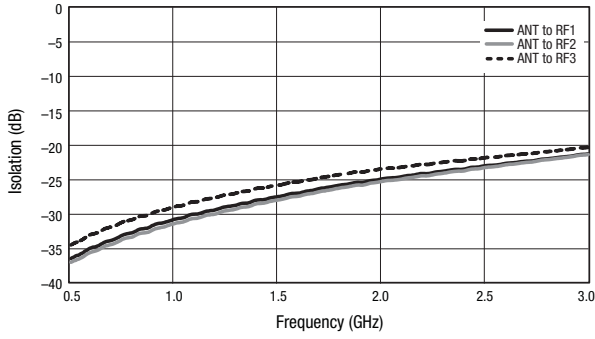
Figure 7. Isolation vs Frequency
(ANT to RF4 Insertion Loss State)
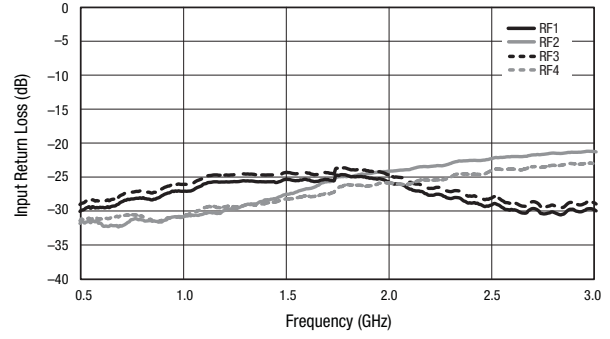


Figure 8. Input Return Loss vs Frequency
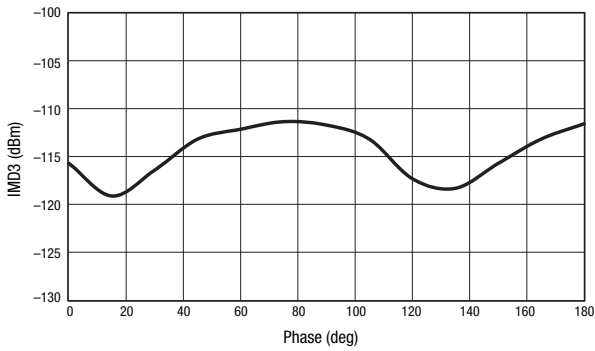


Figure 9. 3rd Order Intermodulation Distortion vs Phase
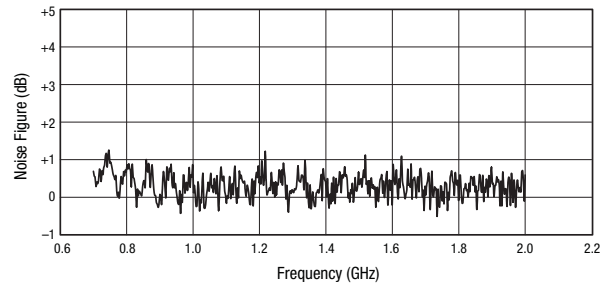


Note: For best receiver sensitivity, VDD is recommended to be between 1.8 and 2.0 V with CTRL1/2 (high) no larger than VDD.

Figure 10. Typical Noise Figure vs Frequency
(V$_{DD}$ = 2 V, CTRL1/2 = 1.8 V)
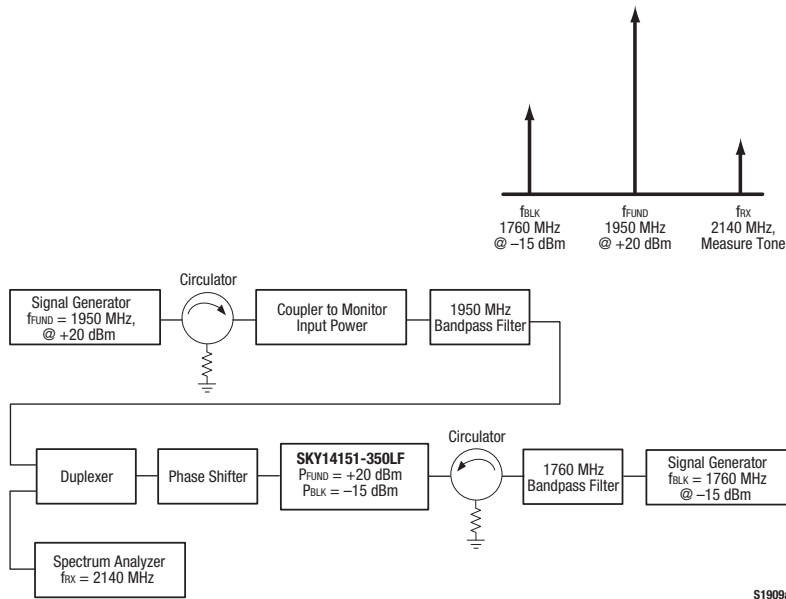


Figure 11. 3rd Order Intermodulation Test Setup

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**Figure 12. Triple Beat Ratio (3BR) Test Setup**

## Evaluation Board Description

The SKY14151-350LF Evaluation Board is used to test the performance of the SKY14151-350LF SP4T Switch. An Evaluation Board schematic diagram is provided in Figure 13. An assembly drawing for the Evaluation Board is shown in Figure 14.

Components C3 and L1 constitute an ESD filter. This topology and the component values noted in Figure 13 may vary according to the ESD requirement and acceptable insertion loss for a specific application.

Decoupling capacitors (C3 through C6) are recommended to suppress noise and to prevent RF leakage into the DC control circuits.

DC blocking capacitors C1, C2, C7, C8, and C9 determine the low frequency operation of the switch. Increase the capacitor values to lower operation frequency.

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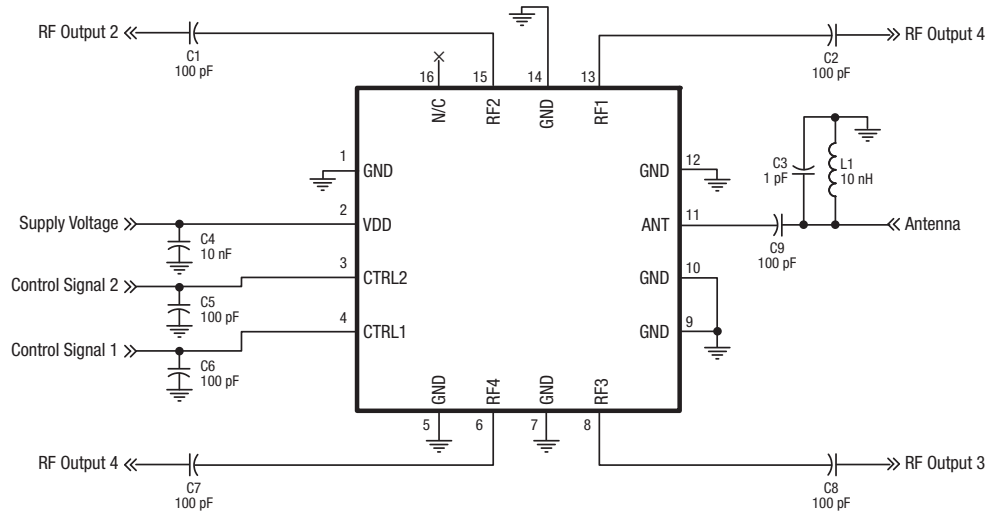
Note: Some component labels may be different than the
corresponding component symbol shown here.
Component values, however, are accurate as of the
date of this Data Sheet.

Use 10 nF DC blocking capacitors (C1, C2, C7, C8,
and C9) for <50 MHz operation.

S1950

**Figure 13. SKY14151-350LF Evaluation Board Schematic**
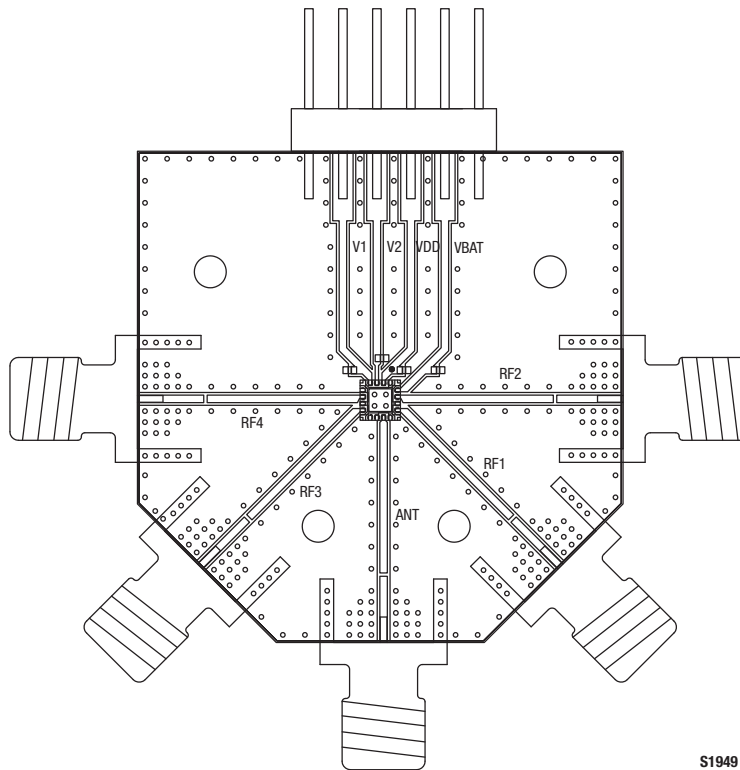


S1949

**Figure 14. SKY14151-350LF Evaluation Board Assembly Diagram**

## Package Dimensions

The PCB layout footprint for the SKY14151-350LF is provided in Figure 15. Typical case markings are shown in Figure 16. Package dimensions for the 16-pin QFN are shown in Figure 17, and tape and reel dimensions are provided in Figure 18.

## Package and Handling Information

Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur

when the part is subjected to high temperature during solder assembly.

THE SKY14151-350LF is rated to Moisture Sensitivity Level 1 (MSL1) at 260 °C. It can be used for lead or lead-free soldering.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.

16X 1.10

0.225 X 45⁰

1.70

12X 0.50 Pitch

0.25

4x Plated Through Vias

See Detail A

Metal Ground Pad 1.70

0.25

0.60

Exposed Soldering Area

Detail A
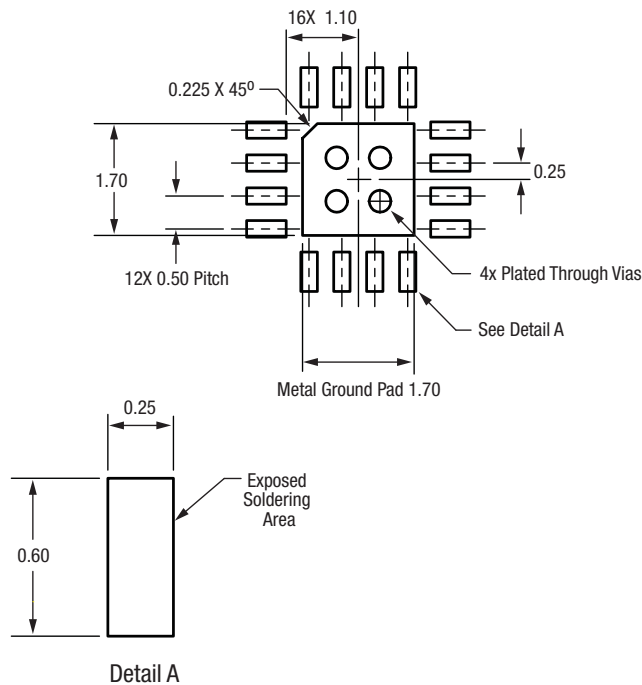
*All dimensions are in millimeters*

**S1691**

**Figure 15. SKY14151-350LF PCB Layout Footprint
(Top View)**

Pin 1
Indicator

Skyworks Part #

Lot #

Date Code:
(Y = Calendar Year
WW = Week)

K 1 5 1
X X X X
Y W W
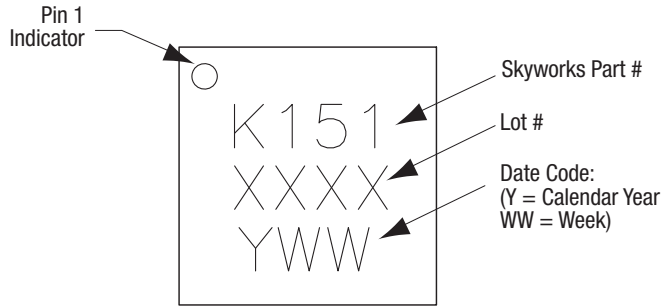
**Figure 16. Typical Case Markings
(Top View)**

3.00

-A-

-B-

Pin 1
Indicator

3.00

2X ⌓ 0.15 C

2X ⌓ 0.15 C

Top View

-C-

(0.20)
Seating Plane

0.02 + 0.03/-0.02

16X /2\
⌓ 0.08 C

0.75 ± 0.05

// 0.10 C

Side View

1.70 +0.10/-0.15

-A-

0.85

Exposed Pad

0.225 x 45°
Pin 1 Indicator

-B-

0.85

1.70 +0.10/-0.15

Detail B

Detail A

Bottom View

0.40 ± 0.10

0.25 +0.05/-0.07

⊕ 0.10 Ⓜ C A B
0.05 Ⓜ C

Detail A
Scale: 20X
16 Places

0.25

0.5

Detail B
Scale: 10X
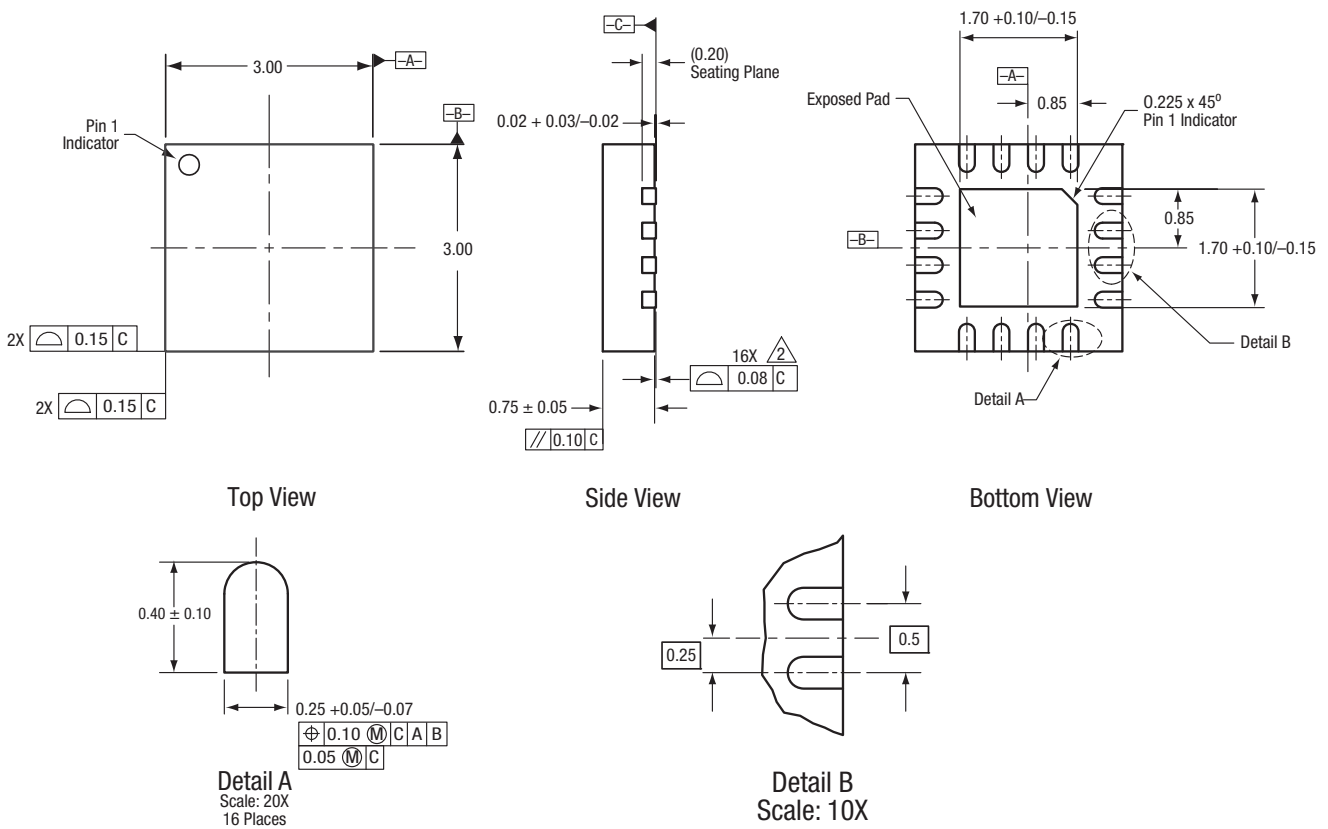
*All measurements are in millimeters.
Dimensioning and tolerancing according to ASME Y14.5M-1994.
Coplanarity applies to the exposed heat sink slug as well as the terminals..
Plating requirement per source control drawing (SCD) 2504.*

**S1692**

**Figure 17. SKY14151-350LF 16-Pin QFN Package Dimensions**
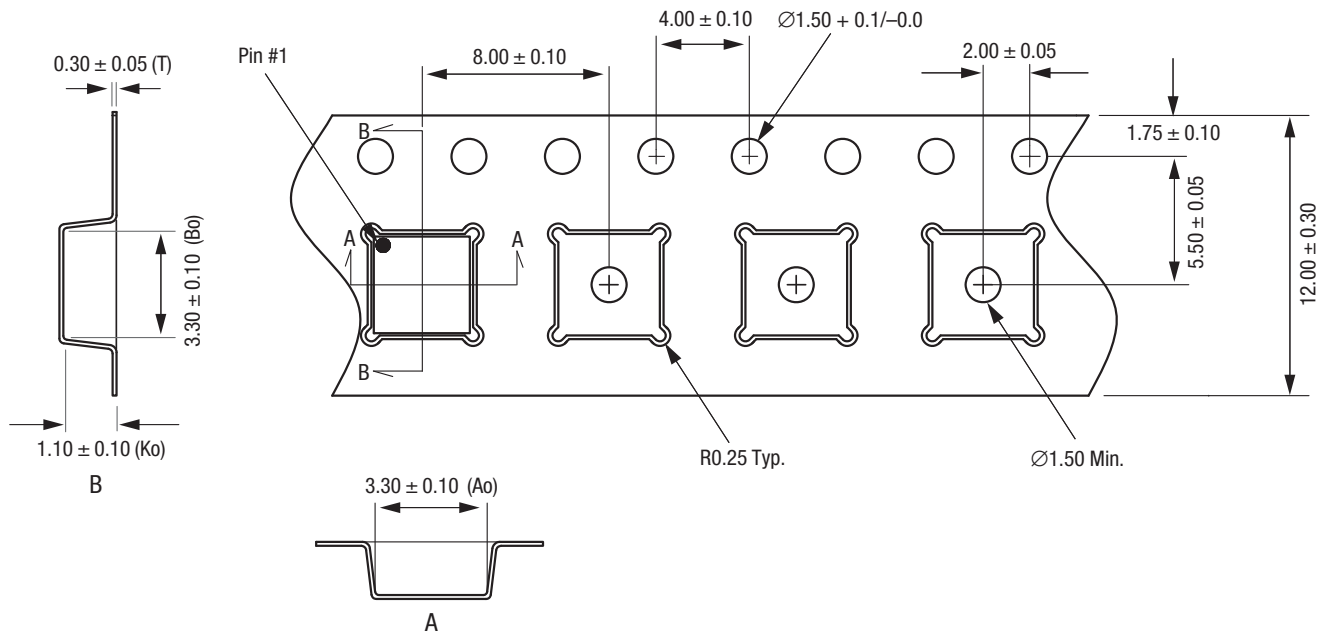
Notes:
1. Carrier tape: black conductive polystyrene, non-bakeable material.
2. Cover tape material: transparent conductive HSA.
3. Cover tape size: 9.20 mm width.
4. All measurements are in millimeters.

S1698

**Figure 18. SKY14151-350LF Tape and Reel Dimensions**

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## Ordering Information

| Model Name | Manufacturing Part Number | Evaluation Board Part Number |
|---|---|---|
| SKY14151-350LF SP4T Switch | SKY14151-350LF | SKY14151-350LF-EVB |