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TVS Diode Arrays (SPA® Diodes)

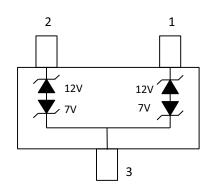
General Purpose ESD Protection - SM712



SM712 Series 600W Asymmetrical TVS Diode Array 🚘 AUTOMOTIVE GRADE ROHS 😥 GREEN



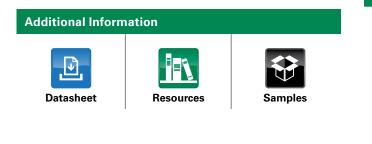
Pinout and Functional Block Diagram



Life Support Note:

Not Intended for Use in Life Support or Life Saving Applications

The products shown herein are not designed for use in life sustaining or life saving applications unless otherwise expressly indicated.



Description

The SM712 TVS Diode Array is designed to protect RS-485 applications with asymmetrical working voltages (-7V to 12V) from damage due to electrostatic discharge (ESD), electrical fast transients (EFT), and lightning induced surges.

The SM712 can absorb repetitive ESD strikes above the maximum level specified in the IEC 61000-4-2 international standard without performance degradation and safely dissipate up to 19A of 8/20us induced surge current (IEC-61000-4-5 2nd edition) with very low clamping voltages.

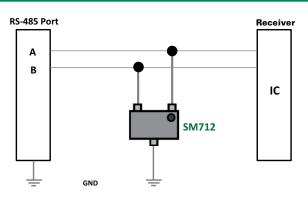
Features

- RoHS compliant and lead-free
- ESD, IEC 61000-4-2, ±30kV contact, ±30kV air
- EFT, IEC 61000-4-4, 50A (5/50ns)
- Lightning, IEC 61000-4-5 2nd edition, 19A (t_p=8/20µs)
- Working Voltages: -7V to 12V
- Low clamping voltage
- Low leakage current
- AEC-Q101 Qualified

Applications

- RS-485
- Fieldbus
- Modbus
- Profibus
- DMX512
- Security SystemsAutomatedTeller Machines
- (ATMs)
- Lighting Control DALI
- Communication
 Equipments

RS-485 Application Example





TVS Diode Arrays (SPA[®] Diodes) General Purpose ESD Protection - SM712

Absolute Maximum Ratings

			11.5
Symbol	Parameter	Value	Units
P _{Pk}	Peak Pulse Power (t _p =8/20µs)	600	W
I _{PP}	Peak Pulse Current (t _p =8/20µs)	19	А
T _{op}	Operating Temperature	-40 to 125	C°
T _{STOR}	Storage Temperature	-55 to 150	°C

Notes:

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Thermal Information

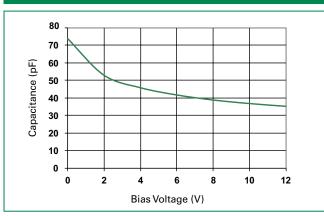
Parameter	Rating	Units
Storage Temperature Range	-55 to 150	°C
Maximum Junction Temperature	150	°C
Maximum Lead Temperature (Soldering 20-40s)	260	°C

SM712 Electrical Characteristics (T_{op}=25°C)

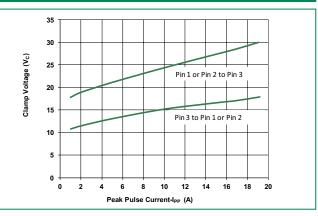
Parameter	Symbol	Test Conditions	Min	Тур	Max	Units
Reverse Standoff Voltage	V _{RWM}	I _R ≤1μA, Pin3 to Pin1 or Pin2			7.0	V
		I _R ≤1μA, Pin1 or Pin2 to Pin3			12.0	V
Reverse Breakdown	V _R	I _R =1mA, Pin3 to Pin1 or Pin2	7.5			V
Voltage		I _R =1mA, Pin1 or Pin2 to Pin3	13.3			V
Leakage Current	I _{LEAK}	V _R =7V			20	μΑ
		V _R =12V			1	μA
Clamp Voltage ¹	V _c	I_{pp} =1A, t_p =8/20µs, Pin 1 or Pin 2 to Pin 3			19	V
		I_{pp} =1A, t_p =8/20µs, Pin 3 to Pin 1 or Pin 2			11	V
		I_{pp} =19A, t_p =8/20µs, Pin 1 or Pin 2 to Pin 3			31	V
		I_{pp} =19A, t_p =8/20µs, Pin 3 to Pin 1 or Pin 2			19	V
Dynamic Resistance ¹	R _{dyn}	$(V_{C2} - V_{C1}) / (I_{PP2} - I_{PP1})$		0.5		Ω
ESD Withstand Voltage ¹	V _{ESD}	IEC 61000-4-2 (Contact Discharge)	±30			kV
		IEC 61000-4-2 (Air Discharge)	±30			kV
		Reverse Bias=0V, f=1MHz; Pin 1 or Pin2 to Pin 3			75	pF

Notes : 1. Parameter is guaranteed by design and/or device characterization.

Capacitance vs. Reverse Bias



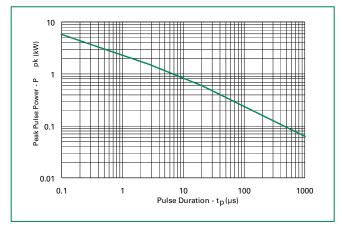
Clamping Voltage vs. I_{PP}



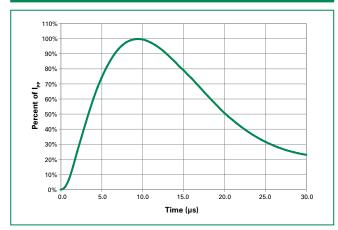
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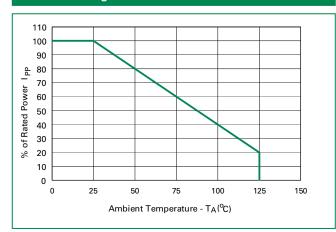
Non-Repetitive Peak Pulse Power vs. Pulse Time



Pulse Waveform



Power Derating Curve



Product Characteristics

Lead Plating	PPF	
Lead Material	Copper Alloy	
Lead Coplanarity	0.0004 inches (0.102mm)	
Substrate material	Silicon	
Body Material	Molded Epoxy	
Flammability	UL 94 V-0	

Notes

1. All dimensions are in millimeters

Dimensions include solder plating.
 Dimensions are exclusive of mold flash & metal burr.

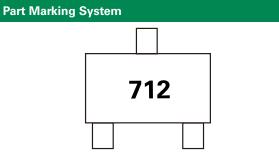
Blo is facing up for mold and facing down for trim/form, i.e. reverse trim/form.
 Package surface matte finish VDI 11-13.

t_P Temperature \mathbf{T}_{P} Critical Zone T∟ to T₽ Ramp-up \mathbf{T}_{L} T_{S(max)} Ramp-down Preheat T_{S(min)} 25 time to peak temperature Time ⊏

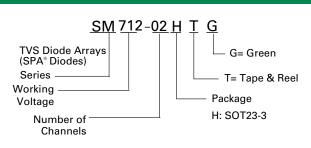
Soldering Parameters

Reflow Condition		Pb – Free assembly	
	-Temperature Min (T _{s(min)})	150°C	
Pre Heat	-Temperature Max (T _{s(max)})	200°C	
	-Time (min to max) (t _s)	60 – 180 secs	
Average ramp up rate (Liquidus) Temp (T_L) to peak		3°C/second max	
T _{S(max)} to T _L - Ramp-up Rate		3°C/second max	
Reflow	-Temperature (T _L) (Liquidus)	217°C	
	-Temperature (t _L)	60 – 150 seconds	
Peak Temperature (T _P)		260 ^{+0/-5} °C	
Time within 5°C of actual peak Temperature (t _p)		20 – 40 seconds	
Ramp-down Rate		6°C/second max	
Time 25°C to peak Temperature (T _P)		8 minutes Max.	
Do not exceed		260°C	





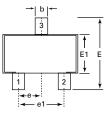
Part Numbering System



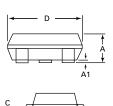
Ordering Information

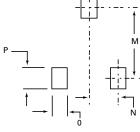
Part Number	Package	Marking	Min. Order Qty.
SM712-02HTG	SOT23-3	712	3000

Package Dimensions – SOT23-3



Recommended Pad Layout





Package	SOT23-3				
Pins	3				
JEDEC	TO-236				
	Millimeters		Inches		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A1	0.01	0.1	0.0004	0.004	
b	0.3	0.5	0.012	0.020	
C	0.08	0.2	0.003	0.008	
D	2.8	3.04	0.110	0.120	
E	2.1	2.64	0.083	0.104	
E1	1.2	1.4	0.047	0.055	
е	0.95 BSC		0.038 BSC		
e1	1.90 BSC		0.075 BSC		
L1	0.54 REF		0.021 REF		
М		2.29		.090	
N		0.95		0.038	
0		0.78		.030TYP	
P		0.78		.030TYP	