



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

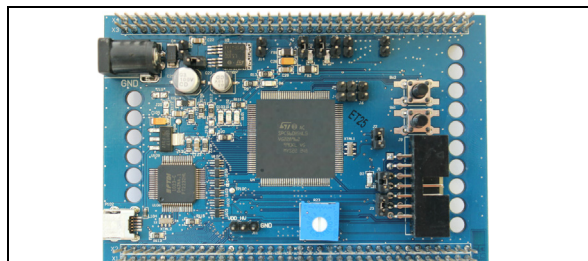
Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



SPC560B-DIS Discovery evaluation board

Data brief - production data



Features

- SPC560B54L5 32-bit 48 MHz e200z0h CPU, 32-bit Power Architecture® Technology CPU, 768 kB Code Flash in an LQFP144 package.
- On-board USB-JTAG PLS debugger and HW selection mode to use stand-alone JTAG debuggers.
- Board power supply: through external +12 V PSU or from the USB bus (5 V supply voltage).
- Seven LEDs: D6 for 5 V power on, D2 for Reset, D101, D102, D103 for PLS debugger section, D7 and D8 for PC3 and PC2 outputs
- Two push buttons (reset and user)
- A potentiometers for ADC input (user)
- Two extension headers (2 x 36 pin - 100 mil) for all LQFP144 pins and for quick connection to prototyping boards, additional modules and easy probing.
- JTAG interface (2 x 7 male 100mil connector)
- USB port (mini -B)
- 16 MHz crystal
- Specification:
 - Board size 100 x 55 mm
 - MCU: SPC560B54L5 - LQFP144
 - 2 extension headers 2 x 36 pin - 100 mil
 - DC connector (+12 V input)
 - Mini USB - B
 - JTAG 2 x 7 - 100 mil connector

Description

The SPC560B-DIS Discovery is a low cost and quick way to discover SPC56 B line.

Based on SPC560B54L5, the discovery board includes a JTAG debugger from PLS, LEDs, push buttons and extension headers to connect prototyping boards or additional modules.

PLS UDE software is available for free download and includes a perpetual 256 kByte debugging/programming license.

Free ready-to-run application firmware examples are available on SPC5Studio to support quick evaluation and development.

SPC5Studio includes visual configurable code generation engine, board support package (BSP), startup routines, interrupt services, free RTOS (optional) and a full set of low level drivers. SPC5Studio includes HighTec GNU "C" compiler, with a 30-days full free trial support. SPC5Studio is available for free download.

The SPC56 B line is designed to address the specific need of car body applications but as well many other markets where high temperature operation, high reliability, low power stand-by operation with quick wake up and real-time loads diagnostic are required.

An E2E Community is available on ST WEB to get ST experts support in getting started quickly with SPC56 microcontrollers.

Table 1. Device summary

| Order code | Reference |
|-------------|-----------------------------------|
| SPC560B-DIS | SPC56B-Discovery with SPC560B54L5 |

1 System requirements, HW and SW resources

1.1 System requirements

- Windows PC (XP, Vista, 7 or 8)
- USB type A to mini-B cable

1.2 Development toolchain

- SPC5Studio (includes Hightec GNU "C" compiler, with a 30-days full free trial support)

1.3 Demonstration software

Demonstration software is preloaded in the MCU flash memory for easy demonstration of the SPC560B54L5 in stand-alone mode. For more information and to download the latest version available, please refer to ST web.

2 Revision history

Table 2. Revision history

| Date | Revision | Changes |
|-------------|----------|--|
| 04-Oct-2013 | 1 | Initial release. |
| 19-Mar-2014 | 2 | Updated Description. |
| 14-Jul-2015 | 3 | Updated value of Code Flash from 256 to 768 kB in Features . Updated value from 128 kByte to 256 for debugging/programming PLS UDE software in Description . Updated Section 1: System requirements, HW and SW resources . |

IMPORTANT NOTICE – PLEASE READ CAREFULLY

STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice. Purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST's terms and conditions of sale in place at the time of order acknowledgement.

Purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of Purchasers' products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

ST and the ST logo are trademarks of ST. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2015 STMicroelectronics – All rights reserved