

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









Digital SiSonicTM Microphone



The SPM0437HD4H is a miniature, high-performance, low power, top port silicon digital microphone with a single bit PDM output. Using Knowles' proven high performance SiSonicTM MEMS technology, the SPM0437HD4H consists of an acoustic sensor, a low noise input buffer, and a sigma-delta modulator. These devices are suitable for applications such as cellphones, smart phones, laptop computers, sensors, digital still cameras, portable music recorders, and other portable electronic devices where excellent wideband audio performance and RF immunity are required.

Features:

- Low Current
- PDM Output
- Supports Dual Multiplexed Channels
- Ultra-Stable Performance
- Standard SMD Reflow
- Omnidirectional



1. ABSOLUTE MAXIMUM RATINGS

Parameter	Absolute Maximum Rating	Units
V _{DD} , DATA to Ground	-0.3, +5.0	V
CLOCK to Ground	-0.3, +5.0	V
SELECT to Ground	-0.3, +5.0	V
Input Current	±5	mA
Short Circuit Current to/from DATA	Indefinite to Ground or V_{DD}	sec
Temperature Range	-40 to +100	°C

Stresses exceeding these "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only. Functional operation at these or any other conditions beyond those indicated under "Acoustic & Electrical Specifications" is not implied. Exposure beyond those indicated under "Acoustic & Electrical Specifications" for extended periods may affect device reliability.

2. ACOUSTIC & ELECTRICAL SPECIFICATIONS

TEST CONDITIONS: 23 $\pm 2^{\circ}$ C, 55 $\pm 20\%$ R.H., V_{DD} =1.8 V, F_{CLOCK} =2.4 MHz, SELECT pin is grounded, no load, unless otherwise indicated

amess otherwise massaces						
Parameter	Symbol	Conditions	Min	Тур	Max	Units
Supply Voltage ¹	V_{DD}		1.6	-	3.6	V
Supply Current ^{1,2,3}	I _{DD}		-	380	500	μΑ
Sleep Current ³	I _{SLEEP}	F _{CLOCK} < 1 kHz	-	6	10	μΑ
Sensitivity ¹	S	94 dB SPL @ 1 kHz	-29	-26	-23	dBFS
Signal to Noise Ratio	SNR	94 dB SPL @ 1 kHz, A-weighted		57.5	-	dB(A)
Total Harmonic Distortion	THD	94 dB SPL @ 1 kHz, S = Typ	-	.5		%
Acoustic Overload Point	AOP	10% THD @ 1 kHz, S = Typ	-	119	-	dB SPL
Power Supply Rejection Ratio	PSRR	200 mVpp sinewave @ 1 kHz	-	51	1	dBV /FS
Power Supply Rejection	PSR	100 mVpp square wave @ 217 Hz, A-weighted	-	-80	-	dBFS (A)
DC Output		Fullscale = ±100	-	-6	-	% FS
Directivity			Omnidirectional			
Polarity		Increasing sound pressure	Decreasing density of 1's			
Data Format			½ Cycle PDM			
Logic Input High	V _{IH}		0.65xV _{DD}	-	3.6	V



Parameter	Symbol	Conditions	Min	Тур	Max	Units
Logic Input Low	V_{IL}		-0.3	1	$0.35xV_{DD}$	V
Logic Output High	V_{OH}	I _{OUT} = 2 mA	$0.7xV_{DD}$	1	V_{DD}	V
Logic Output Low	V_{OL}	I _{OUT} = 2 mA	0	-	$0.3xV_{DD}$	V
SELECT (high)			V _{DD} -0.2	-	3.6	٧
SELECT (low)			-0.3	-	0.2	V
Short Circuit Current	I _{sc}	Grounded DATA pin	2	-	10	mA
Output Load	C _{LOAD}		-	-	160	pF
Clock Frequency	F _{CLOCK}		1.0	1	3.25	MHz
Clock Duty Cycle			40	-	60	%
Clock Rise/Fall Time	t _{EDGE}		-	-	10	ns
Fall-asleep Time ^{4,5}		F _{CLOCK} < 1 kHz	-	-	10	ms
Wake-up Time ^{4,6}		F _{CLOCK} ≥ 1 MHz	-	-	20	ms
Delay Time for Valid		No load for min t_{DV}	18		125	
Data	t_{DV}	Max C _{LOAD} for max t _{DV}	10		125	ns
Delay Time for High Z	t _{DZ}		0	-	16	ns

¹ 100% tested.

 $^{^2}$ I_{DD} varies with C_{LOAD} according to: Δ I_{DD} = 0.5*V_{DD}* Δ C_{LOAD}*F_{CLOCK}.

³ Maximum specifications are measured at maximum V_{DD}. Typical specifications are measured at standard test conditions.

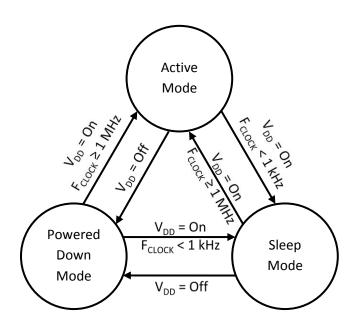
⁴ Valid microphones states are: Powered Down Mode (mic off), Sleep Mode (low current, DATA = high-Z, fast startup), and Active Mode (normal operation).

 $^{^{5}}$ Time from F_{CLOCK} < 1 kHz to I_{SLEEP} specification is met when transitioning from Active Mode to Sleep Mode.

⁶ Time from F_{CLOCK} ≥ 1 MHz to all applicable specifications are met when transitioning from Sleep Mode to Active Mode.



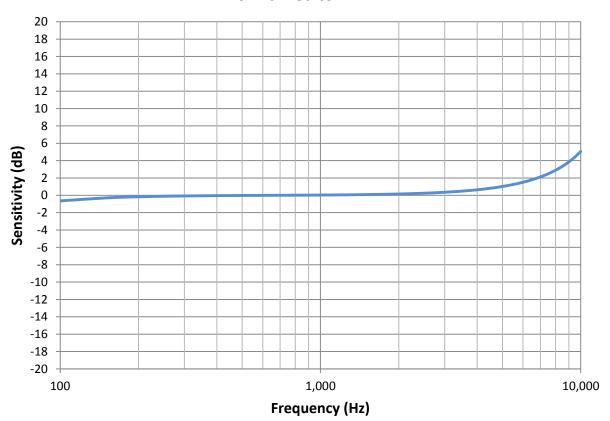
3. MICROPHONE STATE DIAGRAM





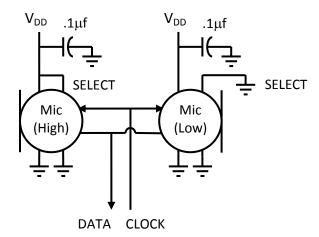
4. FREQUENCY RESPONSE CURVE

Typical Free Field Response Normalized to 1 kHz





5. INTERFACE CIRCUIT



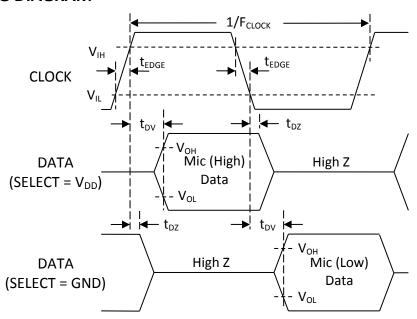
Microphone	SELECT	Asserts DATA On	Latch DATA On
Mic (High)	V_{DD}	Rising Clock Edge	Falling Clock Edge
Mic (Low)	GND	Falling Clock Edge	Rising Clock Edge

Note: All Ground pins must be connected to ground.

Bypass Capacitors near each mic on Vdd are recommened to provide maximum SNR performance. Capacitors near the microphone should not contain Class 2 dielectrics.

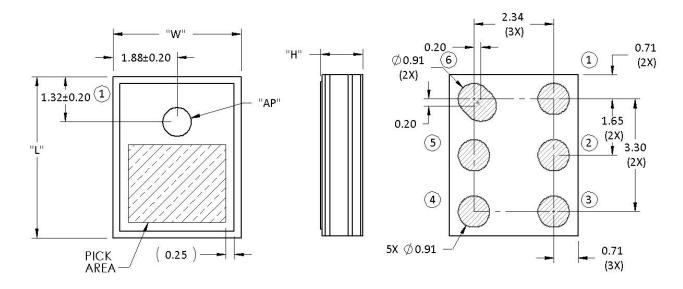
Detailed information on acoustic, mechanical, and system integration can be found in the latest $SiSonic^{TM}$ Design Guide application note.

6. TIMING DIAGRAM





7. MECHANICAL SPECIFICATIONS



Item	Dimension	Tolerance
Length (L)	4.72	±0.10
Width (W)	3.76	±0.10
Height (H)	1.25	±0.10
Acoustic Port (AP)	Ø0.84	±0.08

Pin#	Pin Name	Туре	Description
1	GROUND	Power	Ground
2	SELECT	Non-Digital Input	Lo/Hi (L/R) Select This pin is internally pulled low.
3	GROUND	Power	Ground
4	CLOCK	Digital I	Clock Input
5	DATA	Digital O	PDM Output
6	V_{DD}	Power	Power Supply

Notes: Pick Area only extends to 0.25 mm of any edge or hole unless otherwise specified.

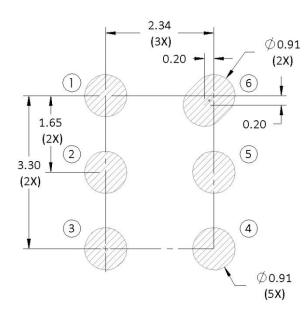


 $\label{lem:decomposition} \mbox{Dimensions are in millimeters unless otherwise specified.}$

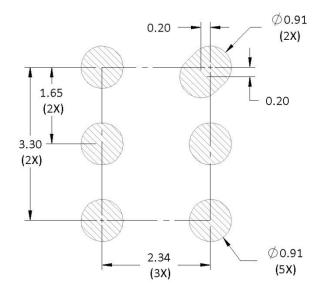
Tolerance is ±0.15mm unless otherwise specified



8. EXAMPLE LAND PATTERN



9. EXAMPLE SOLDER STENCIL PATTERN

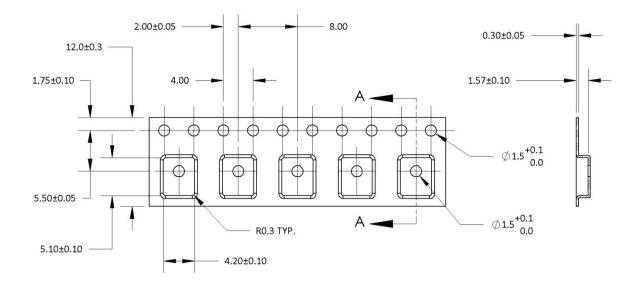


Notes: Dimensions are in millimeters unless otherwise specified.

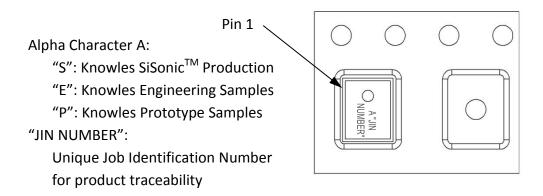
Further optimizations based on application should be performed.



10. PACKAGING & MARKING DETAIL



Model Number	Suffix	Reel Diameter	Quantity Per Reel
SPM0437HD4H-B	-6	13"	4,800



Notes: Dimensions are in millimeters unless otherwise specified.

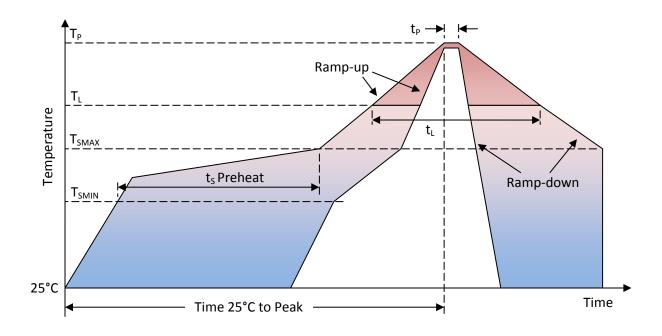
Vacuum pickup only in the pick area indicated in Mechanical Specifications.

Tape & reel per EIA-481.

Labels applied directly to reel and external package.



11. RECOMMENDED REFLOW PROFILE



Profile Feature	Pb-Free
Average Ramp-up rate (T _{SMAX} to T _P)	3°C/second max.
Preheat	
Temperature Min (T _{SMIN})	150°C
 Temperature Max (T_{SMAX}) 	200°C
 Time (T_{SMIN} to T_{SMAX}) (t_S) 	60-180 seconds
Time maintained above:	
Temperature (T _L)	217°C
Time (t _L)	60-150 seconds
Peak Temperature (T _P)	260°C
Time within 5°C of actual Peak Temperature (t _P)	20-40 seconds
Ramp-down rate (T _P to T _{SMAX})	6°C/second max
Time 25°C to Peak Temperature	8 minutes max

Notes: Based on IPC/JDEC J-STD-020 Revision C.

All temperatures refer to topside of the package, measured on the package body surface.



12. ADDITIONAL NOTES

- (A) Shelf life: Twelve (12) months when devices are to be stored in factory supplied, unopened ESD moisture sensitive bag under maximum environmental conditions of 30°C, 70% R.H.
- (B) MSL (moisture sensitivity level) Class 1.
- (C) Maximum of 3 reflow cycles is recommended.
- (D) In order to minimize device damage:
 - Do not board wash or clean after the reflow process.
 - Do not brush board with or without solvents after the reflow process.
 - Do not directly expose to ultrasonic processing, welding, or cleaning.
 - Do not insert any object in port hole of device at any time.
 - Do not apply over 30 psi of air pressure into the port hole.
 - Do not pull a vacuum over port hole of the microphone.
 - Do not apply a vacuum when repacking into sealed bags at a rate faster than 0.5 atm/sec.

13. MATERIALS STATEMENT

Meets the requirements of the European RoHS directive, 2011/65/EC as amended.

Meets the requirements of the industry-standard IEC 61249-2-21:2003 for halogenated substances and Knowles Green Materials Standards Policy section on Halogen-Free.

Ozone depleting substances are not used in the product or the processes used to make the product including compounds listed in annex A, B, and C of the "Montreal Protocol on Substances that deplete the ozone Layer."



14. RELIABILITY SPECIFICATIONS

Test	Description
Thermal Shock	100 cycles air-to-air thermal shock from -40°C to +125°C with 15 minute soaks. (IEC 68-2-4)
High Temperature Storage	1,000 hours at +105°C environment (IEC 68-2-2 Test Ba)
Low Temperature Storage	1,000 hours at -40°C environment (IEC 68-2-2 Test Aa)
High Temperature Bias	1,000 hours at +105°C under bias (IEC 68-2-2 Test Ba)
Low Temperature Bias	1,000 hours at -40°C under bias (IEC 68-2-2 Test Aa)
Temperature / Humidity Bias	1,000 hours at +85°C/85% R.H. under bias. (JESD22-A101A-B)
Vibration	4 cycles of 20 to 2,000 Hz sinusoidal sweep with 20 G peak acceleration lasting 12 minutes in X, Y, and Z directions. (Mil-Std-883E, method 2007.2 A)
ESD-HBM	3 discharges of ±2 kV direct contact to I/O pins. (ESD STM5.2)
ESD-LID/GND	3 discharges of ±8 kV direct contact to lid while unit is grounded. (IEC 61000-4-2)
ESD-MM	3 discharges of ±2 kV direct contact to I/O pins. (MIL 883E, Method 3015.7)
Reflow	5 reflow cycles with peak temperature of +260°C
Mechanical Shock	3 pulses of 10,000 G in the X, Y, and Z direction (IEC 68-2-27, Test Ea)

Note: After reliability tests are performed, the sensitivity of the microphones shall not deviate more than 3 dB from its initial value.



15. SPECIFICATION REVISIONS

Revision	Specification Changes	Date
А	Initial Release (C10115114)	7/25/13

Information contained herein is subject to change without notice. It may be used by a party at their own discretion and risk. We do not guarantee any results or assume any liability in connection with its use. This publication is not to be taken as a license to operate under any existing patents.