Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!

Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832
Email & Skype: info@chipsmall.com Web: www.chipsmall.com
Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China

Middle Power LED Series
3030

LM301Z
CRI 70

Features & Benefits

- 0.3 W class middle power LED
- EMC resin for high reliability
- Standard form factor for design flexibility (3.0 × 3.0 mm)

Table of Contents

1. Characteristics

a) Absolute Maximum Rating

Item	Symbol	Rating	Unit	Condition
Ambient / Operating Temperature	T_a	-40 ~ +85	℃	-
Storage Temperature	T_{stg}	-40 ~ +100	℃	-
LED Junction Temperature	T_j	125	℃	-
Forward Current	I_F	400	mA	-
Pulse Forward Current	I_{Fp}	600	mA	Duty 1/10, pulse width 10 ms
Assembly Process Temperature	-	260 <10	℃ s	-
ESD (HBM)	-	5	kV	-

b) Electro-optical Characteristics (I_F = 65 mA, T_s = 25 ºC)

Item	Nominal CCT (K)	Rank	Bin	Min.	Typ.	Max.	Unit
Forward Voltage (V_F)		WA	AY	2.6	-	2.7	V
			AZ	2.7	-	2.8	
			A1	2.8	-	2.9	
Reverse Voltage (@ 5 mA)				0.7	-	1.2	V
Color Rendering Index (R_a)		3		70	-	-	-
Special CRI (R9)				-	-	-	-
Thermal Resistance (junction to solder point)				-	12	-	℃/W
Beam Angle				-	120	-	○

Note:

Samsung maintains measurement tolerance of: forward voltage = ±0.1 V, luminous flux = ±5 %, CRI = ±3

2. Product Code Information

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18
S	P	M	W	H	3	3	2	6	M	D	3	W	A	V	0	S	A

Digit	PKG Information	Code	Specification
1 2 3	Samsung Package Middle Power	SPM	
4 5	Color	WH	White
6	Product Version	3	Zener version
7 8 9	Form Factor	326	3.0 x 3.0 x 0.65 mm
10	Sorting Current	M	65 mA
11	Chromaticity Coordinates	D	MacAdam
12	CRI	3	Min. 70
13 14	Forward Voltage (V)	WA	2.6~2.9 Bin Code: AY 2.6~2.7; AZ 2.7~2.8; A1 2.8~2.9
15 16	CCT (K)	V☆	3000 Bin Code: VN, VP, VQ, VR, VS, VT, VU
		U☆	3500 Bin Code: UN, UP, UQ, UR, US, UT, UU
		T☆	4000 Bin Code: TN, TP, TQ, TR, TS, TT, TU
		R☆	5000 Bin Code: RN, RP, RQ, RR, RS, RT, RU
			☆ : "0" (Whole Bin) "3" (MacAdam 3- step) "Y" (Kitting)
17 18	Luminous Flux (lm)	SA	Bin Code: SA

a) Luminous Flux Bins (I_F = 65 mA, T_s = 25℃)

Nominal CCT (K)	CRI Min.	Product Code	Flux Bin	Flux Range (Φ_v, lm)
3000	70	SPMWH3326MD3WAV☆SA	SA	30.0 – 33.0
3500	70	SPMWH3326MD3WAU☆SA	SA	30.5 – 33.5
4000	70	SPMWH3326MD3WAT☆SA	SA	31.0 – 34.0
5000	70	SPMWH3326MD3WAR☆SA	SA	33.0 - 36.0

Note:

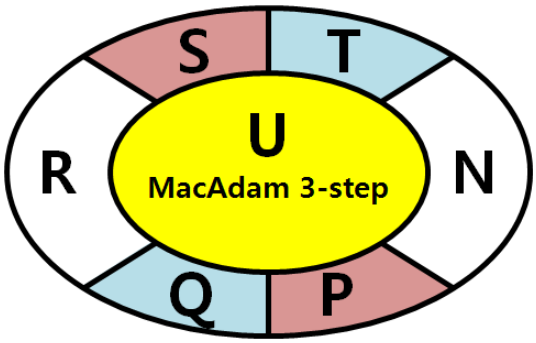
"☆" can be "0" (Whole Bin), “3” (MacAdam 3-step), "Y" (Kitting)

b) Kitting Rule

1) Y Kitting bin Concept

1. Under agreement between customer and SAMSUNG ELECTRONICS, SAMSUNG can supply kitting bin (VF, Color, lm).
2. A forward voltage (VF) of kitting bin is combined by a pair of same VF rank such as (A2+A2) or (A3+A3).
3. A Chromaticity Coordinates of kitting bin is mixed by kitting procedure.(below kitting simulation)

[Kitting example]



[Binning Information]

	Bin #1	Bin #2
VF	AY	AY
	AZ	AZ
	A1	A1
CIE	U	U
	N	R
	P	S
	Q	T

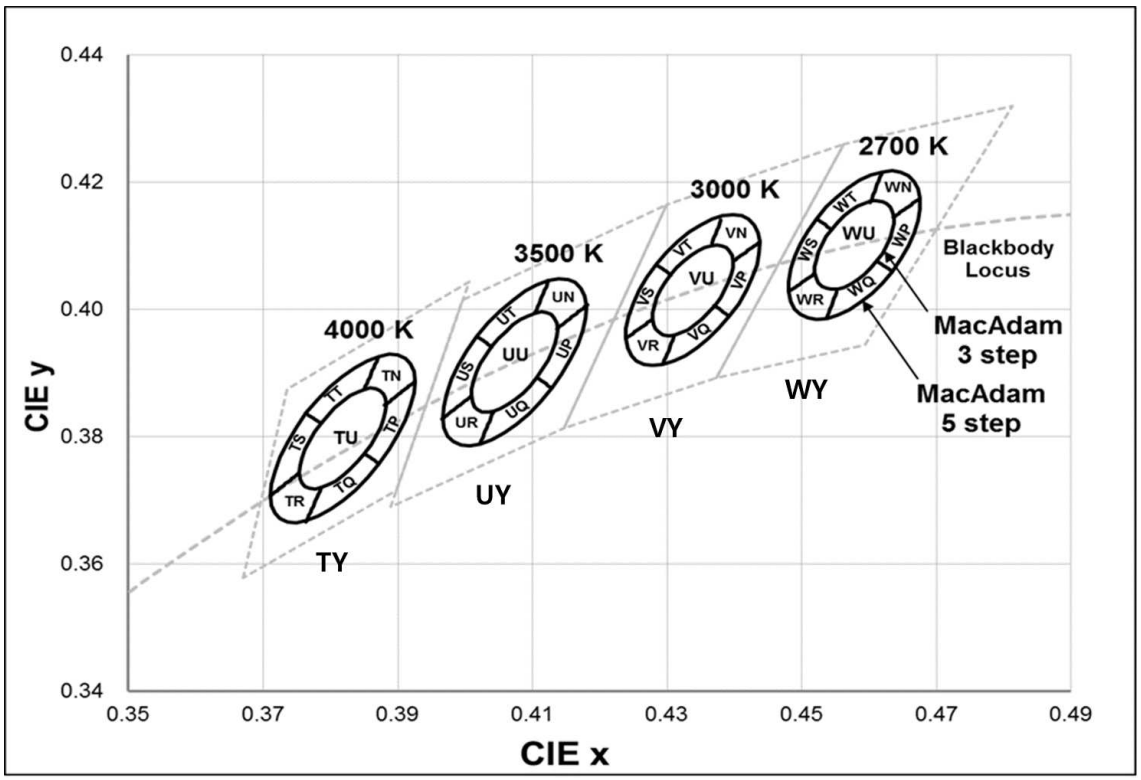
c) Color Bins (I_F = 65 mA, T_s = 25℃)

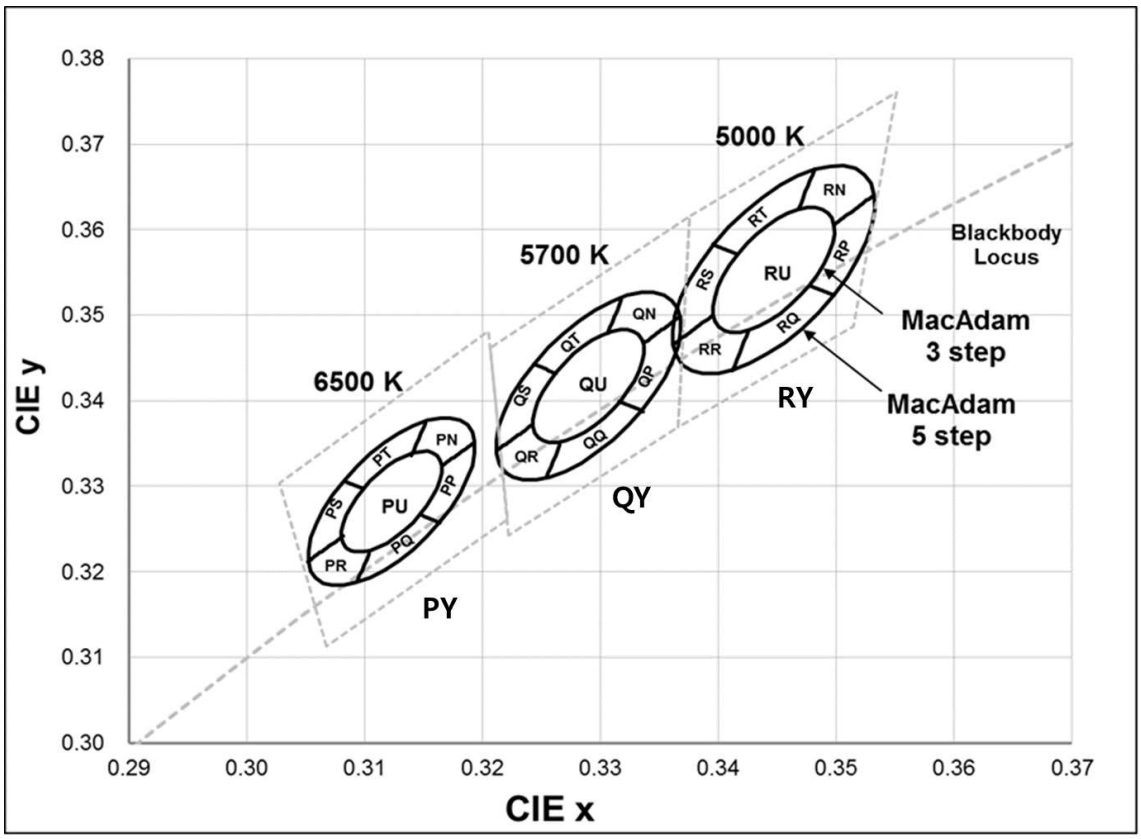
Nominal CCT (K)	CRI	Product Code	Color Rank	Chromaticity Bins
3000	70	SPMWH3326MD3WAV0SA	V0 (Whole Bin)	VN, VP, VQ, VR, VS, VT, VU
		SPMWH3326MD3WAV3SA	V3 (MacAdam 3-step)	VU
		SPMWH3326MD3WAVYSA	VY (Kitting)	VN, VP, VQ, VR, VS, VT, VU
3500	70	SPMWH3326MD3WAU0SA	U0 (Whole Bin)	UN, UP, UQ, UR, US, UT, UU
		SPMWH3326MD3WAU3SA	U3 (MacAdam 3-step)	UU
		SPMWH3326MD3WAUYSA	UY (Kitting)	UN, UP, UQ, UR, US, UT, UU
4000	70	SPMWH3326MD3WAT0SA	T0 (Whole Bin)	TN, TP, TQ, TR, TS, TT, TU
		SPMWH3326MD3WAT3SA	T3 (MacAdam 3-step)	TU
		SPMWH3326MD3WATYSA	TY (Kitting)	TN, TP, TQ, TR, TS, TT, TU
5000	70	SPMWH3326MD3WAR0SA	R0 (Whole Bin)	RN, RP, RQ, RR, RS, RT, RU
		SPMWH3326MD3WAR3SA	R3 (MacAdam 3-step)	RU
		SPMWH3326MD3WARYSA	RY (Kitting)	RN, RP, RQ, RR, RS, RT, RU

d) Voltage Bins (I_F = 65 mA, T_s = 25℃)

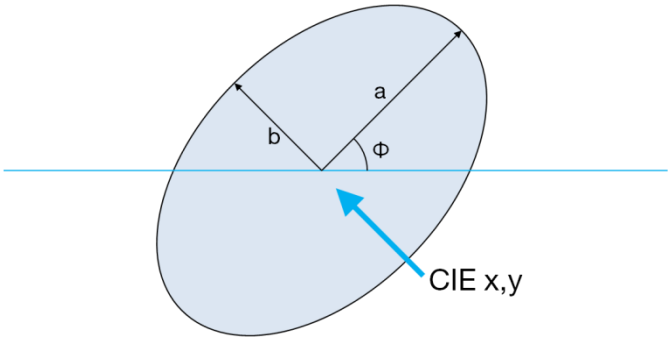
Nominal CCT (K)	CRI Min.	Product Code	Voltage Rank	Voltage Bin	Voltage Range (V)
-	-	-	WA	AY	2.6 ~ 2.7
				AZ	2.7 ~ 2.8
				A1	2.8 ~ 2.9

e) Chromaticity Region & Coordinates (I_F = 65 mA, T_s = 25℃)





f) Chromaticity Region & Coordinates (I_F = 65 mA, T_s = 25℃)

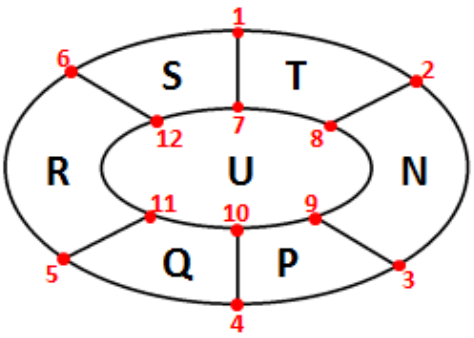


MacAdam	CCT (K)	Center point		Major-axis	Minor-axis	Rotation
		CIE x	CIE y	a	b	Φ
3 step	3000	0.4338	0.4030	0.0083	0.0041	53.22
	3500	0.4073	0.3917	0.0093	0.0041	54.00
	4000	0.3818	0.3797	0.0094	0.0040	53.72
	5000	0.3447	0.3553	0.0082	0.0035	59.62
5 step	3000	0.4338	0.4030	0.0138	0.0068	53.22
	3500	0.4073	0.3917	0.0155	0.0068	54.00
	4000	0.3818	0.3797	0.0157	0.0067	53.72
	5000	0.3447	0.3553	0.0137	0.0058	59.62

Note:

Samsung maintains measurement tolerance of: Cx, Cy = ±0.005

e) Chromaticity Region & Coordinates



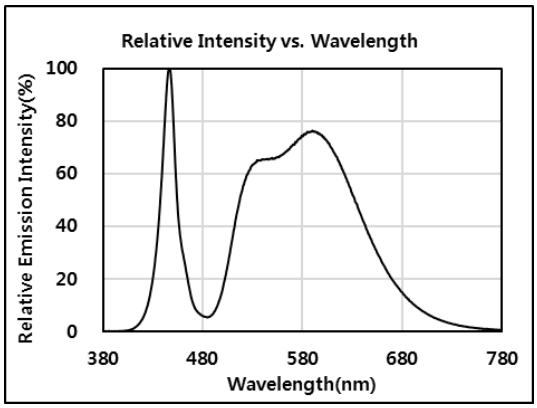
CCT	Region	CIE x	CIE y
3000K	1	0.4283	0.4071
	2	0.4382	0.4146
	3	0.4437	0.4105
	4	0.4393	0.3989
	5	0.4293	0.3913
	6	0.4239	0.3954
	7	0.4305	0.4054
	8	0.4364	0.41
	9	0.4397	0.4075
	10	0.4371	0.4005
	11	0.4311	0.396
	12	0.4279	0.3984

CCT	Region	CIE x	CIE y	CCT	Region	CIE x	CIE y	CCT	Region	CIE x	CIE y
3500K	1	0.4018	0.3957	4000K	1	0.3764	0.3837	5000K	1	0.3397	0.3583
	2	0.4125	0.4046		2	0.3871	0.3926		2	0.3482	0.367
	3	0.418	0.4005		3	0.3925	0.3887		3	0.3532	0.364
	4	0.4128	0.3877		4	0.3872	0.3758		4	0.3497	0.3524
	5	0.4022	0.3788		5	0.3765	0.3668		5	0.3412	0.3436
	6	0.3966	0.3828		6	0.3711	0.3707		6	0.3362	0.3465
	7	0.404	0.3941		7	0.3786	0.3821		7	0.3417	0.3571
	8	0.4104	0.3994		8	0.385	0.3874		8	0.3468	0.3623
	9	0.4137	0.397		9	0.3882	0.3851		9	0.3498	0.3605
	10	0.4106	0.3893		10	0.385	0.3773		10	0.3477	0.3535
	11	0.4042	0.384		11	0.3786	0.372		11	0.3426	0.3483
	12	0.4009	0.3864		12	0.3754	0.3743		12	0.3396	0.35

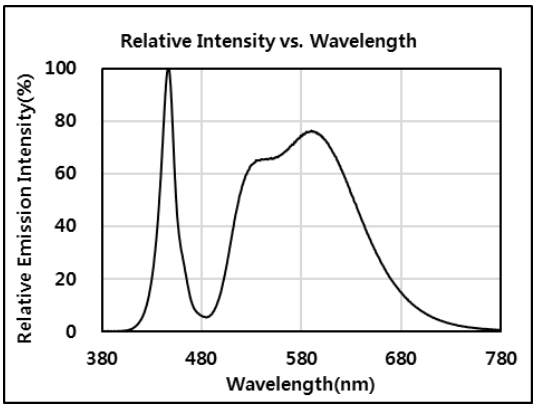
3. Typical Characteristics Graphs

a) Spectrum Distribution (I_F = 65 mA, T_s = 25℃)

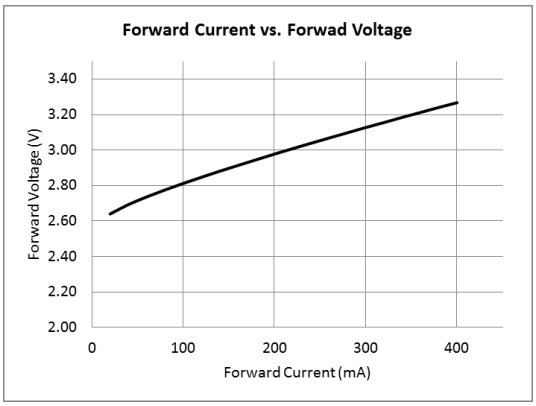
CCT : 4000K (70 CRI)

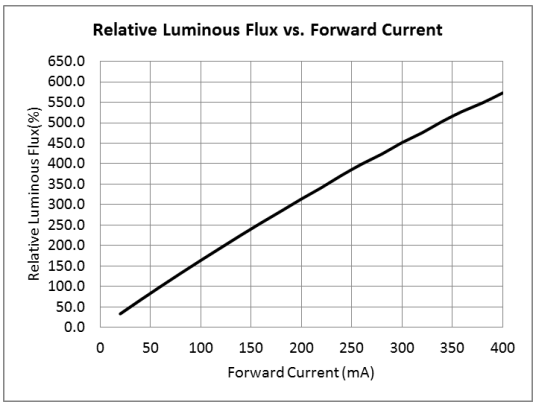


CCT : 5000K (70 CRI)

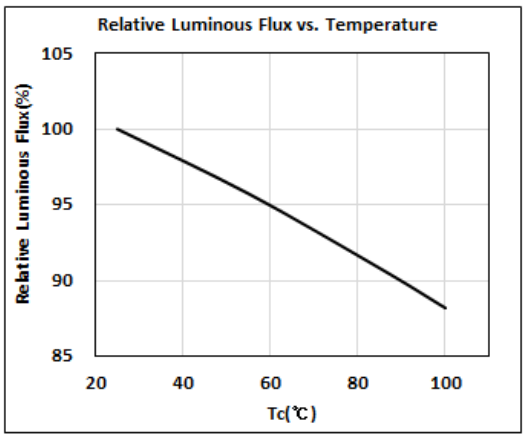


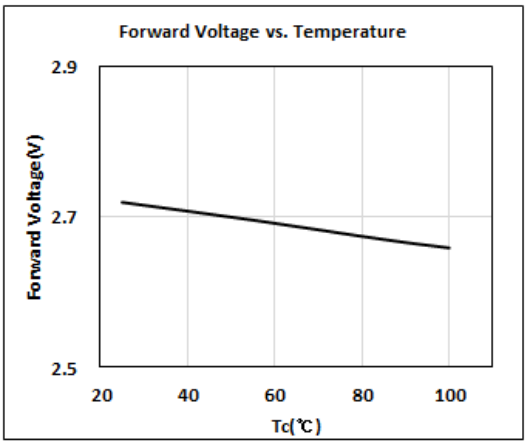
b) Forward Current Characteristics (T_s = 25℃)



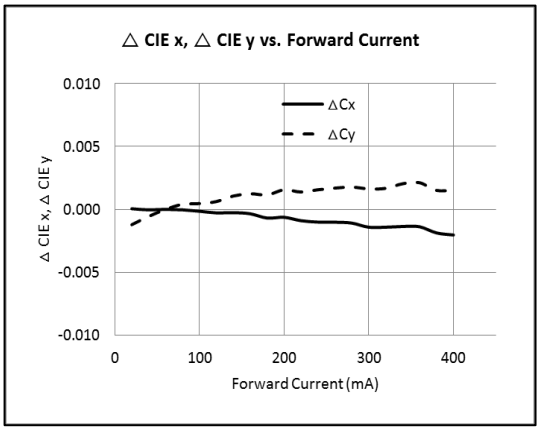


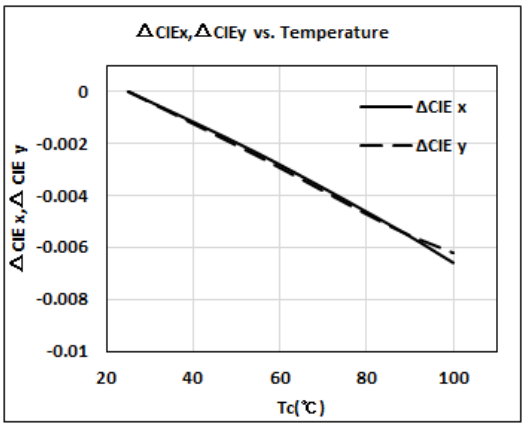
c) Temperature Characteristics (IF = 65mA)



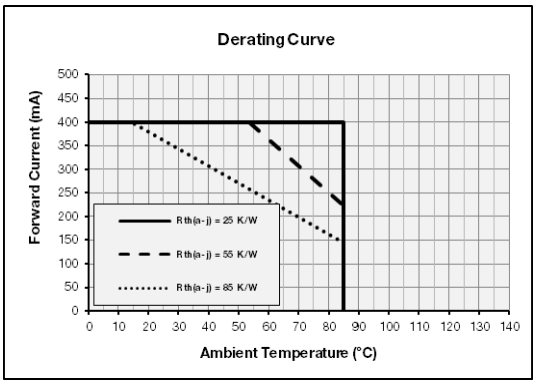


d) Color Shift Characteristics (IF = 65mA, T_s = 25℃)

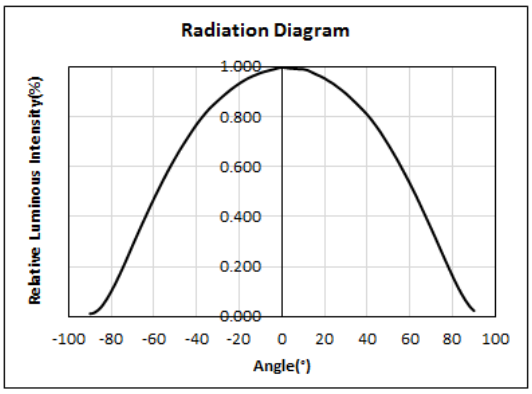




e) Derating Curve

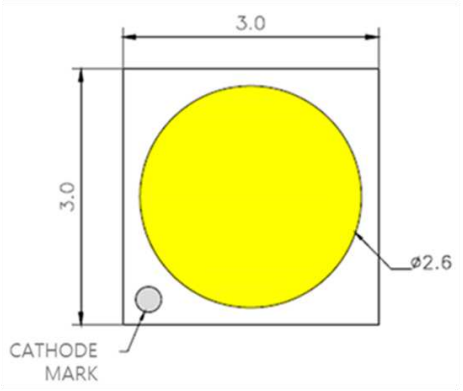


f) Beam Angle Characteristics (IF = 65mA, T_s = 25℃)

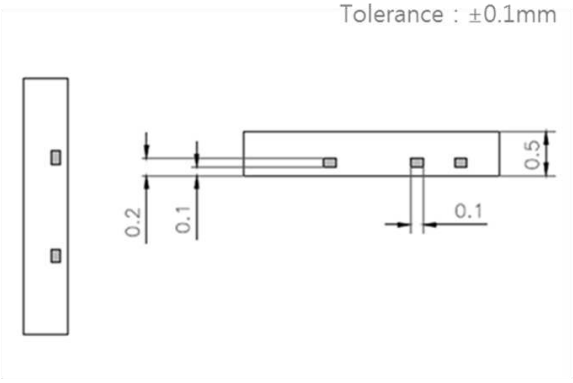


4. Outline Drawing & Dimension

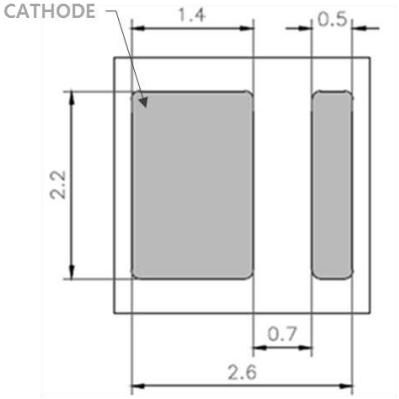
Measurement unit : mm
Tolerance : ±0.1mm



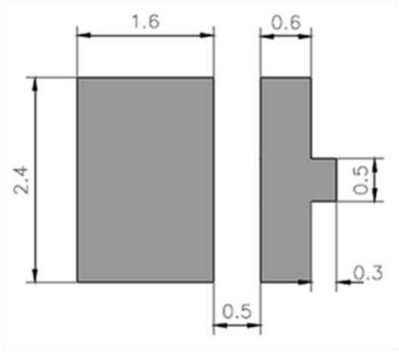
[TOP VIEW]



[SIDE VIEW]



[BOTTOM VIEW]



[RECOMMENDED PCB SOLDER PAD]

Notes:

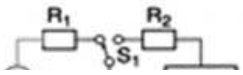
1) This LED has built-in ESD protection device(s) connected in parallel to LED chip(s).
2) T_s point and measurement method:
   ① Measure one point at the cathode pad, if necessary remove PSR of PCB to reach T_s point.
   ② All pads must be soldered to the PCB to dissipate heat properly, otherwise the LED can be damaged.

Precautions:

1) Pressure on the LEDs will influence to the reliability of the LEDs. Precautions should be taken to avoid strong pressure on the LEDs. Do not put stress on the LEDs during heating.
2) Re-soldering should not be done after the LEDs have been soldered. If re-soldering is unavoidable, LED`s characteristics should be carefully checked before and after such repair.
3) Do not stack assembled PCBs together. Since materials of LEDs is soft, abrasion between two PCB assembled with LED might cause catastrophic failure of the LEDs.

5. Reliability Test Items & Conditions

a) Test Items

Test Item	Test Condition	Test Hour / Cycle	Sample No.
Room Temperature Life Test	25 ºC, DC 400 mA	1000 h	22
High Temperature Life Test	85 ºC, DC 400 mA	1000 h	22
High Temperature Humidity Life Test	60 ºC, 90 % RH, DC 400 mA	1000 h	22
Low Temperature Life Test	-40 ºC, DC 400 mA	1000 h	22
Powered Temperature Cycle Test	-45 ºC / 20 min ↔ 85 ºC / 20 min, sweep 100 min cycle on/off: each 5 min, DC 150 mA	100 cycles	22
Thermal Cycle	-40 ºC / 15 min ↔ 100 ºC / 15 min → Hot plate 180 ºC	500 cycles	100
High Temperature Storage	120 ºC	1000 h	11
Low Temperature Storage	-40 ºC	1000 h	11
ESD (HBM)	 R_1: 10 MΩ R_2: 1.5 kΩ C: 100 pF V: ±5 kV	5 times	30
ESD (MM)	R_1: 10 MΩ R_2: 0 C: 200 pF V: ±0.5 kV	5 times	30
Vibration Test	20~2000~20 Hz, 200 m/s², sweep 4 min X, Y, Z 3 direction, each 1 cycle	4 cycles	11
Mechanical Shock Test	1500 g, 0.5 ms 3 shocks each X-Y-Z axis	5 cycles	11

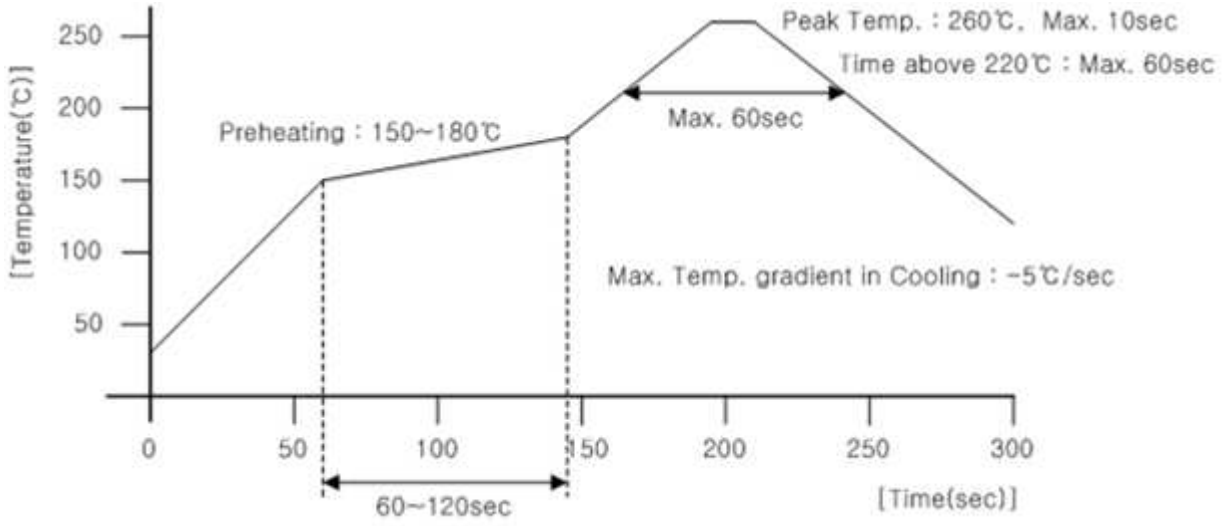
b) Criteria for Judging the Damage

Item	Symbol	Test Condition (T_s = 25 ºC)	Limit	
			Min	Max
Forward Voltage	V_F	I_F = 65 mA	Init. Value * 0.9	Init. Value * 1.1
Luminous Flux	Φ_v	I_F = 65 mA	Init. Value * 0.7	Init. Value * 1.1

6. Soldering Conditions

a) Reflow Conditions (Pb free)

Reflow frequency: 2 times max.



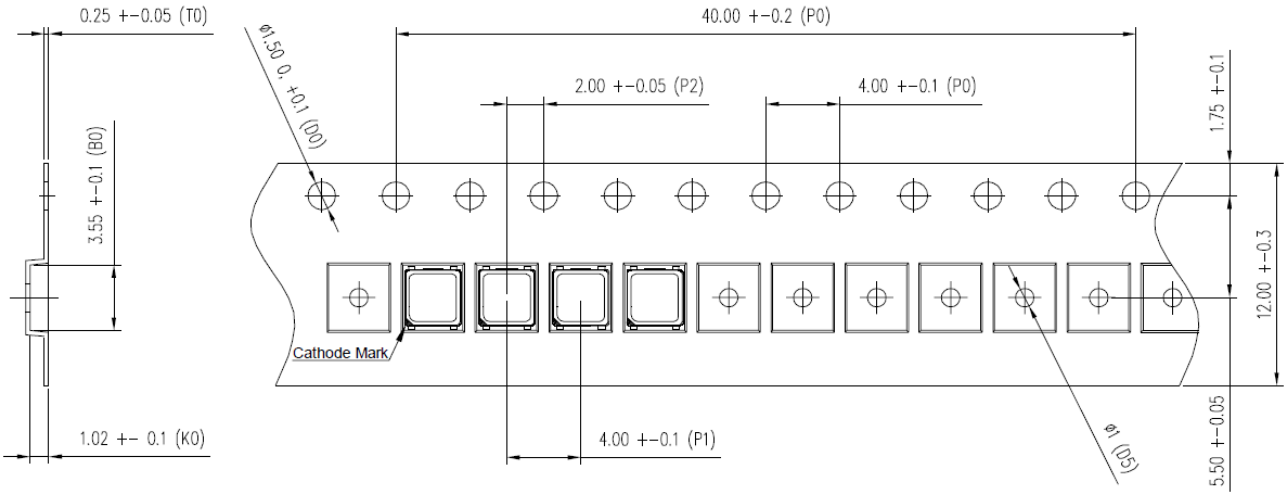
b) Manual Soldering Conditions

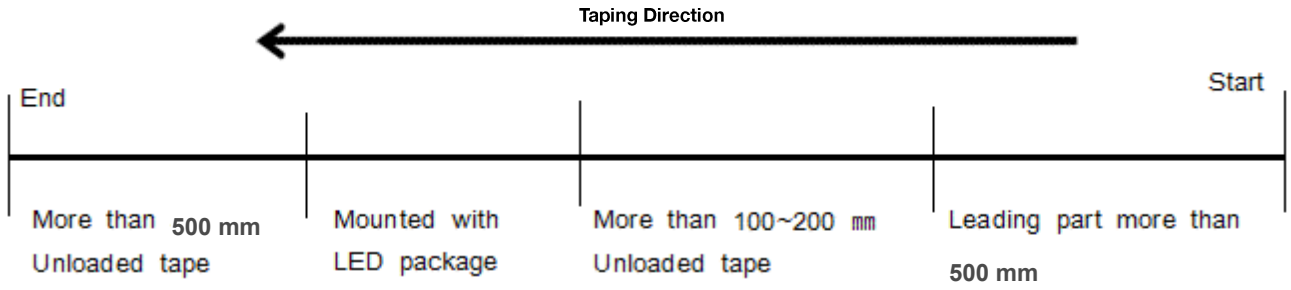
Not more than 5 seconds @ max. 300 ºC, under soldering iron.

7. Tape & Reel

a) Taping Dimension

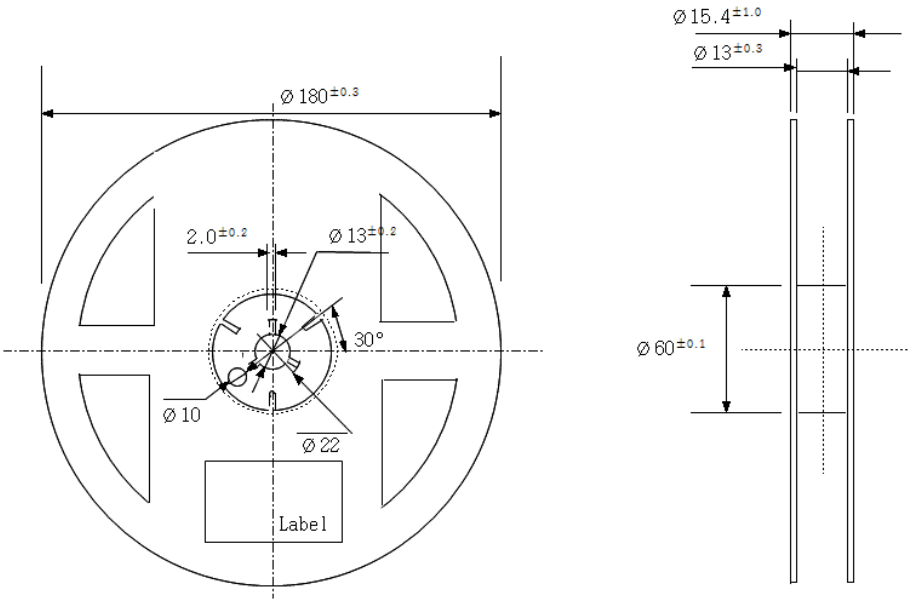
(unit: mm)





b) Reel Dimension

(unit: mm)

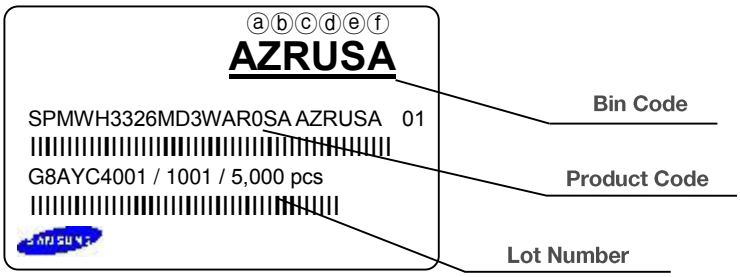


Notes:

1) Quantity: The quantity/reel is 5,000 pcs
2) Cumulative Tolerance: Cumulative tolerance / 10 pitches is ±0.2 mm
3) Adhesion Strength of Cover Tape: Adhesion strength is 0.1-0.7 N when the cover tape is turned off from the carrier tape at 10° angle to the carrier tape
4) Packaging: P/N, Manufacturing data code no. and quantity are indicated on the aluminum packing bag

8. Label Structure

a) Label Structure



Note: Denoted bin code and product code above is only an example (see description on page 5)

Bin Code:

ⓐⓑ: Forward Voltage bin (refer to page 8)

ⓒⓓ: Chromaticity bin (refer to page 10-13)

ⓔⓕ: Luminous Flux bin (refer to page 8)

b) Lot Number

The lot number is composed of the following characters:

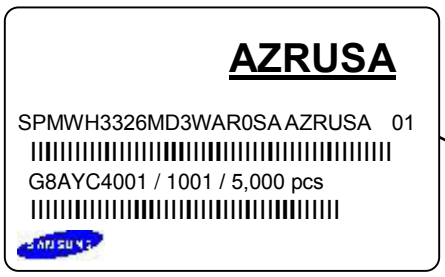
① ②③④⑤⑥⑦⑧⑨ / 1ⓐⓑⓒ / 5,000 pcs

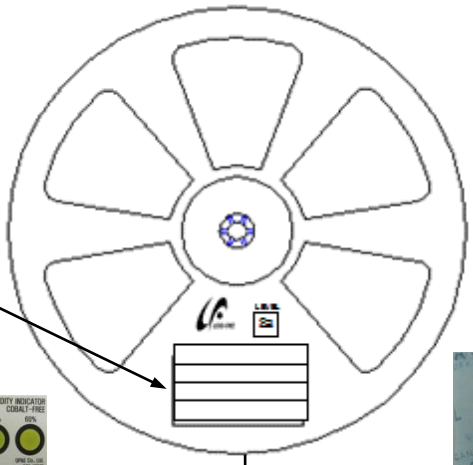
①	:	Production site (S: Giheung, Korea, G: Tianjin, China)
②	:	8 (LED)
③	:	Product state (A: Normal, B: Bulk, C: First Production, R: Reproduction, S: Sample)
④	:	Year (Z: 2015, A: 2016, B: 2017…)
⑤	:	Month (1~9, A, B, C)
⑥⑦⑧⑨	:	Day (1~9, A, B~V)
ⓐⓑⓒ	:	Product serial number (001 ~ 999)

9. Packing Structure

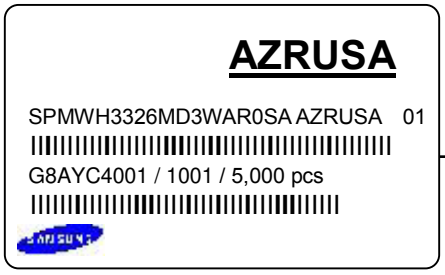
a) Packing Process (The quantity of PKG on the Reel to be Max 5,000pcs)

Reel





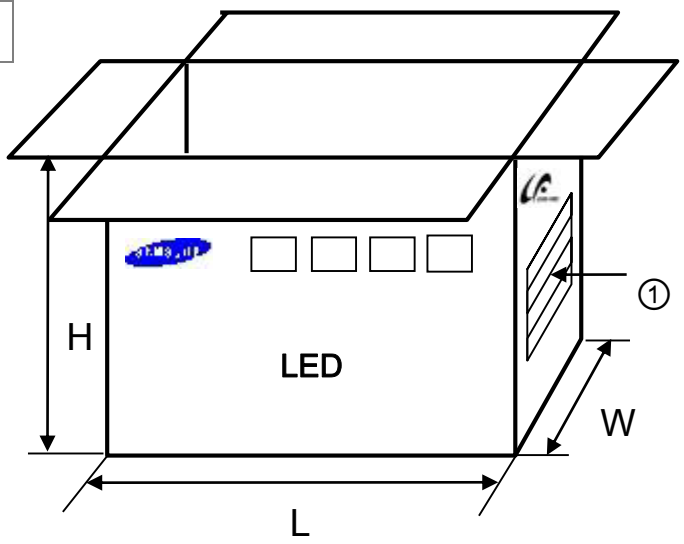
Aluminum Vinyl Packing Bag



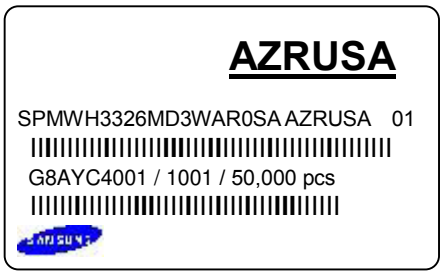
Outer Box

Material: Paper (SW3B(B))

Type	Size (mm)			Note
	L	W	H	
7 inch L	245 ± 5	220 ± 5	182 ± 5	Up to 10 reels
7 inch S	245 ± 5	220 ± 5	86 ± 5	Up to 5 reels



① **Side Label**



b) Packing Process for kitting (The quantity of PKG on the Reel to be Max 5,000pcs)

Reel

Kitting 'A'

AY★YSA

SPMWH3326MD3WA★YSA AY★YSA 01

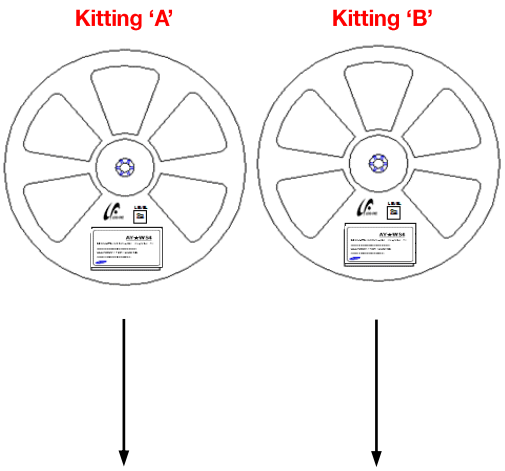
G8AW94001 / 1001 / 5,000 pcs

Kitting 'B'

AY★YSA

SPMWH3326MD3WA★YSA AY★YSA 01

G8AW94001 / 1001 / 5,000 pcs



Aluminum Vinyl Packing Bag

Kitting 'A'

AY★YSA

SPMWH3326MD3WA★YSA AY★YSA 01

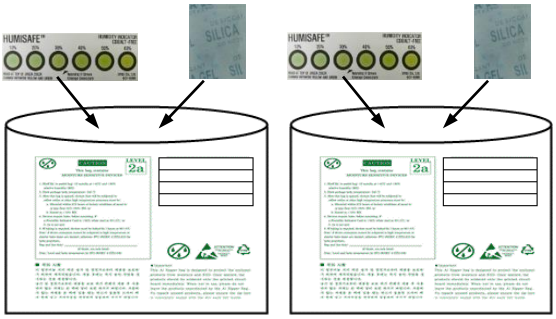
G8AW94001 / 1001 / 5,000 pcs

Kitting 'B'

AY★YSA

SPMWH3326MD3WA★YSA AY★YSA 01

G8AW94001 / 1001 / 5,000 pcs



Outer Box

Kitting 'A'

AY★YSA

SPMWH3326MD3WA★YSA AY★YSA 01

G8AW94001 / 1001 / 50,000 pcs

[BOX Label]

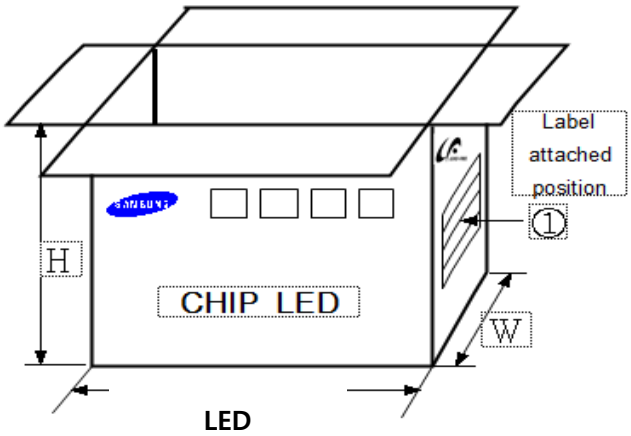
Kitting 'B'

AY★YSA

SPMWH3326MD3WA★YSA AY★YSA 01

G8AW94001 / 1001 / 50,000 pcs

[BOX Label]

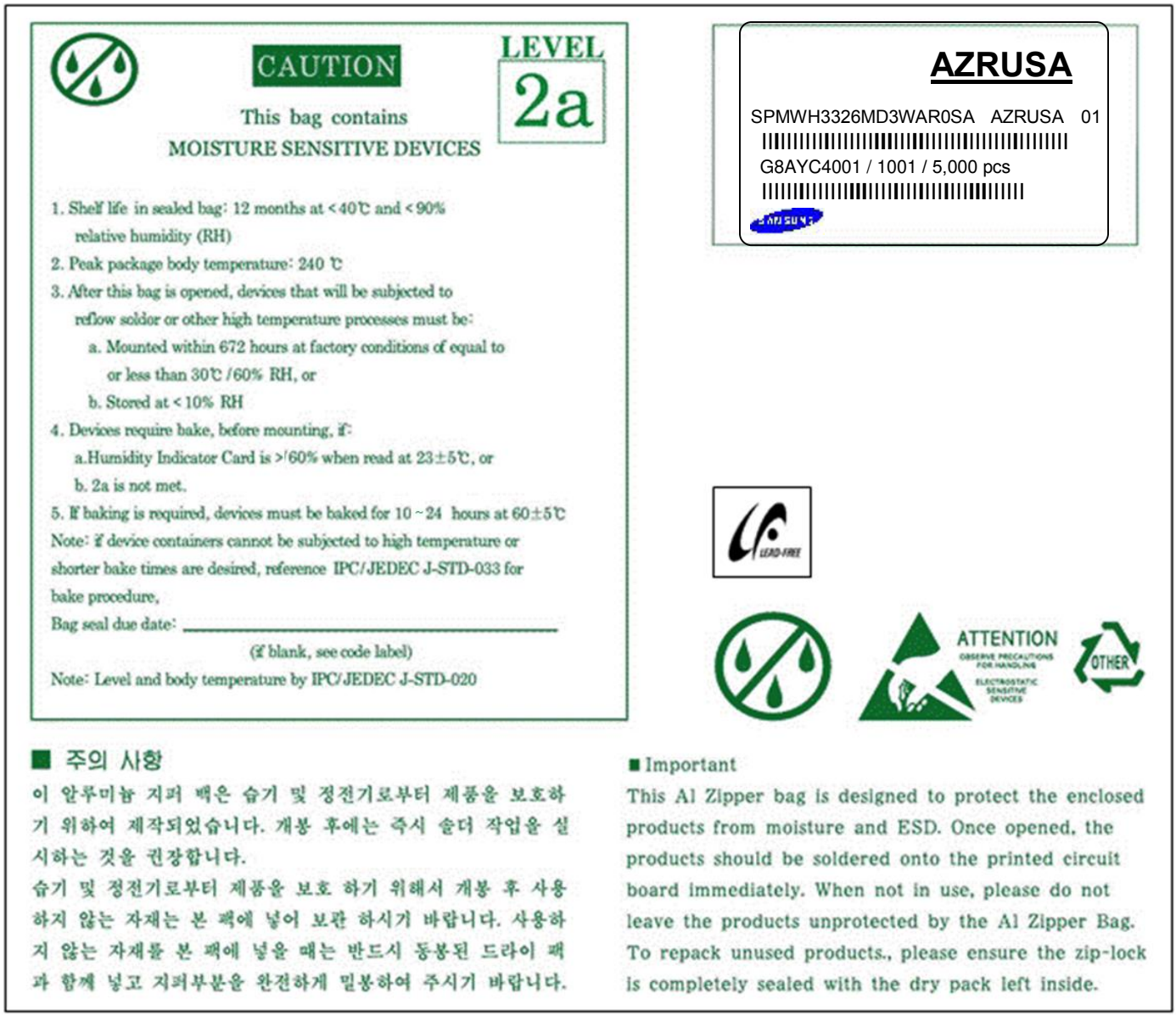


Note: "★" can be Nominal CCT code.

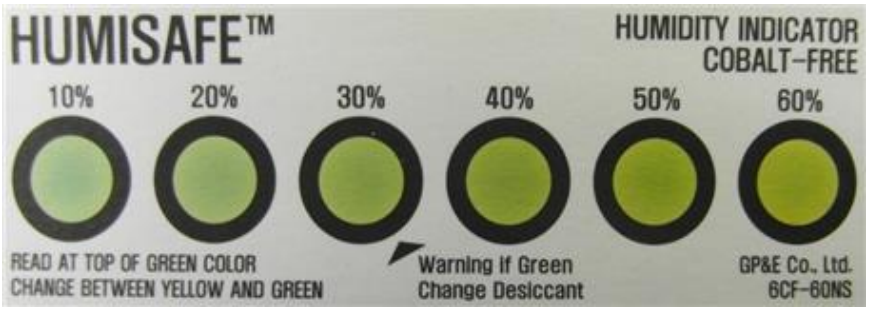
Material: Paper (SW3B(B))

Type	Size (mm)			Note
	L	W	H	
7 inch L	245 ± 5	220 ± 5	182 ± 5	Up to 10 reels

c) Aluminum Vinyl Packing Bag



c) Silica Gel & Humidity Indicator Card inside Aluminum Vinyl Bag



10. Precautions in Handling & Use

1) For over-current-proof function, customers are recommended to apply resistors to prevent sudden change of the current caused by slight shift of the voltage.

2) This device should not be used in any type of fluid such as water, oil, organic solvent, etc. When washing is required, IPA is recommended to use.

3) When the LEDs illuminate, operating current should be decided after considering the ambient maximum temperature.

4) LEDs must be stored in a clean environment. If the LEDs are to be stored for three months or more after being shipped from Samsung, they should be packed by a sealed container with nitrogen gas injected (shelf life of sealed bags: 12 months, temperature ~40 ºC, ~90 % RH).

5) After storage bag is opened, device subjected to soldering, solder reflow, or other high temperature processes must be:
   a. Mounted within 672 hours (28 days) at an assembly line with a condition of no more than 30 ºC / 60 % RH, or
   b. Stored at <10 % RH

6) Repack unused products with anti-moisture packing, fold to close any opening and then store in a dry place.

7) Devices require baking before mounting, if humidity card reading is >60 % at 23 ± 5 ºC.

8) Devices must be baked for 10~24 hours at 60 ± 5 ºC, if baking is required.

9) The LEDs are sensitive to the static electricity and surge. It is recommended to use a wrist band or anti-electrostatic glove when handling the LEDs. If voltage exceeding the absolute maximum rating is applied to LEDs, it may cause damage or even destruction to LED devices. Damaged LEDs may show some unusual characteristics such as increase in leak current, lowered turn-on voltage, or abnormal lighting of LEDs at low current.

10) VOCs (Volatile Organic Compounds) can be generated from adhesives, flux, hardener or organic additives used in luminaires (fixtures). Transparent LED silicone encapsulant is permeable to those chemicals and they may lead a discoloration of encapsulant when they exposed to heat or light. This phenomenon can cause a significant loss of light emitted (output) from the luminaires (fixtures). In order to prevent these problems, we recommend users to know the physical properties of the materials used in luminaires, and they must be selected carefully.

11) Risk of sulfurization (or tarnishing)
   The LED from Samsung Electronics Co., Ltd. uses a silver-plated lead frame and its surface color may change to black (or dark colored) when it is exposed to sulfur (S), chlorine (Cl) or other halogen compound. Sulfurization of lead frame may cause intensity degradation, change of chromaticity coordinates and, in extreme cases, open circuit. It requires caution. Due to possible sulfurization of lead frame, LED should not be used and stored together with oxidizing substances made of materials such as: rubber, plain paper, lead solder cream, etc.

Legal and additional information.

About Samsung Electronics Co., Ltd.

Samsung Electronics Co., Ltd. inspires the world and shapes the future with transformative ideas and technologies that redefine the worlds of TVs, smartphones, wearable devices, tablets, cameras, digital appliances, printers, medical equipment, network systems, and semiconductor and LED solutions. We are also leading in the Internet of Things space with the open platform SmartThings, our broad range of smart devices, and through proactive cross-industry collaboration. We employ 319,000 people across 84 countries with annual sales of US $196 billion. To discover more, and for the latest news, feature articles and press material, please visit the Samsung Newsroom at news.samsung.com.

Samsung Electronics Co., Ltd.
95, Samsung 2-ro
Giheung-gu
Yongin-si, Gyeonggi-do, 446-711
KOREA