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LM231B – CRI 90 Middle Power LED



Introduction

Features


- Package : Silicone Reflector LED Package
- Beam Angle : 120°
- Precondition : JEDEC Level 2a
- Dimension : 2.3 x 2.3 x 0.7 mm
- ESD withstand Voltage : up to $\pm 5\text{KV}$ [HBM]

Applications

- INDOOR LIGHTING : Ambient Light, LED tube, Down light, LED bulb and Ceiling Light

SAMSUNG ELECTRONICS

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1. Product Code Information

1) Luminous Flux Bins ($T_s = 25^\circ\text{C}$)

Nominal CCT	Product Code	Flux Rank	Sorting Condition $I_m @65\text{mA}$	
			Flux Bin	Flux Range (Φ_v, I_m)
2700K	SPMWHT223MD7WAW0S0	S0	S1	16.50 ~ 19.00
			S2	19.00 ~ 21.50
			S3	21.50 ~ 24.00
3000K	SPMWHT223MD7WAV0S0	S0	S1	17.00 ~ 19.50
			S2	19.50 ~ 22.00
			S3	22.00 ~ 24.50
3500K	SPMWHT223MD7WAU0S0	S0	S1	17.50 ~ 20.00
			S2	20.00 ~ 22.50
			S3	22.50 ~ 25.00
4000K	SPMWHT223MD7WAT0S0	S0	S1	18.00 ~ 20.50
			S2	20.50 ~ 23.00
			S3	23.00 ~ 25.50

Notes:

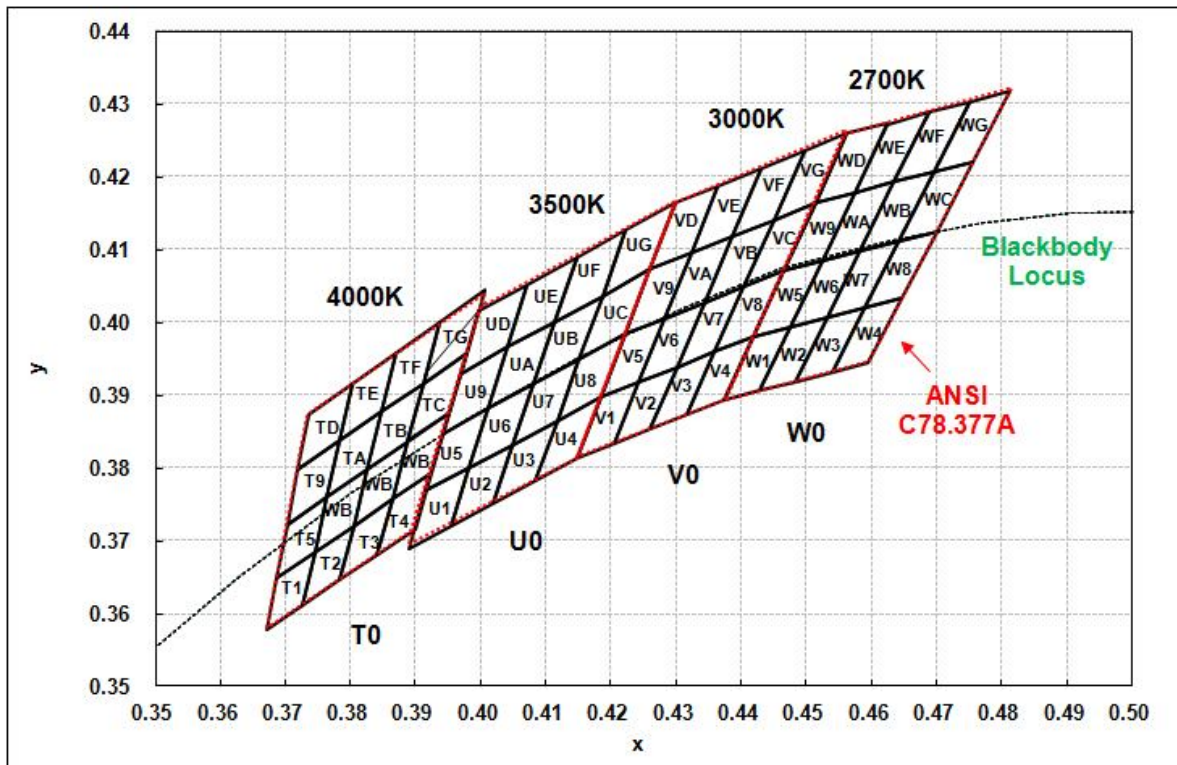
SAMSUNG ELECTRONICS maintains a tolerance of $\pm 5\%$ on Luminous Flux measurements

2) Color Bins ($T_s = 25^\circ\text{C}$)

1) Color Binning

Nominal CCT	Product Code	Color Rank	Chromaticity Bins
2700K	SPMWHT223MD7WAW0S0	W0(Whole bin)	W1, W2, W3, W4, W5, W6, W7, W8, W9, WA, WB, WC, WD, WE, WF, WG
	SPMWHT223MD7WAWKS0	WK(Kitting bin)	-
3000K	SPMWHT223MD7WAV0S0	V0(Whole bin)	V1, V2, V3, V4, V5, V6, V7, V8, V9, VA, VB, VC, VD, VE, VF, VG
	SPMWHT223MD7WAVKS0	VK(Kitting bin)	-
3500K	SPMWHT223MD7WAU0S0	U0(Whole bin)	U1, U2, U3, U4, U5, U6, U7, U8, U9, UA, UB, UC, UD, UE, UF, UG
	SPMWHT223MD7WAUKS0	UK(Kitting bin)	-
4000K	SPMWHT223MD7W0T0S0	T0(Whole bin)	T1, T2, T3, T4, T5, T6, T7, T8, T9, TA, TB, TC, TD, TE, TF, TG
	SPMWHT223MD7W0TKS0	TK(Kitting bin)	-

2) Chromaticity Region & Coordinates



2) Chromaticity Region & Coordinates (Continued)

Region	CIE X	CIE Y	Region	CIE X	CIE Y
W rank (2700K)					
W1	0.4373	0.3893	W9	0.4465	0.4071
	0.4418	0.3981		0.4513	0.4164
	0.4475	0.3994		0.4573	0.4178
	0.4428	0.3906		0.4523	0.4085
W2	0.4428	0.3906	WA	0.4523	0.4085
	0.4475	0.3994		0.4573	0.4178
	0.4532	0.4008		0.4634	0.4193
	0.4483	0.3919		0.4582	0.4099
W3	0.4483	0.3919	WB	0.4582	0.4099
	0.4532	0.4008		0.4634	0.4193
	0.4589	0.4021		0.4695	0.4207
	0.4538	0.3931		0.4641	0.4112
W4	0.4538	0.3931	WC	0.4641	0.4112
	0.4589	0.4021		0.4695	0.4207
	0.4646	0.4034		0.4756	0.4221
	0.4593	0.3944		0.4700	0.4126
W5	0.4418	0.3981	WD	0.4513	0.4164
	0.4465	0.4071		0.4562	0.4260
	0.4523	0.4085		0.4624	0.4274
	0.4475	0.3994		0.4573	0.4178
W6	0.4475	0.3994	WE	0.4573	0.4178
	0.4523	0.4085		0.4624	0.4274
	0.4582	0.4099		0.4687	0.4289
	0.4532	0.4008		0.4634	0.4193
W7	0.4532	0.4008	WF	0.4634	0.4193
	0.4582	0.4099		0.4687	0.4289
	0.4641	0.4112		0.4750	0.4304
	0.4589	0.4021		0.4695	0.4207
W8	0.4589	0.4021	WG	0.4695	0.4207
	0.4641	0.4112		0.4750	0.4304
	0.4700	0.4126		0.4813	0.4319
	0.4646	0.4034		0.4756	0.4221

Region	CIE X	CIE Y	Region	CIE X	CIE Y
V rank (3000K)					
V1	0.4147	0.3814	V9	0.4221	0.3984
	0.4183	0.3898		0.4259	0.4073
	0.4242	0.3919		0.4322	0.4096
	0.4203	0.3833		0.4281	0.4006
V2	0.4203	0.3833	VA	0.4281	0.4006
	0.4242	0.3919		0.4322	0.4096
	0.4300	0.3939		0.4385	0.4119
	0.4259	0.3853		0.4342	0.4028
V3	0.4259	0.3853	VB	0.4342	0.4028
	0.4300	0.3939		0.4385	0.4119
	0.4359	0.3960		0.4449	0.4141
	0.4316	0.3873		0.4403	0.4049
V4	0.4316	0.3873	VC	0.4403	0.4049
	0.4359	0.3960		0.4449	0.4141
	0.4418	0.3981		0.4513	0.4164
	0.4373	0.3893		0.4465	0.4071
V5	0.4183	0.3898	VD	0.4259	0.4073
	0.4221	0.3984		0.4299	0.4165
	0.4281	0.4006		0.4364	0.4188
	0.4242	0.3919		0.4322	0.4096
V6	0.4242	0.3919	VE	0.4322	0.4096
	0.4281	0.4006		0.4364	0.4188
	0.4342	0.4028		0.4430	0.4212
	0.4300	0.3939		0.4385	0.4119
V7	0.4300	0.3939	VF	0.4385	0.4119
	0.4342	0.4028		0.4430	0.4212
	0.4403	0.4049		0.4496	0.4236
	0.4359	0.3960		0.4449	0.4141
V8	0.4359	0.3960	VG	0.4449	0.4141
	0.4403	0.4049		0.4496	0.4236
	0.4465	0.4071		0.4562	0.4260
	0.4418	0.3981		0.4513	0.4164



2) Chromaticity Region & Coordinates (Continued)

Region	CIE X	CIE Y	Region	CIE X	CIE Y
U rank (3500K)					
U1	0.3889	0.3690	U9	0.3941	0.3848
	0.3953	0.3720		0.4010	0.3882
	0.3981	0.3800		0.4040	0.3966
	0.3915	0.3768		0.3968	0.3930
U2	0.3953	0.3720	UA	0.4010	0.3882
	0.4017	0.3751		0.4080	0.3916
	0.4048	0.3832		0.4113	0.4001
	0.3981	0.380		0.4040	0.3966
U3	0.4017	0.3751	UB	0.4080	0.3916
	0.4082	0.3782		0.4150	0.3950
	0.4116	0.3865		0.4186	0.4037
	0.4048	0.3832		0.4113	0.4001
U4	0.4082	0.3782	UC	0.4150	0.3950
	0.4147	0.3814		0.4221	0.3984
	0.4183	0.3898		0.4259	0.4073
	0.4116	0.3865		0.4186	0.4037
U5	0.3915	0.3768	UD	0.3968	0.3930
	0.3981	0.3800		0.4040	0.3966
	0.4010	0.3882		0.4071	0.4052
	0.3941	0.3848		0.3996	0.4015
U6	0.3981	0.3800	UE	0.4040	0.3966
	0.4048	0.3832		0.4113	0.4001
	0.4080	0.3916		0.4146	0.4089
	0.4011	0.3882		0.4071	0.4052
U7	0.4048	0.3832	UF	0.4113	0.4001
	0.4116	0.3865		0.4186	0.4037
	0.4150	0.3950		0.4222	0.4127
	0.4080	0.3916		0.4146	0.4089
U8	0.4116	0.3865	UG	0.4186	0.4037
	0.4183	0.3898		0.4259	0.4073
	0.4221	0.3984		0.4299	0.4165
	0.4150	0.3950		0.4222	0.4127

Region	CIE X	CIE Y	Region	CIE X	CIE Y
T rank (4000K)					
T1	0.3670	0.3578	T9	0.3702	0.3722
	0.3726	0.3612		0.3763	0.3760
	0.3744	0.3685		0.3782	0.3837
	0.3686	0.3649		0.3719	0.3797
T2	0.3726	0.3612	TA	0.3763	0.3760
	0.3783	0.3646		0.3825	0.3798
	0.3804	0.3721		0.3847	0.3877
	0.3744	0.3685		0.3782	0.3837
T3	0.3783	0.3646	TB	0.3825	0.3798
	0.3840	0.3681		0.3887	0.3836
	0.3863	0.3758		0.3912	0.3917
	0.3804	0.3721		0.3847	0.3877
T4	0.3840	0.3681	TC	0.3887	0.3837
	0.3898	0.3716		0.3950	0.3875
	0.3924	0.3794		0.3978	0.3958
	0.3863	0.3758		0.3912	0.3917
T5	0.3686	0.3649	TD	0.3719	0.3797
	0.3744	0.3685		0.3782	0.3837
	0.3763	0.3760		0.3802	0.3916
	0.3702	0.3722		0.3736	0.3874
T6	0.3744	0.3685	TE	0.3782	0.3837
	0.3804	0.3721		0.3847	0.3877
	0.3825	0.3798		0.3869	0.3958
	0.3763	0.3760		0.3802	0.3916
T7	0.3804	0.3721	TF	0.3847	0.3877
	0.3863	0.3758		0.3912	0.3917
	0.3887	0.3836		0.3937	0.4001
	0.3825	0.3798		0.3869	0.3958
T8	0.3863	0.3758	TG	0.3912	0.3917
	0.3924	0.3794		0.3978	0.3958
	0.3950	0.3875		0.4006	0.4044
	0.3887	0.3836		0.3937	0.4001

Notes:

SAMSUNG ELECTRONICS maintains ± 0.005 tolerance of CCx, CCy



2. Luminous Flux Characteristics (T_s = 25°C)

Nominal CCT	Minimum CRI	If(mA)	Vf(V)	Power(W)	Flux(lm)	lm/W
2700K	90	50	2.85	0.14	16.3	114
		60	2.88	0.17	19.1	111
		65	2.90	0.19	20.5	109
		70	2.91	0.20	21.8	107
		80	2.93	0.23	24.4	104
		90	2.96	0.27	26.8	101
		100	2.98	0.30	29.2	98
		150	3.07	0.46	39.7	86
3000K	90	50	2.85	0.14	17.1	120
		60	2.88	0.17	20.1	116
		65	2.90	0.19	21.6	114
		70	2.91	0.20	23.0	113
		80	2.93	0.23	25.8	110
		90	2.96	0.27	28.4	107
		100	2.98	0.30	31.0	104
		150	3.07	0.46	42.5	92
3500K	90	50	2.85	0.14	17.9	126
		60	2.88	0.17	21.1	122
		65	2.90	0.19	22.6	120
		70	2.91	0.20	24.1	119
		80	2.93	0.23	27.1	115
		90	2.96	0.27	30.0	113
		100	2.98	0.30	32.8	110
		150	3.07	0.46	45.2	98
4000K	90	50	2.85	0.14	18.2	127
		60	2.88	0.17	21.4	124
		65	2.90	0.19	22.9	122
		70	2.91	0.20	24.5	120
		80	2.93	0.23	27.5	117
		90	2.96	0.27	30.4	114
		100	2.98	0.30	33.3	112
		150	3.07	0.46	46.3	100

Notes:

Luminous Flux(Φ_v , lm) values are for representative reference only

3. Characteristics

1) Absolute Maximum Rating

Item	Symbol	Rating	Condition
Operating temperature range	T_{op}	-40°C ~ +85°C	-
Storage temperature range	T_{stg}	-40°C ~ +100°C	-
LED junction temperature	T_J	110°C	-
Forward Current	I_F	150 mA	-
Peak Pulsed Forward Current	I_{FP}	300 mA	Duty 1/10 pulse width 10ms
Assembly Process Temperature	-	260°C, < 10sec	-
ESD	-	5kV	HBM

2) Electro-optical Characteristics

Item	Unit	Nominal CCT	Product Code	Rank	Min	Typ	Max	
Forward Voltage ¹⁾ (V_F) (@65 mA, $T_s = 25^\circ\text{C}$)	V	-	-	WA	AZ	2.70	-	2.80
				A1	2.80	-	2.90	
				A2	2.90	-	3.00	
				A3	3.00	-	3.10	
				A4	3.10	-	3.20	
Luminous Flux ²⁾ (Φ_v) (@65 mA, $T_s = 25^\circ\text{C}$)	lm	2700K (W0)	*WAW0S1	S1	16.50	-	19.00	
			*WAW0S2	S2	19.00	-	21.50	
			*WAW0S3	S3	21.50	-	24.00	
		3000K (V0)	*WAV0S1	S1	17.00	-	19.50	
			*WAV0S2	S2	19.50	-	22.00	
			*WAV0S3	S3	22.00	-	24.50	
		3500K (U0)	*WAU0S1	S1	17.50	-	20.00	
			*WAU0S2	S2	20.00	-	22.50	
			*WAU0S3	S3	22.50	-	25.00	
		4000K (T0)	*WAT0S1	S1	18.00	-	20.50	
			*WAT0S2	S2	20.50	-	23.00	
			*WAT0S3	S3	23.00	-	25.50	
Reverse Voltage (@5 mA, $T_s = 25^\circ\text{C}$)	V	-	-	-	0.7	-	1.2	
Color Rendering Index ³⁾ (R_a)	-	-	-	7	90	-	-	
Special CRI ⁴⁾ (R9)	-	-	-	-	50	-	-	
Thermal resistance	°C/W	-	-	-	-	20	-	
Beam Angle					-	120	-	

Notes:

1)~4) SAMSUNG ELECTRONICS maintains a tolerance of $V_F:\pm 0.1$ V, $\Phi_v:\pm 5$ %, $R_a:\pm 3.0$, $R9:\pm 6.5$ on measurements

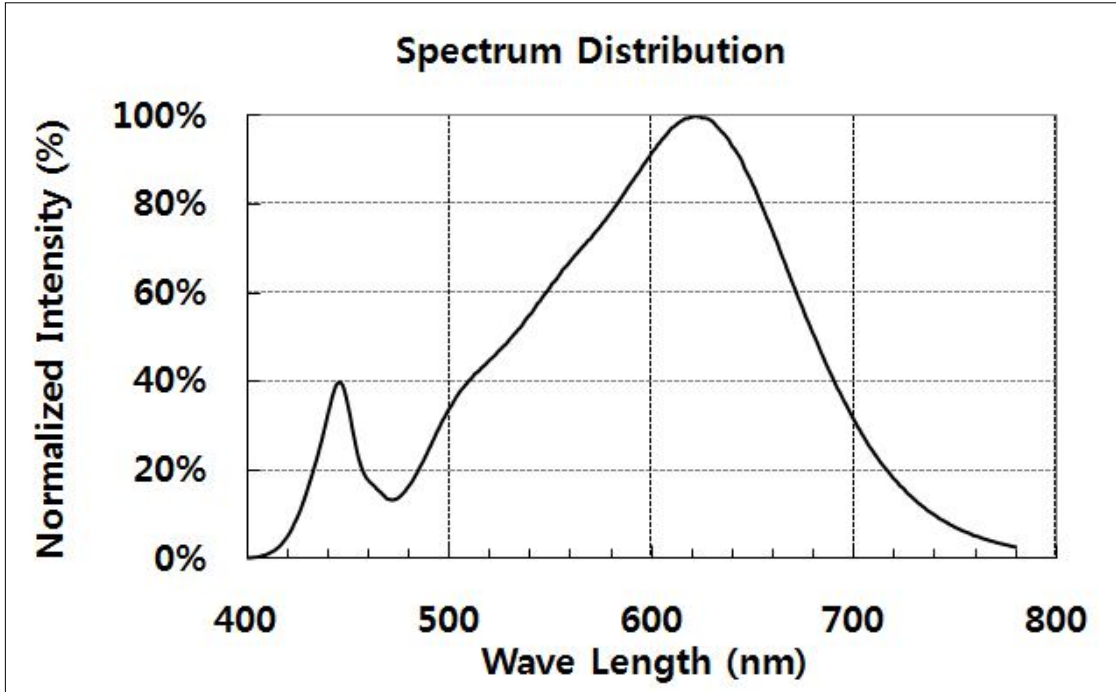
5) " * " is Product Code of "SPMWHT223MD7"

4. Typical Characteristics Graph (@65mA)

1) Spectrum Distribution

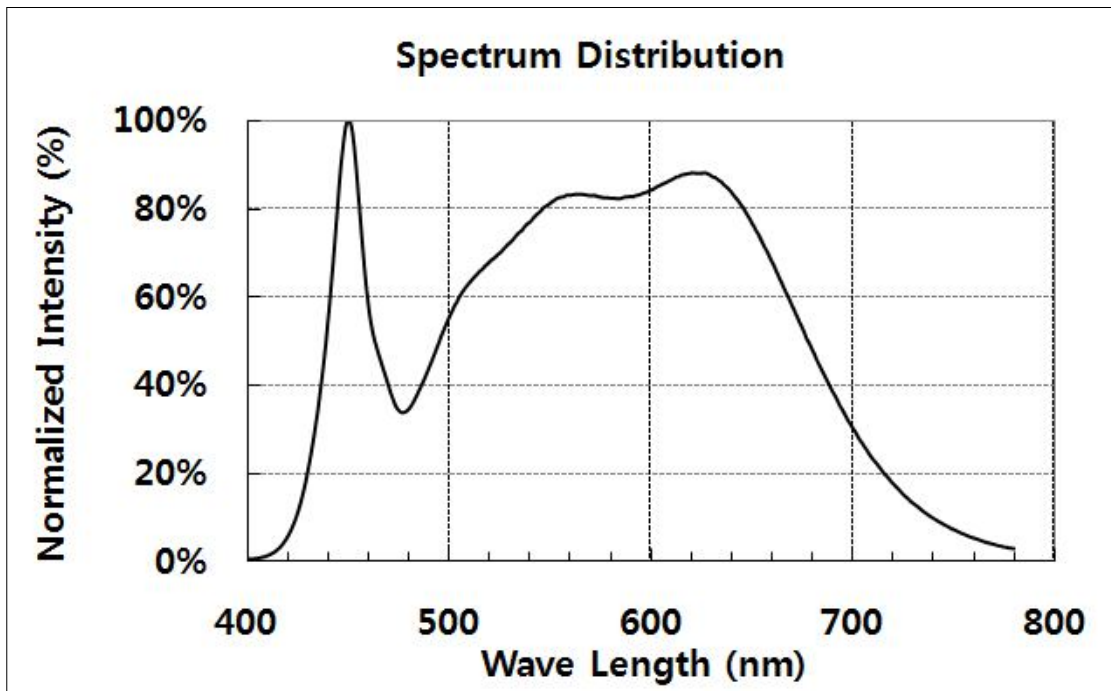
[CCT : 2700K & 3000K]

$T_s = 25^\circ\text{C}$



[CCT : 3500K & 4000K]

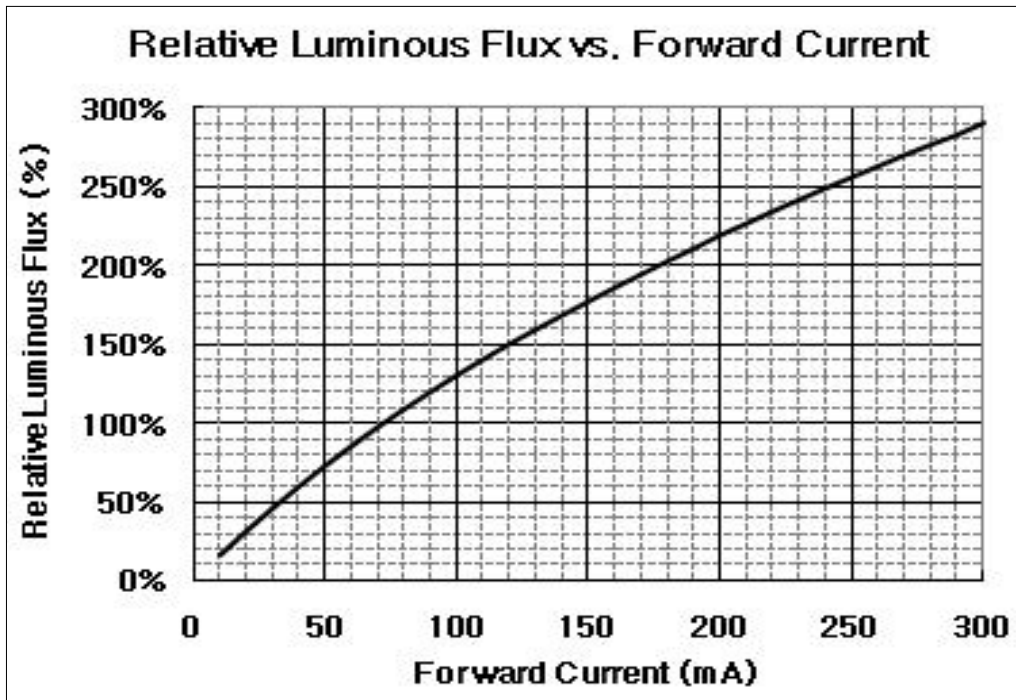
$T_s = 25^\circ\text{C}$



2) Forward Current Characteristics

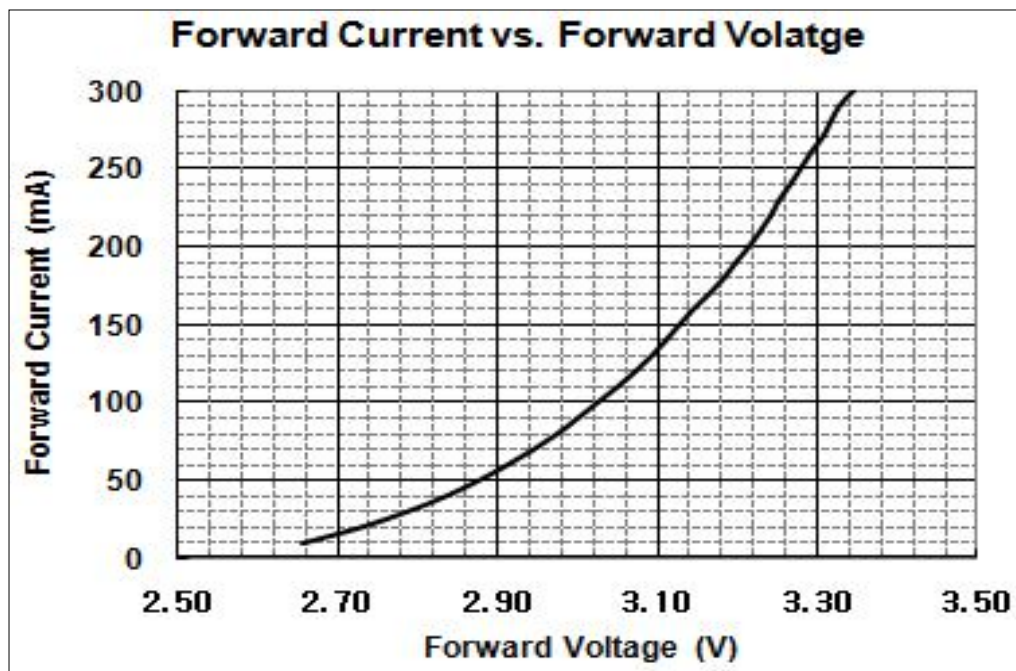
[Relative Luminous Flux vs. Forward Current]

$T_s = 25^\circ\text{C}$



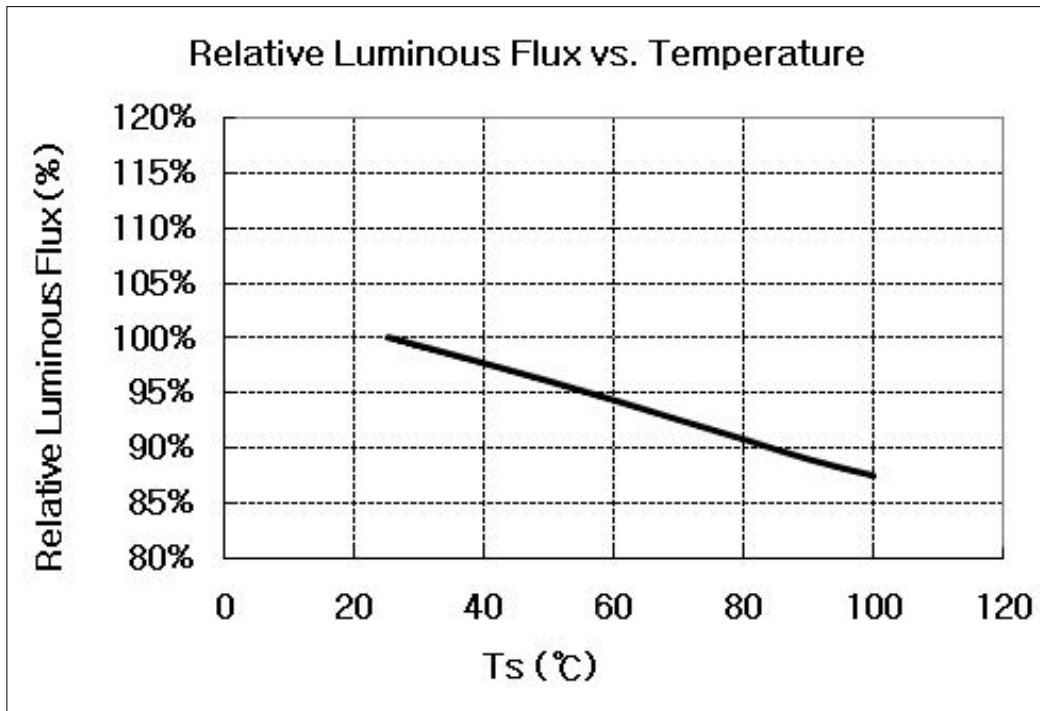
[Forward Current vs. Forward Voltage]

$T_s = 25^\circ\text{C}$

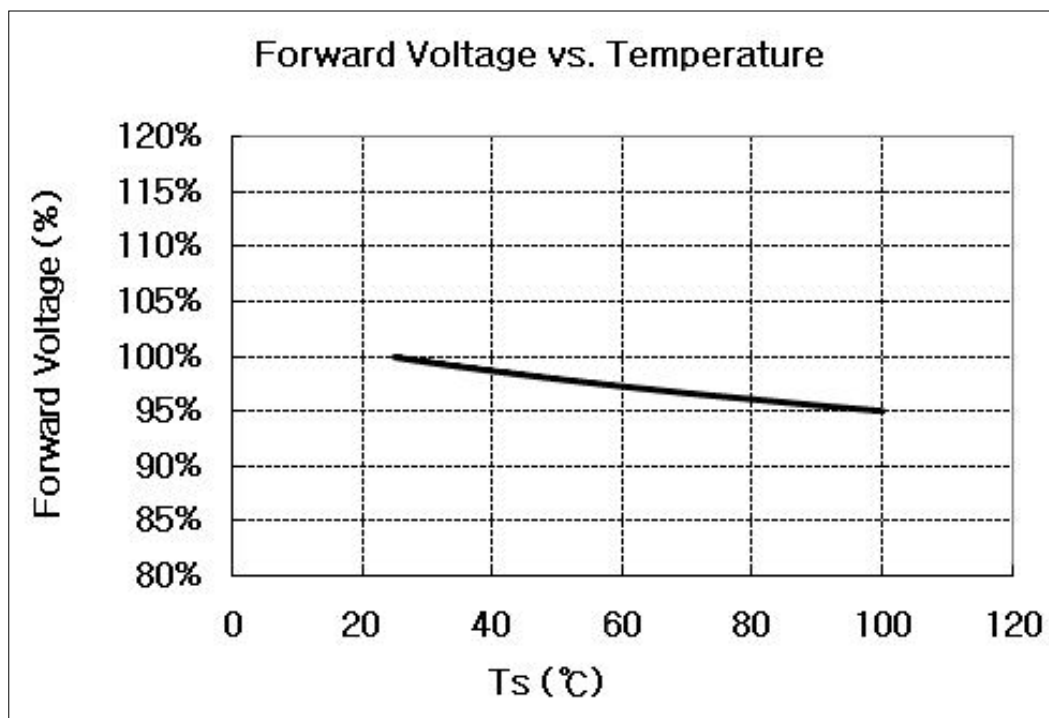


3) Temperature Characteristics (@65mA)

[Relative Luminous Flux vs. Ts (solder temp.)]

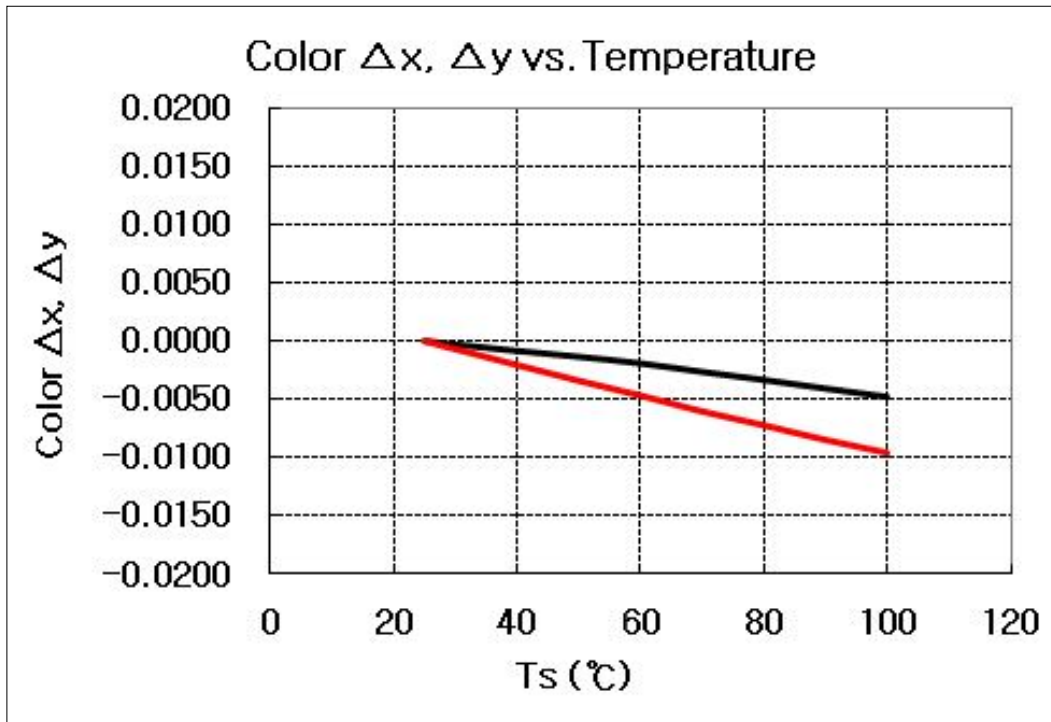


[Forward Voltage vs. Ts (solder temp.)]



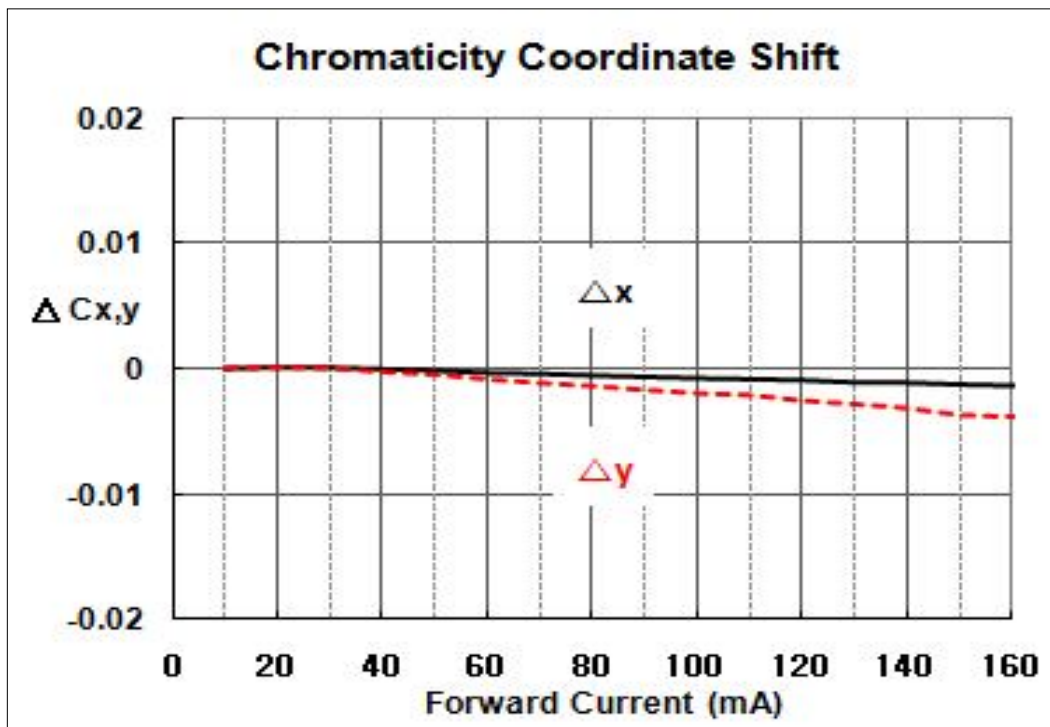
[Color Δx , Δy vs. T_s (solder temp.) @65mA]

CCT : 3000K



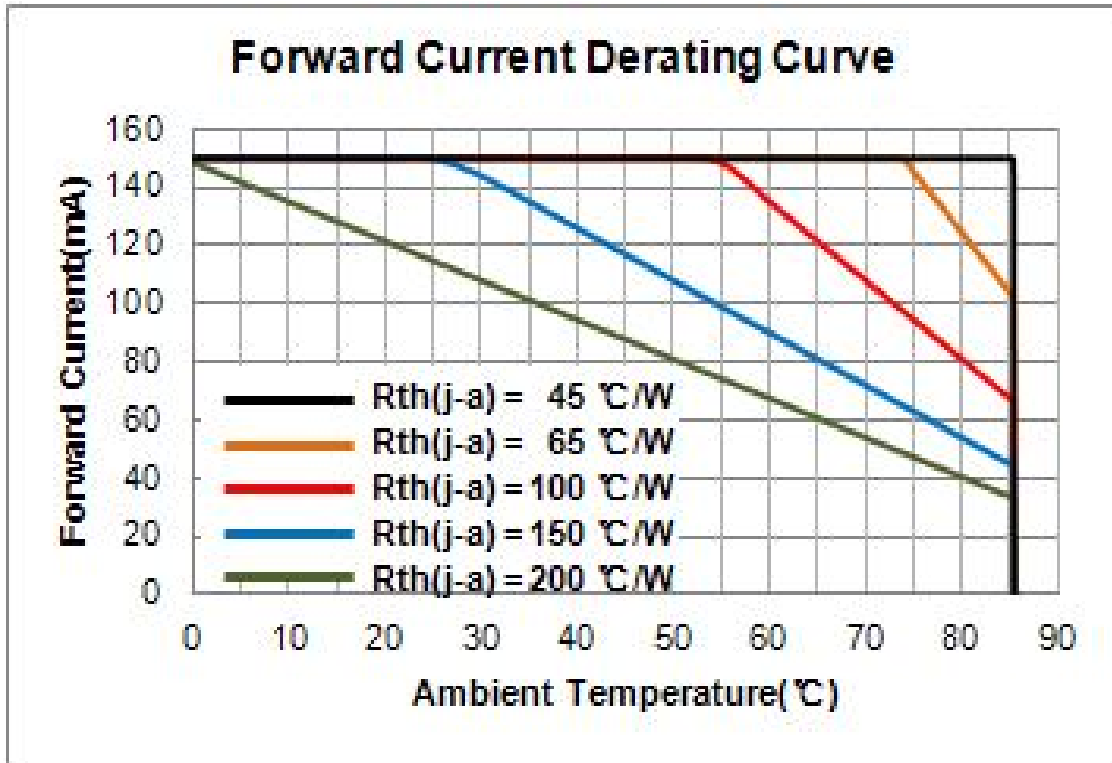
4) Color shift Characteristics ($T_s = 25^\circ\text{C}$)

[Color Δx , Δy vs. Forward Current



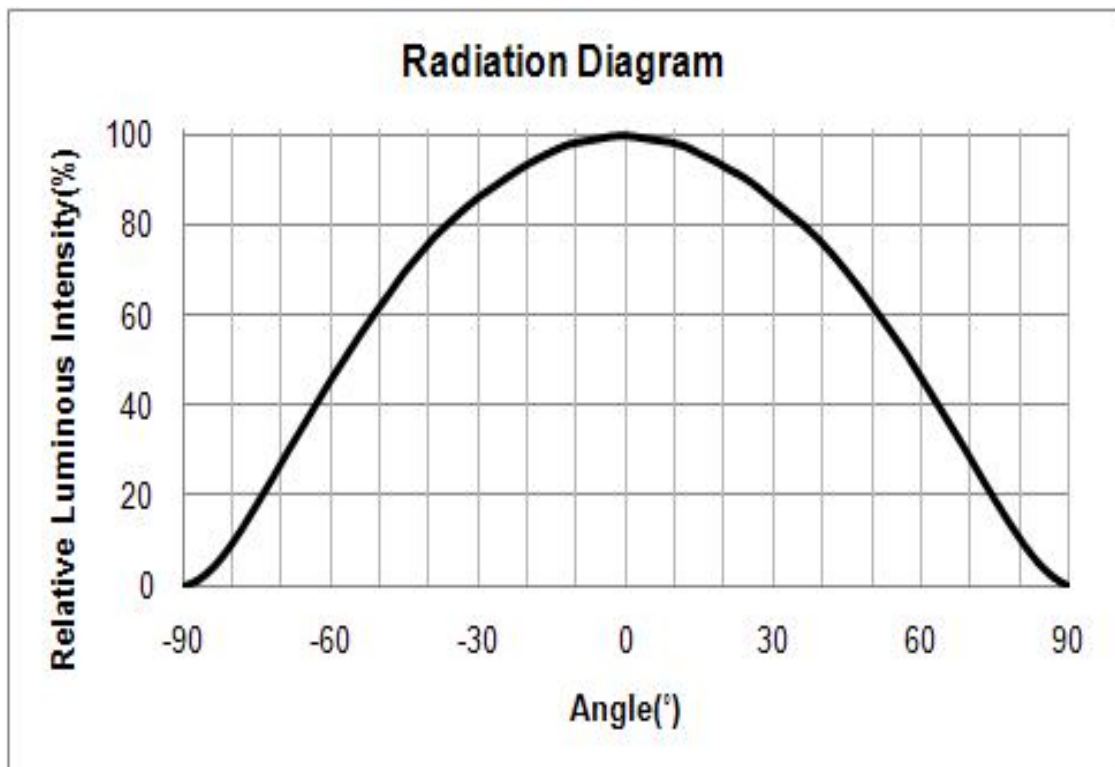
5) Derating Curve

$T_a = 25^\circ\text{C}$

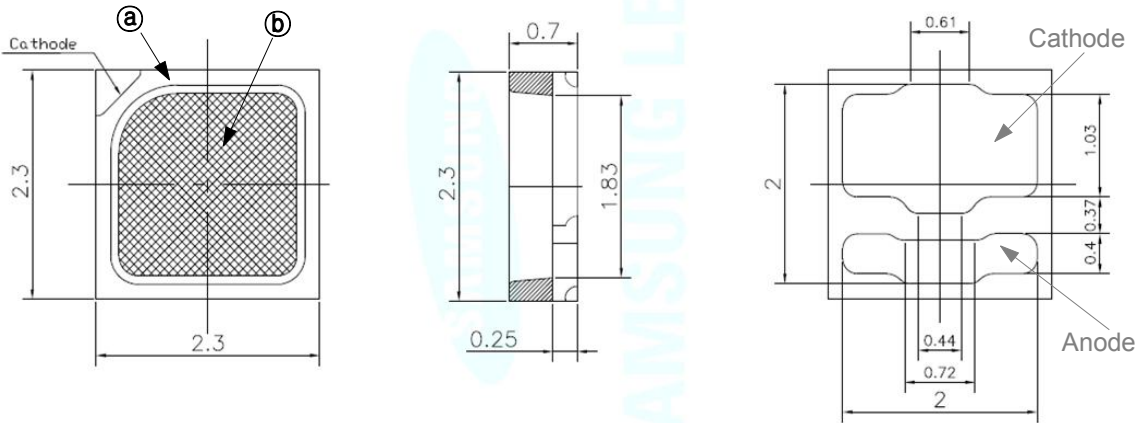


6) Beam Angle Characteristics (@65mA)

$T_s = 25^\circ\text{C}$

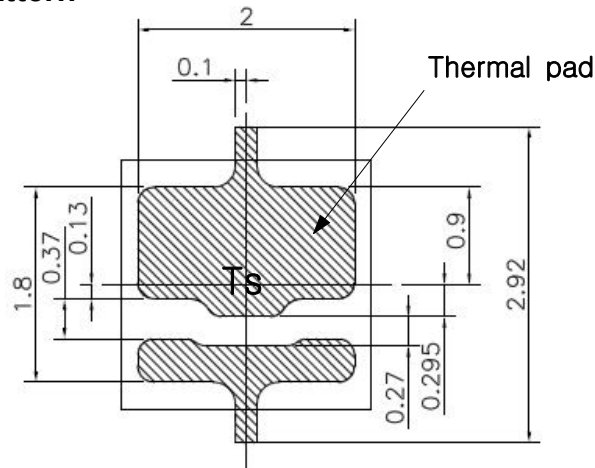


5. Outline Drawing & Dimension



1. Tolerance is ± 0.1 mm
2. The maximum compressing force is 15N on the silicone ①
3. Do not place pressure on the encapsulation resin ②

Recommended Land Pattern



Notes:

- 1) This LED has built-in ESD protection device(s) connected in parallel to LED Chip(s).
- 2) Ts point & measurement method
 - ① Measure the nearest point to the thermal pad. If necessary, remove PSR of PCB to reach Ts point.
 - ② Thermal pad must be soldered to the PCB to dissipate heat properly. Otherwise, LED can be damaged.
- 3) The thermal pad is electrically connected to the cathode contact pads.
- 4) Precautions
 - ① The pressure on the LEDs will influence to the reliability of the LEDs. Precautions should be taken to avoid the strong pressure on the LEDs. Do not put stress on the LEDs during heating.
 - ② Re-soldering should not be done after the LEDs have been soldered. If re-soldering is unavoidable, LED's characteristics should be carefully checked before and after such repair.
 - ③ Do not stack assembled PCBs together. Since materials of LEDs is soft, abrasion between two PCB assembled with LED might cause catastrophic failure of the LEDs.

6. Reliability Test Items & Conditions

1) Test Items and Results

Test Item	Test Conditions	Test Hours/Cycles	Sample No	
MSL Test	125°C, 24hrs → 60°C, 60%RH, 120hrs → Peak 260±5°C, 220°C over time 60sec, 3 cycles	1 cycle	11	
Room Temperature Life Test	25°C±3°C, DC 150mA	1,000 hrs	22	
High Temperature Life Test	85°C±3°C, DC 150mA	1,000 hrs	22	
High Temperature Humidity Life Test	85°C±3°C, 85%±2%RH, DC 150mA	1,000 hrs	22	
Low Temperature Life Test	-40°C±3°C, DC 150mA	1,000 hrs	22	
Powered Temperature Cycle Test	-45°C/20min ↔ 85°C/20min, Sweep 100min cycle on/off: each 5min, DC 150mA	100 cycles	22	
Thermal Shock	-45°C/15min ↔ 125°C/15min, → Hot plate 180°C	500 cycles	100	
High Temperature Storage	Ta=120°C±3°C	1,000 hrs	11	
Low Temperature Storage	Ta=-40°C±3°C	1,000 hrs	11	
ESD(HBM)		R1 : 10MΩ, R2 : 1.5KΩ, C : 100pF, V = ±5kV	5 times	5
ESD(MM)		R1 : 10MΩ, R2 : 0, C : 200pF, V = ±0.5kV	5 times	5
Vibration Test	20~2000~20 Hz 200 m/s ² , Sweep 4 min X, Y, Z 3 direction, each 1 cycle	4 cycles	11	
Mechanical Shock Test	1500G, 0.5ms,	5 cycles	11	

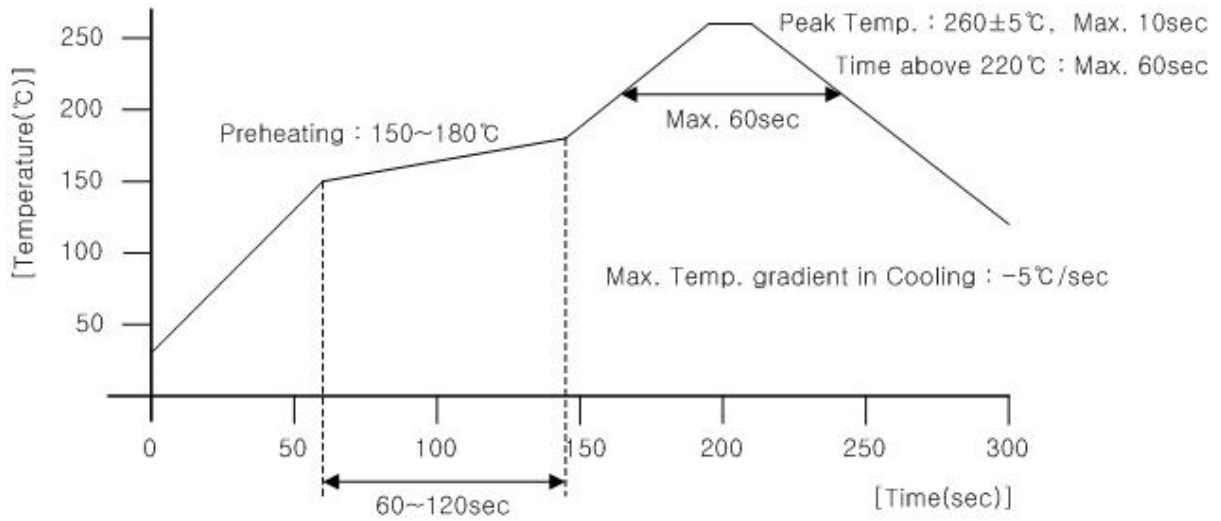
2) Criteria for Judging the Damage

Item	Symbol	Test Condition	Limit	
			Min	Max
Forward Voltage	V _F	I _F = 65 mA	Init. Value*0.9	Init. Value*1.1
Luminous Flux	Φ _v	I _F = 65 mA	Init. Value*0.7	Init. Value*1.1

7. Solder Conditions

1) Reflow Conditions (Pb Free)

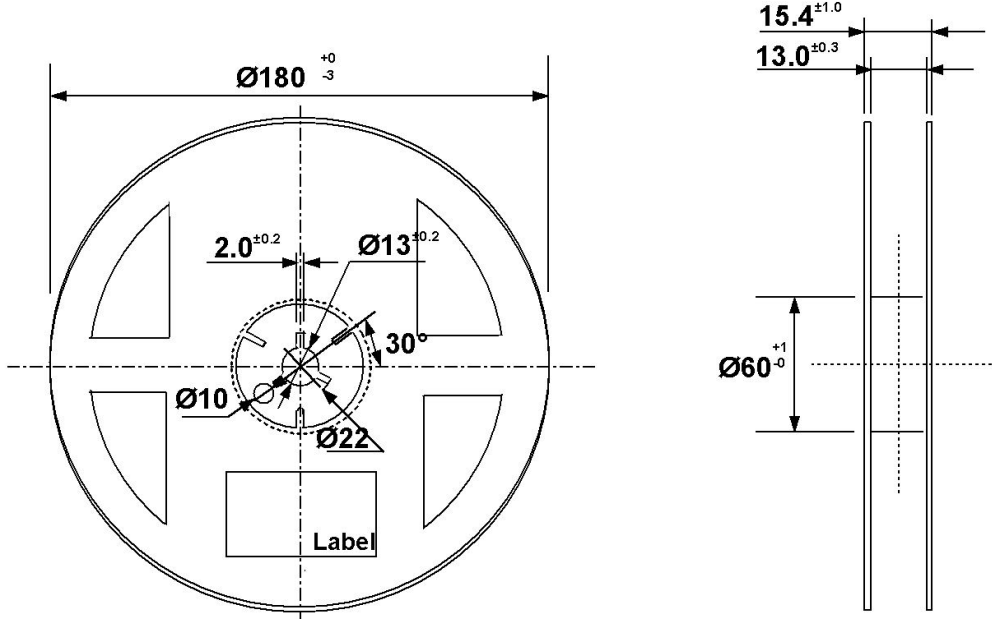
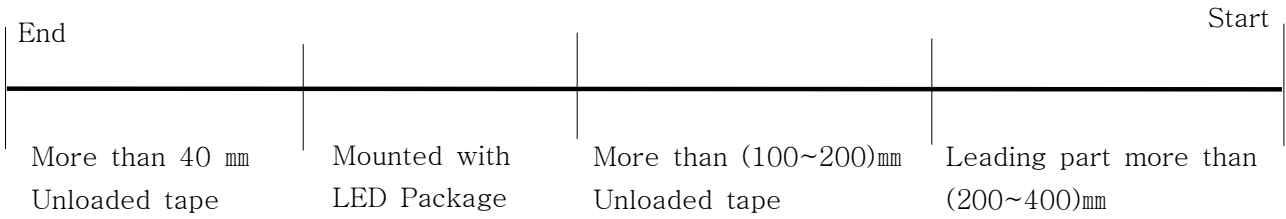
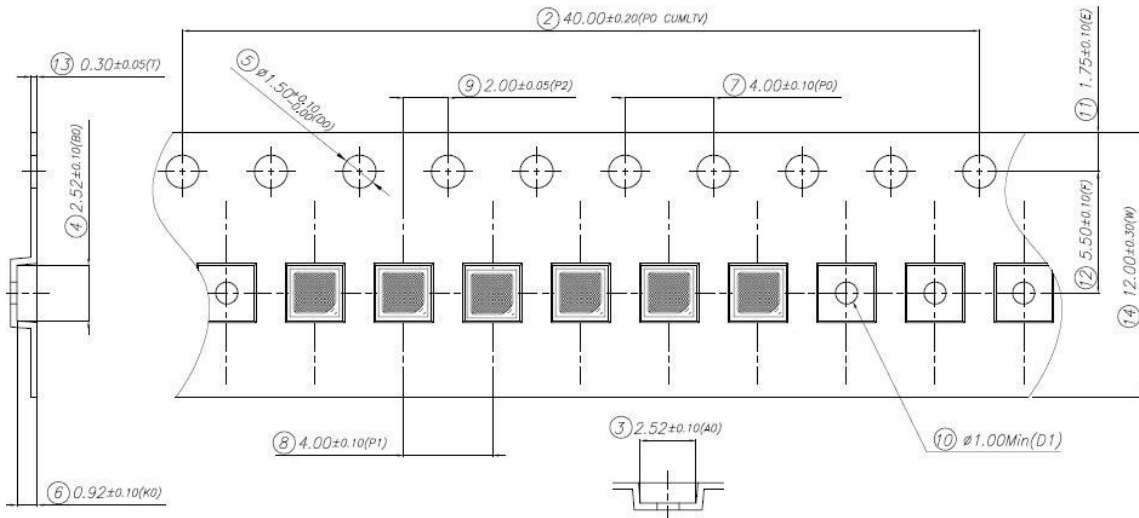
Reflow Frequency : 2 times max.



2) For Manual Soldering

Not more than 5 seconds @Max. 300°C, under soldering iron.

8. Tape & Reel

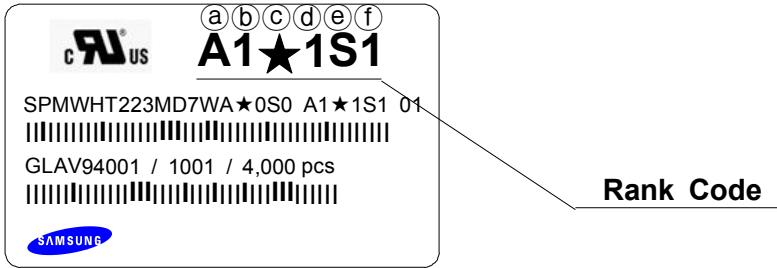


Tolerance ± 0.2 , Unit:mm

- (1) Quantity : The quantity/reel to be 4,000 pcs.
- (2) Cumulative Tolerance : Cumulative tolerance/10 pitches to be ± 0.2 mm
- (3) Adhesion Strength of Cover Tape : Adhesion strength to be 0.1-0.7 N when the cover tape is turned off from the carrier tape at 10 °C angle to be the carrier tape.
- (4) Packaging : P/N, Manufacturing data code no. and quantity to be indicated on a damp proof package.

9. Label Structure

1) Label Structure



N.B) Denoted rank is the only example.

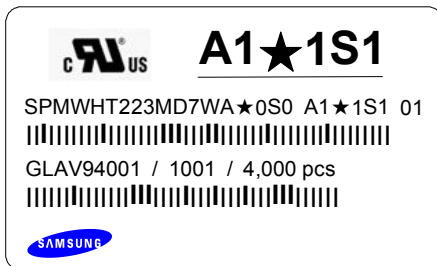
'★' means All kind of Chromaticity Coordinate Rank.

Rank Code

- ⒶⒷ : Forward Voltage(V_F) Rank
- ⒸⒹ : Chromaticity Coordinate Rank
- ⒺⒻ : Luminous Intensity(cd) Rank

2) LOT Number

The Lot number is composed of the following characters



①②③④⑤⑥⑦⑧⑨ / 1ⒶⒷⒸ / 4,000 PCS

- ① : Production site (S: Giheung, Korea, G: Tianjin, China)
- ② : L (LED)
- ③ : Product State (A:Normality, B:Bulk, C:First Production, R:Reproduction, S:Sample)
- ④ : Year (Y: 2014, Z: 2015, A:2016...)
- ⑤ : Month (1 ~ 9, A, B, C)
- ⑥ : Day (1 ~ 9, A, B ~ V)
- ⑦⑧⑨ : Product serial number (001 ~ 999)
- ⒶⒷⒸ : Reel Number (001 ~ 999)



10. Packing Structure

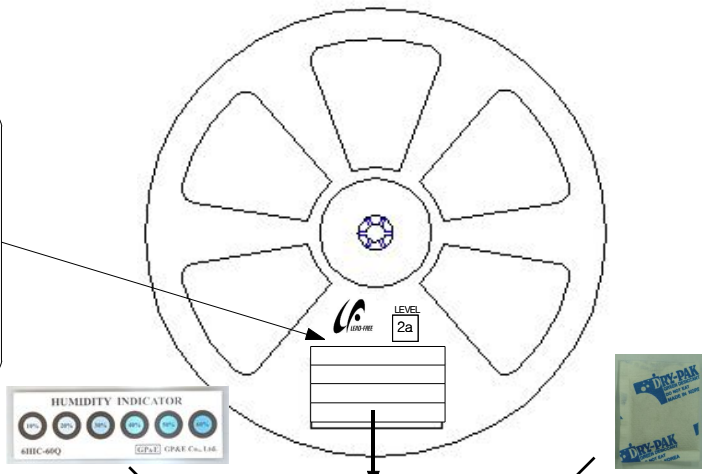
1) Packing Process

Reel

A1★1S1

SPMWHT223MD7WA★0S0 A1★1S1 01

GLAV94001 / 1001 / 4,000 pcs

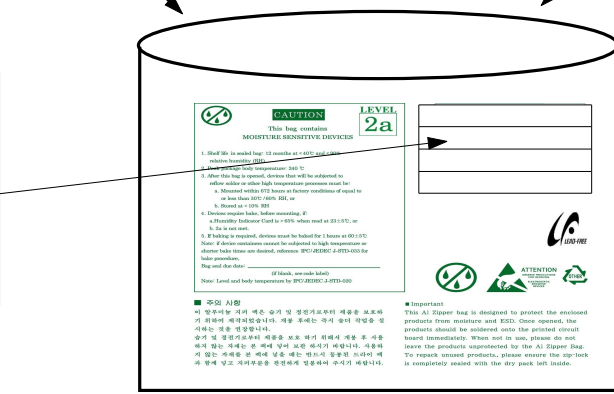


Aluminum Vinyl Bag

A1★1S1

SPMWHT223MD7WA★0S0 A1★1S1 01

GLAV94001 / 1001 / 4,000 pcs



Material : Paper(SW3B(B))

TYPE	SIZE(mm)			Reels/ box
	a	b	c	
7inch	245±5	220±5	182±5	Up to 10 Reels
	245±5	220±5	86±5	Up to 5 Reels

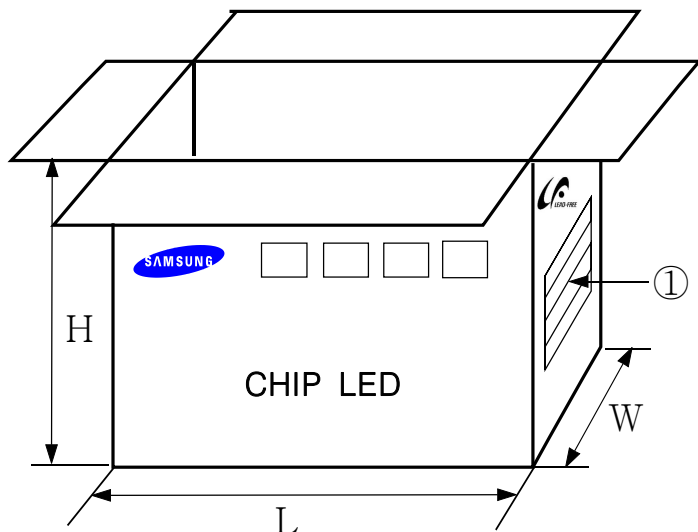
① SIDE

A1★1S1


SPMWHT223MD7WA★0S0 A1★1S1 01

GLAV94001 / 1001 / 40,000 pcs

[Box Label]



2) Aluminum Packing Bag



CAUTION

This bag contains
MOISTURE SENSITIVE DEVICES

LEVEL


2a

1. Shelf life in sealed bag: 12 months at <math>< 40^{\circ}\text{C}</math> and <math>< 90\%</math> relative humidity (RH)
2. Peak package body temperature: 240 °C
3. After this bag is opened, devices that will be subjected to reflow solder or other high temperature processes must be:
 - a. Mounted within 672 hours at factory conditions of equal to or less than 30°C / 60% RH, or
 - b. Stored at <math>< 10\%</math> RH
4. Devices require bake, before mounting, if:
 - a. Humidity Indicator Card is > 65% when read at 23±5°C, or
 - b. 2a is not met.
5. If baking is required, devices must be baked for 1 hours at 60±5°C

Note: if device containers cannot be subjected to high temperature or shorter bake times are desired, reference IPC/JEDEC J-STD-033 for bake procedure,

Bag seal due date: _____
(if blank, see code label)


Note: Level and body temperature by IPC/JEDEC J-STD-020





A1★1S1

SPMWHT223MD7WA★0S0 A1★1S1 01


GLAV94001 / 1001 / 4,000 pcs







ATTENTION

OBSERVE PRECAUTIONS
FOR HANDLING



ELECTROSTATIC
SENSITIVE
DEVICES



■ 주의 사항

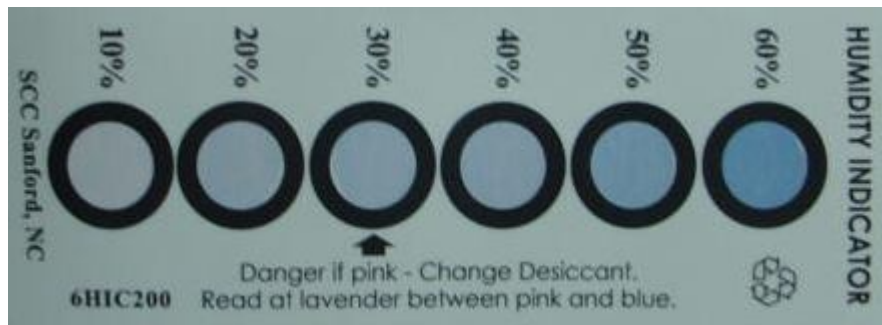
이 알루미늄 지퍼 백은 습기 및 정전기로부터 제품을 보호하기 위하여 제작되었습니다. 개봉 후에는 즉시 솔더 작업을 실시하는 것을 권장합니다.

습기 및 정전기로부터 제품을 보호 하기 위해서 개봉 후 사용하지 않는 자재는 본 팩에 넣어 보관 하시기 바랍니다. 사용하지 않는 자재를 본 팩에 넣을 때는 반드시 동봉된 드라이 팩과 함께 넣고 지퍼부분을 완전하게 밀봉하여 주시기 바랍니다.

■ Important

This Al Zipper bag is designed to protect the enclosed products from moisture and ESD. Once opened, the products should be soldered onto the printed circuit board immediately. When not in use, please do not leave the products unprotected by the Al Zipper Bag. To repack unused products., please ensure the zip-lock is completely sealed with the dry pack left inside.

Silica gel & Humidity Indicator Card in Aluminum Vinyl Bag



11. Kitting Rule

1) Kitting bin Concept – 2700K, 3000K, 3500K and 4000K

1. This item is included to ☆K models.
2. Under agreement between customer and SAMSUNG ELECTRONICS, SAMSUNG can supply kitting bin(V_F , Color, lm).
3. A forward voltage(V_F) of kitting bin is combined by a pair of same V_F rank such as (A1+A1), (A2+A2), (A3+A3), (A4+A4) or (AZ+AZ).
4. A Chromaticity Coordinates of kitting bin is mixed by kitting procedure.(below kitting simulation)
Especially, one of 1, 2, 3, or 4 rank can be mixed with other rank, or can be used alone.
5. A luminous flux(lm) is average by kitting procedure.(below kitting simulation)
For example Kitting lm is average S1 and S2 [Kitting $lm = (S1+S2)/2$]
6. '□' means one of the W(2700K), V(3000K), U(3500K) and T(4000K) a segment of the CCT rank.

[Kitting example]

Target

D	E	F	G
9	A	B	C
5	6	7	8
1	2	3	4

User can get the green box position by kitting combination.

Kitting Combination : +

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