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With the principle of "Quality Parts,Customers Priority,Honest Operation,and Considerate Service",our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

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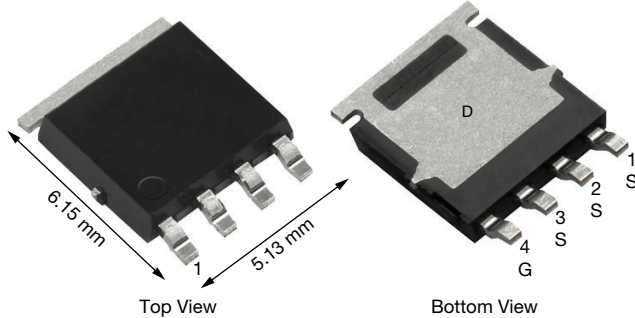
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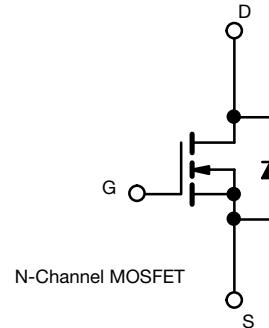


Automotive N-Channel 60 V (D-S) 175 °C MOSFET

PowerPAK® SO-8L Single

FEATURES

- TrenchFET® power MOSFET
- AEC-Q101 qualified
- 100 % R_g and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

AUTOMOTIVE GRADE


RoHS
 COMPLIANT
 HALOGEN
FREE


N-Channel MOSFET

PRODUCT SUMMARY	
V _{DS} (V)	60
R _{DS(on)} (Ω) at V _{GS} = 10 V	0.0125
R _{DS(on)} (Ω) at V _{GS} = 4.5 V	0.0160
I _D (A)	30
Configuration	Single
Package	PowerPAK SO-8L

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V _{DS}	60	V
Gate-Source Voltage		V _{GS}	± 20	
Continuous Drain Current	T _C = 25 °C ^a	I _D	30	A
	T _C = 125 °C		24.6	
Continuous Source Current (Diode Conduction) ^a		I _S	30	
Pulsed Drain Current ^b		I _{DM}	84	
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	23	
Single Pulse Avalanche Energy		E _{AS}	26	
Maximum Power Dissipation ^b	T _C = 25 °C	P _D	45	W
	T _C = 125 °C		15	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +175	°C
Soldering Recommendations (Peak Temperature) ^{d, e}			260	

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount ^c	R _{thJA}	70	°C/W
Junction-to-Case (Drain)		R _{thJC}	3.3	

Notes

- Package limited.
- Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
- When mounted on 1" square PCB (FR4 material).
- See solder profile (www.vishay.com/doc?73257). The PowerPAK SO-8L is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.



SPECIFICATIONS ($T_C = 25\text{ }^\circ\text{C}$, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0, I_D = 250\text{ }\mu\text{A}$	60	-	-	V
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	1.5	2.0	2.5	
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$	-	-	± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{GS} = 0\text{ V}$ $V_{DS} = 60\text{ V}$	-	-	1	μA
		$V_{GS} = 0\text{ V}$ $V_{DS} = 60\text{ V}, T_J = 125\text{ }^\circ\text{C}$	-	-	50	
		$V_{GS} = 0\text{ V}$ $V_{DS} = 60\text{ V}, T_J = 175\text{ }^\circ\text{C}$	-	-	150	
On-State Drain Current ^a	$I_{D(on)}$	$V_{GS} = 10\text{ V}$ $V_{DS} \geq 5\text{ V}$	30	-	-	A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 10\text{ V}$ $I_D = 8\text{ A}$	-	0.0101	0.0125	Ω
		$V_{GS} = 4.5\text{ V}$ $I_D = 6\text{ A}$	-	0.0131	0.0160	
		$V_{GS} = 10\text{ V}$ $I_D = 8\text{ A}, T_J = 125\text{ }^\circ\text{C}$	-	-	0.0202	
		$V_{GS} = 10\text{ V}$ $I_D = 8\text{ A}, T_J = 175\text{ }^\circ\text{C}$	-	-	0.0249	
Forward Transconductance ^b	g_{fs}	$V_{DS} = 15\text{ V}, I_D = 8\text{ A}$	-	46	-	S
Dynamic ^b						
Input Capacitance	C_{iss}	$V_{GS} = 0\text{ V}$ $V_{DS} = 25\text{ V}, f = 1\text{ MHz}$	-	1195	1600	μF
Output Capacitance	C_{oss}		-	506	660	
Reverse Transfer Capacitance	C_{rss}		-	31	45	
Total Gate Charge ^c	Q_g	$V_{GS} = 10\text{ V}$ $V_{DS} = 30\text{ V}, I_D = 4\text{ A}$	-	18	30	nC
Gate-Source Charge ^c	Q_{gs}		-	3.4	-	
Gate-Drain Charge ^c	Q_{gd}		-	2.5	-	
Gate Resistance	R_g	$f = 1\text{ MHz}$	0.2	0.47	0.8	Ω
Turn-On Delay Time ^c	$t_{d(on)}$	$V_{DD} = 30\text{ V}, R_L = 7.5\text{ }\Omega$ $I_D \cong 4\text{ A}, V_{GEN} = 10\text{ V}, R_g = 1\text{ }\Omega$	-	9	15	ns
Rise Time ^c	t_r		-	5	10	
Turn-Off Delay Time ^c	$t_{d(off)}$		-	21	35	
Fall Time ^c	t_f		-	5	10	
Source-Drain Diode Ratings and Characteristics ^b						
Pulsed Current ^a	I_{SM}		-	-	84	A
Forward Voltage	V_{SD}	$I_F = 8\text{ A}, V_{GS} = 0$	-	0.82	1.2	V

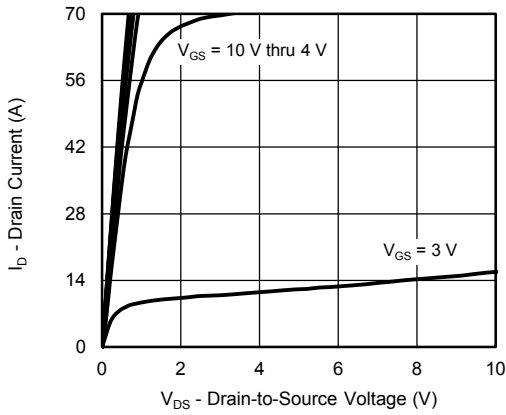
Notes

- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

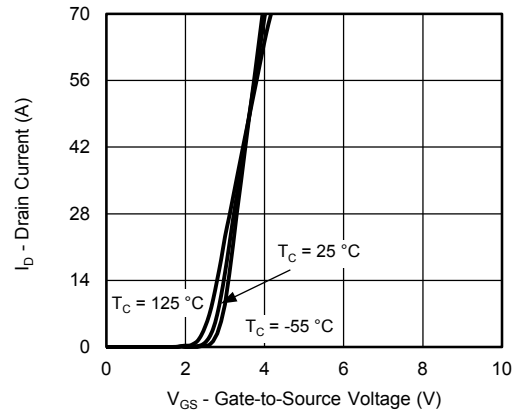
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



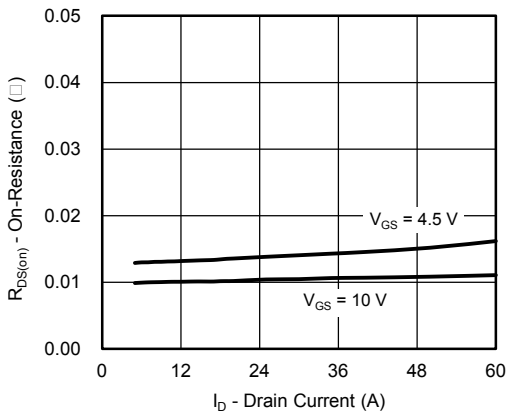
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



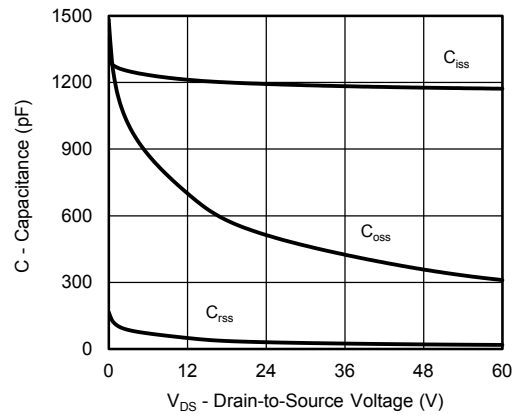
Output Characteristics



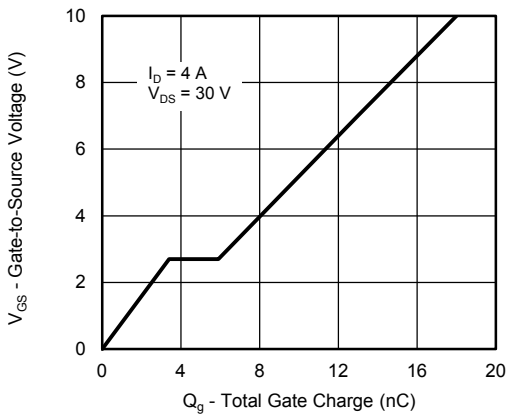
Transfer Characteristics



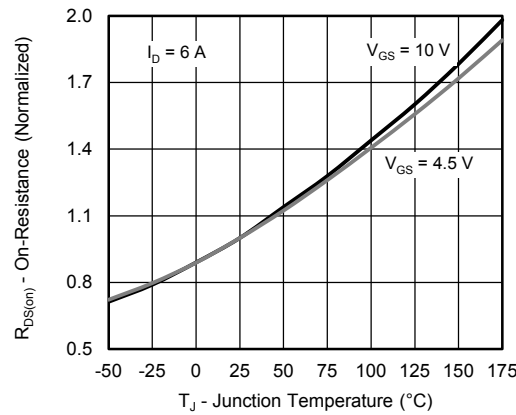
On-Resistance vs. Drain Current



Capacitance



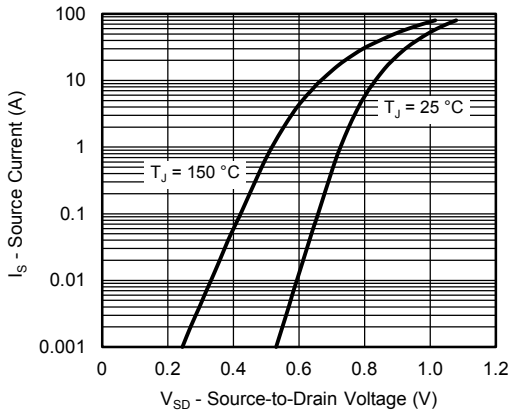
Gate Charge



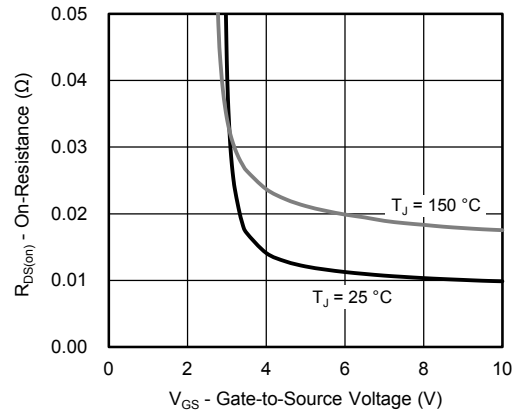
On-Resistance vs. Junction Temperature



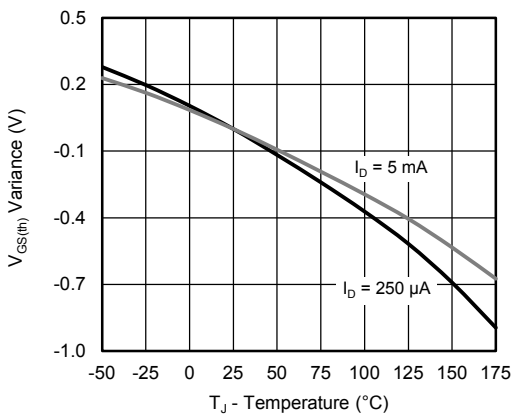
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



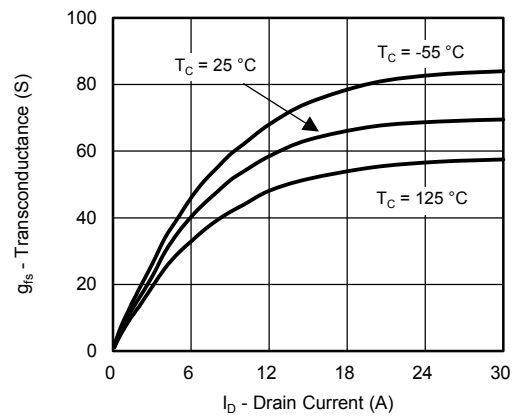
Source Drain Diode Forward Voltage



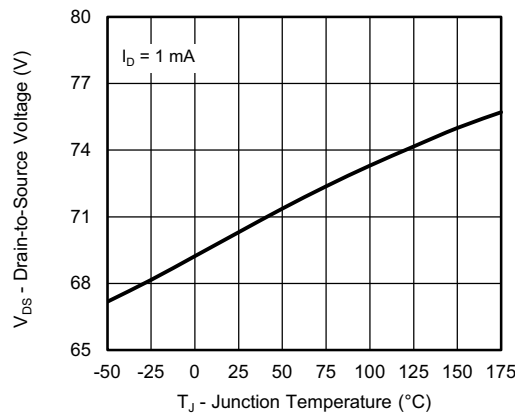
On-Resistance vs. Gate-to Source Voltage



Threshold Voltage

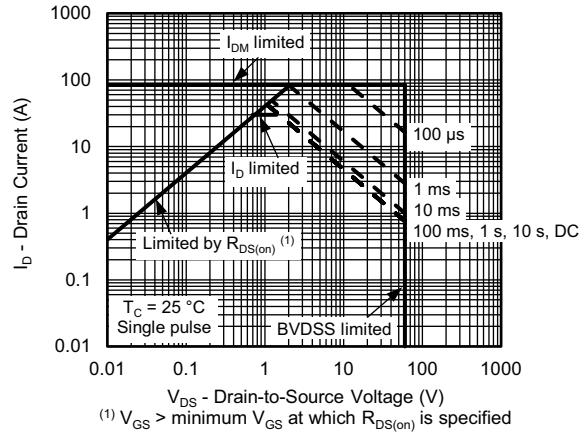


Transconductance

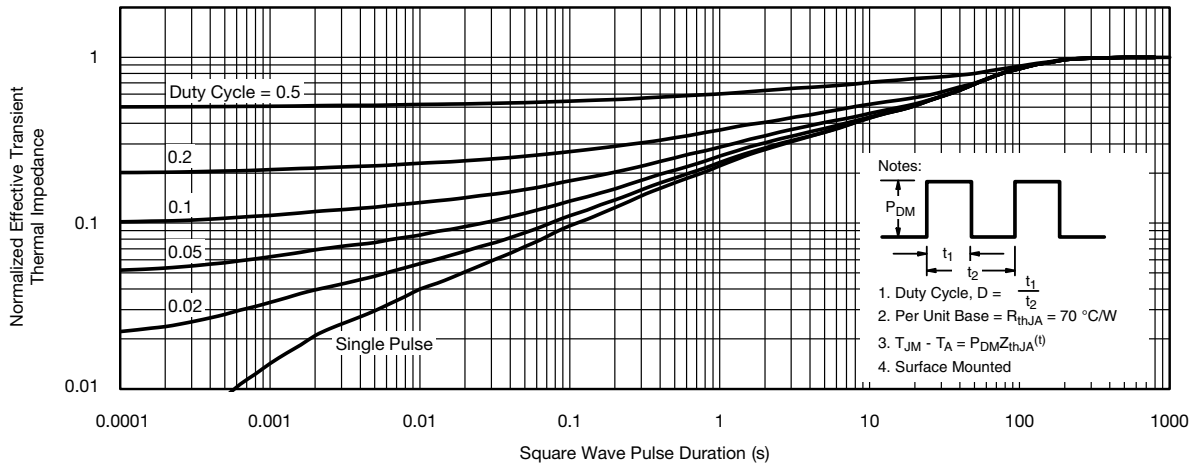


Drain Source Breakdown vs. Junction Temperature

TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



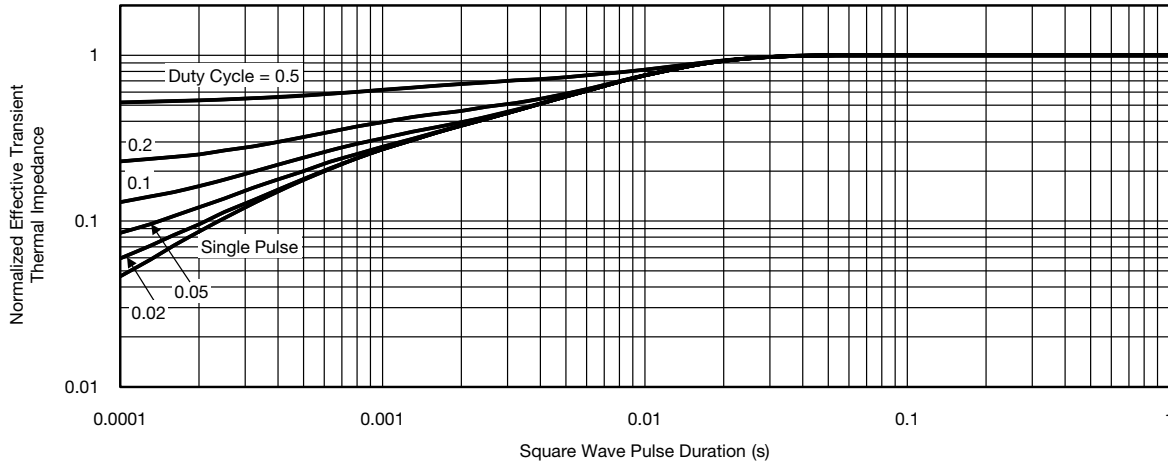
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Case (25 °C)
- are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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