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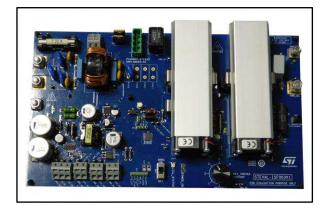




STEVAL-ISF003V1

Low standby losses power front-end with inrush current-limitation

Data brief



Features

- Fully electronic solution without the need for an inrush current limiter resistor and its bulky bypass electromechanical relay
- DC bus disconnection at standby to reduce losses below 0.3 W
- Inrush current compliance with IEC61000-3-3
- Operation allowed with PFC (continuous or discontinuous)
- Compliant with EN 55015, IEC 61000-4-11
- Criteria A at 4 kV IEC 61000-4-5 and at 4 kV IEC 61000-4-4
- RoHS compliant

Description

The STEVAL-ISF003V1 evaluation board allows the inrush-current which charges a DC bus capacitor to be limited to comply with the IEC 61000-3-3 standard. This inrush-current limitation is based on a soft-start procedure of the mixed bridge diodes and SCRs rectifier using progressive phase control at board start-up.

This solution can also drastically reduce standby losses as the DC bus can be totally disconnected from the AC mains when it does not have to operate. DC bus deactivation is simply achieved by turning off SCRs, without requiring an additional relay to open the circuit in standby.

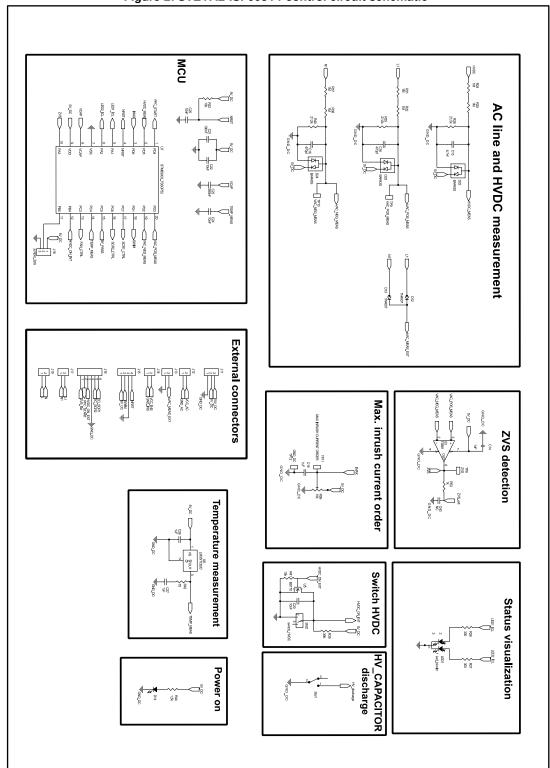
The steady-state losses are also reduced, thanks to the removal of the NTC / PTC resistor traditionally used to limit inrush-current. Therefore, no relay is required to bypass this resistor as it is no longer used.

Schematic diagrams STEVAL-ISF003V1

Schematic diagrams

Figure 1: STEVAL-IFS003V1 power and insulated control schematic SCRs gate control AC power side Fan control STBR6012 \(\text{D10} 250k R54 250k HVDC TP2 **₩** R8 250k R7 250k 1 5

Figure 2: STEVAL-ISF003V1 control circuit schematic



DC Power Supply + C34 TP18 H 100nF D21 D1N4148 1 C4 TP15 15V_DC GND_DC TP21 GND_DC

Figure 3: STEVAL-ISF003V1 flyback SMPS schematic

STEVAL-ISF003V1 Revision history

Revision history

Table 1: Document revision history

Date	Version	Changes
16-Jun-2016	1	Initial release.
18-Apr-2017	2	Updated Section: "Schematic diagrams".

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