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STGF20NB60S

PowerMESH™ IGBT, S series 600 V, 13 A low drop

Datasheet - production data

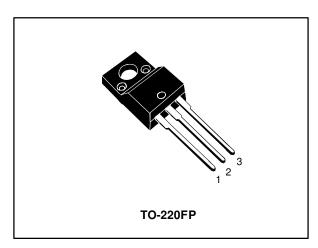
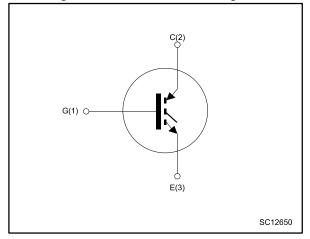


Figure 1: Internal schematic diagram



Features

- Low on-voltage drop (V_{CE(sat)})
- High current capability

Applications

- Light dimmer
- Static relays
- Motor control

Description

Using the latest high voltage technology based on a patented strip layout, STMicroelectronics has designed an advanced family of IGBTs, the PowerMESH™ IGBTs, with outstanding performance. The suffix "S" represents a series optimized to achieve minimum on-voltage drop for low frequency applications.

Table 1: Device summary

Order code	Marking	Package	Packing	
STGF20NB60S	GF20NB60S	TO-220FP	Tube	

Contents STGF20NB60S

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STGF20NB60S Electrical ratings

1 Electrical ratings

Table 2: Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{CES}	Collector-emitter voltage (V _{GE} = 0 V)	600	V
V _{ECS}	Emitter-collector voltage (V _{GE} = 0 V)	-20	V
V _{GE}	Gate-emitter voltage	±20	V
	Continuous collector current at T _C = 25 °C	24	Δ.
lc	Continuous collector current at T _C = 100 °C	13	Α
Icr	Turn-off latching current	70	Α
I _{CM} ⁽¹⁾	Pulsed collector current	70	Α
Ртот	Total dissipation at T _C = 25 °C	40	W
V _{ISO}	Insulation withstand voltage (RMS) from all three leads to external heat sink (t = 1 s, T_C = 25 °C)	2.5	kV
T _{STG}	Storage temperature range	55 to 150	°C
TJ	Operating junction temperature	-55 to 150	-0

Notes:

Table 3: Thermal data

Symbol	Parameter	Value	Unit
R _{thj-case}	Thermal resistance junction-case	3.1	000
R _{thj-amb}	Thermal resistance junction-ambient	62.5	°C/W

 $[\]ensuremath{^{(1)}}\mbox{Pulse}$ width limited by safe operating area.

Electrical characteristics STGF20NB60S

2 Electrical characteristics

T_C = 25 °C unless otherwise specified

Table 4: Static characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
$V_{(BR)CES}$	Collector-emitter breakdown voltage	$V_{GE} = 0 \text{ V}, I_{C} = 250 \mu\text{A}$	600			V
V _{(BR)ECS}	Emitter-collector breakdown voltage	$V_{GE} = 0 \text{ V}, I_{C} = 10 \text{ mA}$	-20			V
		$V_{GE} = 0 \text{ V}, V_{CE} = 600 \text{ V}$			10	
Ices	Collector cut-off current	V _{GE} = 0 V, V _{CE} = 600 V, T _C = 125 °C			100	μΑ
Iges	Gate-emitter leakage current	$V_{CE} = 0 \text{ V}, V_{GE} = \pm 20 \text{ V}$			±100	nA
$V_{\text{GE(th)}}$	Gate threshold voltage	$V_{CE} = V_{GE}$, $I_C = 250 \mu A$	2.5		5	V
	Collector-emitter saturation	$V_{GE} = 15 \text{ V}, I_{C} = 20 \text{ A}$		1.25	1.7	
V _{CE(sat)}	voltage	V _{GE} = 15 V, I _C = 20 A, T _J = 150 °C		1.2		V

Table 5: Dynamic characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
gfs ⁽¹⁾	Forward transconductance	V _{CE} = 10 V, I _C = 8 A	-	20	1	S
Cies	Input capacitance		-	1820	1	
Coes	Output capacitance	V _{CE} = 25 V, f = 1 MHz, V _{GE} = 0 V V _{CC} = 480 V, I _C = 20 A, V _{GE} = 15 V (see <i>Figure 17</i> :	-	167	-	рF
C _{res}	Reverse transfer capacitance		-	27	-	ρι I
Qg	Total gate charge		-	83	115	
Qge	Gate-emitter charge		-	10		nC
Q _{gc}	Gate-collector charge	"Gate charge test circuit")	-	27	-	

Notes:

Table 6: Inductive load switching on characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t _{d(on)}	Turn-on delay time	V _{CC} = 480 V, I _C = 20 A,	-	92	1	ns
t _r	Current rise time	V_{GE} = 15 V, R_{G} = 100 Ω (see Figure 16: "Test circuit for inductive load switching")	1	70	ı	ns
(di/dt) _{on}	Turn-on current slope		1	340	ı	A/μs
t _{d(on)}	Turn-on delay time	Vcc = 480 V, Ic = 20 A, VgE = 15 V, Rg = 100 Ω, T _j = 125 °C (see Figure 16: "Test circuit for inductive load switching")	-	80	-	ns
tr	Current rise time		-	73	1	ns
(di/dt) _{on}	Turn-on current slope		1	320	ı	A/μs

 $^{^{(1)}\}text{Pulse}$ duration= 300 $\mu\text{s},$ duty cycle 1.5 %

Table 7: Inductive load switching off characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
tc	Cross-over time	V _{CC} = 480 V, I _C = 20 A,	1	1.6	-	
$t_r(V_{off})$	Off voltage rise time	$V_{GE} = 480 \text{ V}, R_{G} = 20 \text{ A},$ $V_{GE} = 15 \text{ V}, R_{G} = 100 \Omega$	1	0.8	-	20
t _{d(off)}	Turn-off delay time	(see Figure 16: "Test circuit for inductive load switching")	ı	1.1	-	ns
tf	Current fall time		1	0.8	-	
tc	Cross-over time	V_{CC} = 480 V, I_{C} = 20 A, V_{GE} = 15 V, R_{G} = 100 Ω , T_{j} = 125 °C (see Figure 16: "Test circuit for inductive load switching")	1	2.4	-	
$t_r(V_{off})$	Off voltage rise time		-	1.1	-	20
t _{d(off)}	Turn-off delay time		-	2.4	-	ns
tf	Current fall time		-	1.2	-	

Table 8: Inductive load switching loss characteristics

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Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
E _{on} (1)	Turn-on switching loss	Vcc = 480 V, Ic = 20 A, VgE = 15 V, Rg = 100 Ω (see Figure 18: "Switching waveform") Vcc = 480 V, Ic = 20 A, VgE = 15 V, Rg = 100 Ω , T _j = 125 °C (see Figure 18: "Switching waveform")	-	0.84	1	
E _{off} (2)	Turn-off switching loss		-	7.4	1	mJ
Ets	Total switching loss		-	8.24	-	
E _{on} (1)	Turn-on switching loss		-	0.86	-	
E _{off} ⁽²⁾	Turn-off switching loss		-	11.5	-	mJ
Ets	Total switching loss		-	12.36	-	

Notes:

⁽¹⁾E_{on} is the turn-on loss when a external diode is used in the test circuit in *Figure 16: "Test circuit for inductive load switching"*.

⁽²⁾Turn-off loss includes the tail of the collector current.

2.1 Electrical characteristics (curves)

Figure 2: Output Characteristics (A) V_{CE}= 15V/ 13 V 200 150 10 V 100 9 V 8 V 50 7 V 6 V 10 20 15 $\overline{V}_{CE}(V)$

Figure 3: Transfer Characteristics

IC
(A)
200
V_{CE}= 25V

150
100
0 3 6 9 12 V_{GE}(V)

Figure 5: Normalized collector-emitter on voltage vs temperature

V_{CE(sat)} GF20NB60SOVT (norm)

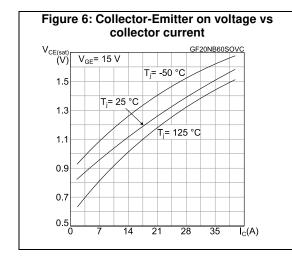
1.15

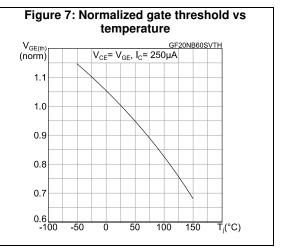
1.10

1.00

0.95

0.90
-100 -50 0 50 100 150 T_j(°C)





STGF20NB60S Electrical characteristics

T_j(°C)

Figure 8: Normalized breakdown voltage vs temperature

V_{(BR)CES}
(V)
1.10
1.05
1.00

50

100 150

0.95

0.90

0.85 -100

Figure 9: Gate charge vs gate-emitter voltage

V_{GE}
(V)
V_{CC}= 480 V, I_C= 20 A

12

8

4

0

0

20

40

60

80

100

Q_g(nC)

Figure 10: Capacitance variations

C(pF)

V_{GE}= 0 V, f = 1MHz

2000

C_{ISS}

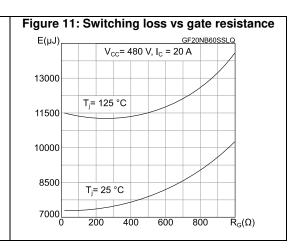
1500

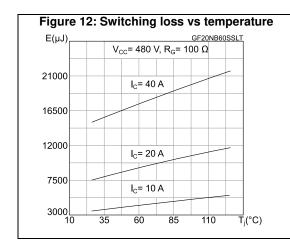
1000

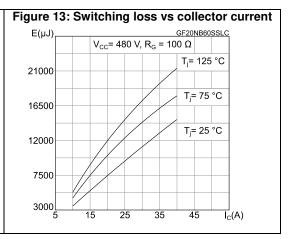
C_{OSS}

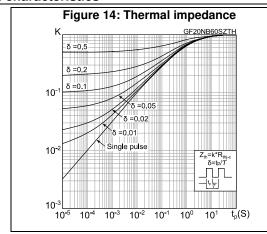
C_{RSS}

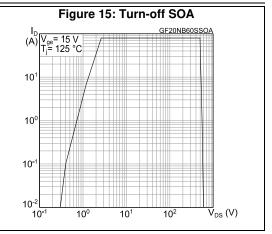
0 10 20 30 40 V_{CE}(V)





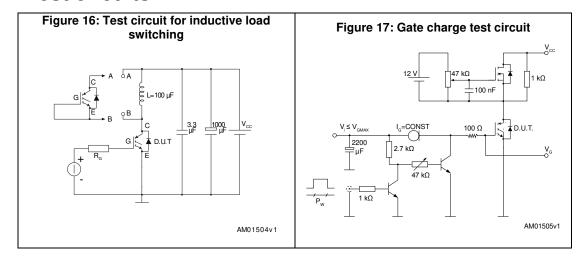


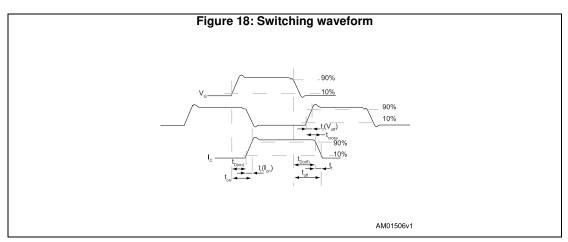




STGF20NB60S Test circuits

3 Test circuits





4 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: **www.st.com**. ECOPACK® is an ST trademark.

STGF20NB60S Package information

4.1 TO-220FP package information

Figure 19: TO-220FP package outline

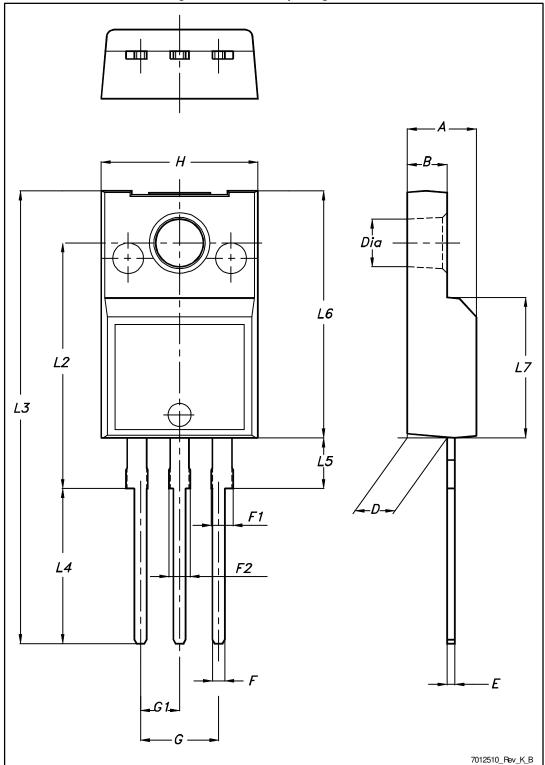


Table 9: TO-220FP package mechanical data

Dim		mm	
Dim.	Min.	Тур.	Max.
A	4.4		4.6
В	2.5		2.7
D	2.5		2.75
Е	0.45		0.7
F	0.75		1
F1	1.15		1.70
F2	1.15		1.70
G	4.95		5.2
G1	2.4		2.7
Н	10		10.4
L2		16	
L3	28.6		30.6
L4	9.8		10.6
L5	2.9		3.6
L6	15.9		16.4
L7	9		9.3
Dia	3		3.2

STGF20NB60S Revision history

5 Revision history

Table 10: Document revision history

Date	Revision	Changes
17-Dec-2004	2	New template, no content change
05-Aug-2005	3	Some values changed in table 6
02-Dec-2015	4	Text and formatting changes throughout document On cover page: - updated Title, Features and Description Added Electrical ratings section heading In section Electrical ratings: - updated tables Absolute Maximum ratings and Thermal Data In section Electrical characteristics: - updated table Static characteristics Added section Package information Updated TO-220FP package information

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