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## STGP10M65DF2

# Trench gate field-stop IGBT, M series 650 V, 10 A low-loss in TO-220 package

Datasheet - production data

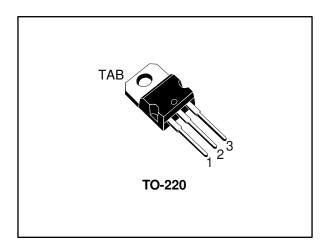
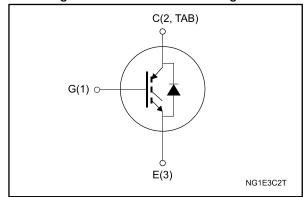


Figure 1: Internal schematic diagram



#### **Features**

- 6 µs of short-circuit withstand time
- V<sub>CE(sat)</sub> = 1.55 V (typ.) @ I<sub>C</sub> = 10 A
- Tight parameter distribution
- Safer paralleling
- Positive V<sub>CE(sat)</sub> temperature coefficient
- Low thermal resistance
- Soft and very fast recovery antiparallel diode
- Maximum junction temperature: T<sub>J</sub> = 175 °C

#### **Applications**

- Motor control
- UPS
- PFC
- General purpose inverter

#### Description

This device is an IGBT developed using an advanced proprietary trench gate field-stop structure. The device is part of the M series IGBTs, which represent an optimal balance between inverter system performance and efficiency where low-loss and short-circuit functionality are essential. Furthermore, the positive  $V_{\text{CE(sat)}}$  temperature coefficient and tight parameter distribution result in safer paralleling operation.

Table 1: Device summary

Order code	Marking	Package	Packing	
STGP10M65DF2	G10M65DF2	TO-220	Tube	

Contents STGP10M65DF2

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STGP10M65DF2 Electrical ratings

# 1 Electrical ratings

Table 2: Absolute maximum ratings

Symbol	Parameter Valu		Unit
Vces	Collector-emitter voltage (V <sub>GE</sub> = 0 V)	650	V
1-	Continuous collector current at T <sub>C</sub> = 25 °C	20	Α
lc	Continuous collector current at T <sub>C</sub> = 100 °C	10	A
ICP <sup>(1)</sup>	Pulsed collector current	40	Α
$V_{GE}$	Gate-emitter voltage	±20	V
l <sub>F</sub>	Continuous forward current at T <sub>C</sub> = 25 °C	20	Α
IF	Continuous forward current at T <sub>C</sub> = 100 °C	10	A
I <sub>FP</sub> <sup>(1)</sup>	Pulsed forward current		Α
Ртот	Total dissipation at T <sub>C</sub> = 25 °C	115	W
T <sub>STG</sub>	T <sub>STG</sub> Storage temperature range - 55 to 150		°C
$T_J$	Operating junction temperature range	- 55 to 175	J

#### Notes:

Table 3: Thermal data

	Symbol	Parameter	Value	Unit
Ī	RthJC	Thermal resistance junction-case IGBT	1.3	
Ī	RthJC	Thermal resistance junction-case diode	2.08	°C/W
ĺ	RthJA	Thermal resistance junction-ambient	62.5	

 $<sup>\</sup>ensuremath{^{(1)}}\mbox{Pulse}$  width limited by maximum junction temperature.

Electrical characteristics STGP10M65DF2

## 2 Electrical characteristics

T<sub>C</sub> = 25 °C unless otherwise specified

**Table 4: Static characteristics** 

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V <sub>(BR)CES</sub>	Collector-emitter breakdown voltage	$V_{GE} = 0 \text{ V}, I_{C} = 250  \mu\text{A}$	650			V
		$V_{GE} = 15 \text{ V}, I_{C} = 10 \text{ A}$		1.55	2.0	
V <sub>CE(sat)</sub>	V <sub>CE(sat)</sub> Collector-emitter saturation voltage	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 10 A, T <sub>J</sub> = 125 °C		1.9		V
		V <sub>GE</sub> = 15 V, I <sub>C</sub> = 10 A, T <sub>J</sub> = 175 °C		2.1		
		I <sub>F</sub> = 10 A		1.5	2.25	
V <sub>F</sub>	Forward on-voltage	I <sub>F</sub> = 10 A, T <sub>J</sub> = 125 °C		1.3		V
		I <sub>F</sub> = 10 A, T <sub>J</sub> = 175 °C		1.2		
$V_{\text{GE(th)}}$	Gate threshold voltage	$V_{CE} = V_{GE}$ , $I_C = 250 \mu A$	5	6	7	V
Ices	Collector cut-off current	V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 650 V			25	μΑ
I <sub>GES</sub>	Gate-emitter leakage current	V <sub>CE</sub> = 0 V, V <sub>GE</sub> = ±20 V			±250	μΑ

**Table 5: Dynamic characteristics** 

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Cies	Input capacitance		-	840	-	
Coes	Output capacitance	V <sub>CE</sub> = 25 V, f = 1 MHz,	-	63	-	рF
Cres	Reverse transfer capacitance	V <sub>GE</sub> = 0 V	1	16	1	į
$Q_g$	Total gate charge	$V_{CC} = 520 \text{ V}, I_{C} = 10 \text{ A},$	ı	28	ı	
Qge	Gate-emitter charge	V <sub>GE</sub> = 0 to 15 V (see <i>Figure 30: " Gate charge</i>	1	6	ı	nC
$Q_{gc}$	Gate-collector charge	test circuit")	-	12	-	

Table 6: IGBT switching characteristics (inductive load)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t <sub>d(on)</sub>	Turn-on delay time			19	-	ns
tr	Current rise time			7.4	-	ns
(di/dt) <sub>on</sub>	Turn-on current slope	V <sub>CE</sub> = 400 V, I <sub>C</sub> = 10 A,		1086	-	A/μs
t <sub>d(off)</sub>	Turn-off-delay time	$V_{GE} = 400 \text{ V}, 10 = 10 \text{ A},$ $V_{GE} = 15 \text{ V}, R_{G} = 22 \Omega$		91	-	ns
t <sub>f</sub>	Current fall time	(see Figure 29: " Test circuit		92	1	ns
E <sub>on</sub> <sup>(1)</sup>	Turn-on switching energy	for inductive load switching")		0.12	-	mJ
E <sub>off</sub> (2)	Turn-off switching energy			0.27	-	mJ
Ets	Total switching energy			0.39	1	mJ
t <sub>d(on)</sub>	Turn-on delay time			18	-	ns
tr	Current rise time			9	1	ns
(di/dt) <sub>on</sub>	Turn-on current slope	V <sub>CE</sub> = 400 V, I <sub>C</sub> = 10 A,		890	-	A/μs
t <sub>d(off)</sub>	Turn-off-delay time	$V_{GE} = 15 \text{ V}, R_G = 22 \Omega,$ $T_{J} = 175 \text{ °C}$		90	1	ns
tf	Current fall time	(see Figure 29: " Test circuit		170	1	ns
E <sub>on</sub> <sup>(1)</sup>	Turn-on switching energy	for inductive load switching")		0.26	-	mJ
E <sub>off</sub> (2)	Turn-off switching energy			0.4	-	mJ
E <sub>ts</sub>	Total switching energy			0.66	1	mJ
	Short-circuit withstand time	V <sub>CC</sub> ≤ 400 V, V <sub>GE</sub> = 13 V, T <sub>Jstart</sub> = 150 °C	10		-	μs
t <sub>sc</sub>	Short-circuit withstand time	V <sub>CC</sub> ≤ 400 V, V <sub>GE</sub> = 15 V, T <sub>Jstart</sub> = 150 °C	6		-	μs

#### Notes:

Table 7: Diode switching characteristics (inductive load)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
trr	Reverse recovery time		ı	96	1	ns
Qrr	Reverse recovery charge	$I_F = 10 \text{ A}, V_R = 400 \text{ V},$	-	373	-	nC
Irrm	Reverse recovery current	V <sub>GE</sub> = 15 V, di/dt = 1000 A/μs	ı	13	1	Α
dl <sub>rr</sub> /dt	Peak rate of fall of reverse recovery current during $t_{\text{b}}$	(see Figure 29: " Test circuit for inductive load switching")	ı	661	ı	A/μs
Err	Reverse recovery energy		ı	52	1	μJ
t <sub>rr</sub>	Reverse recovery time	1 10 4 1/ 100 1/	ı	201	ı	ns
Qrr	Reverse recovery charge	$I_F = 10 \text{ A}, V_R = 400 \text{ V}, V_{GE} = 15 \text{ V}.$	ı	1352	1	nC
I <sub>rrm</sub>	Reverse recovery current	di/dt = 1000 A/µs,	ı	19	1	Α
dl <sub>rr</sub> /dt	Peak rate of fall of reverse recovery current during t <sub>b</sub>	T <sub>J</sub> = 175 °C (see Figure 29: " Test circuit for inductive load switching")	ı	405	1	A/μs
Err	Reverse recovery energy	TOT ITIGUELIVE TOAU SWITCHING	ı	150	-	μJ

<sup>&</sup>lt;sup>(1)</sup>Including the reverse recovery of the diode.

 $<sup>\</sup>ensuremath{^{(2)}}\mbox{Including}$  the tail of the collector current.

## 2.1 Electrical characteristics (curves)

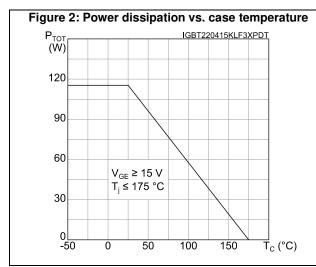
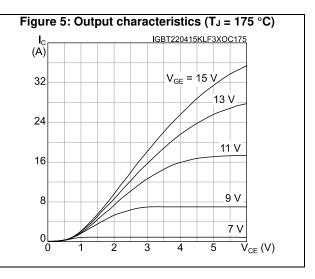
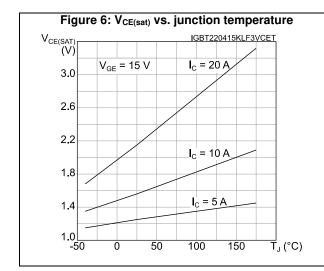


Figure 3: Collector current vs. case temperature  $I_{C}$  (A) 20 15 10  $V_{GE} \ge 15 \text{ V}$   $T_{J} \le 175 \,^{\circ}\text{C}$  15 10 150  $T_{C}(^{\circ}\text{C})$ 





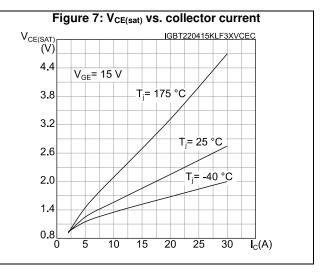
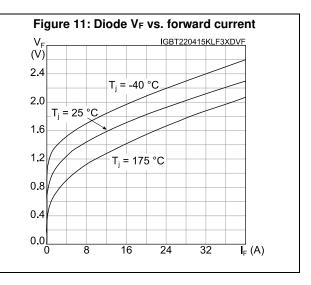
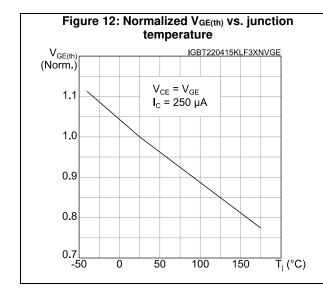
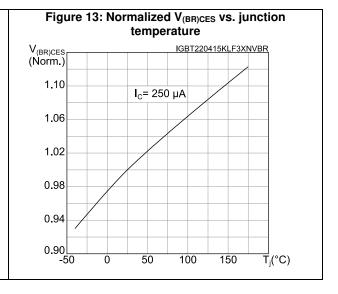
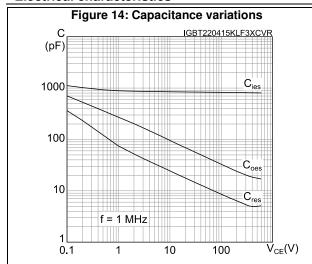


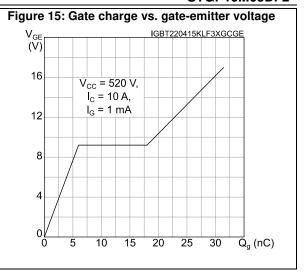
Figure 9: Forward bias safe operating area I<sub>C</sub> (A) I<sub>GBT220415KLF3XFSOA</sub>  $t_p$ =1  $\mu$ s  $t_p$ =10  $\mu$ s  $t_p$ =100  $\mu$ s  $t_p$ =100  $\mu$ s  $t_p$ =1 ms  $t_p$ =1 ms  $t_p$ =1 ms  $t_p$ =1 ms

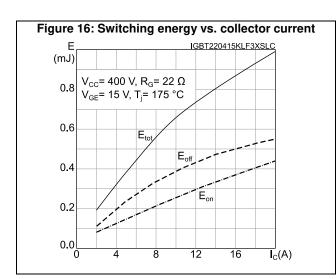


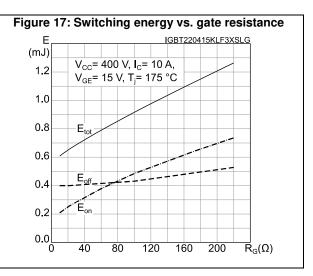


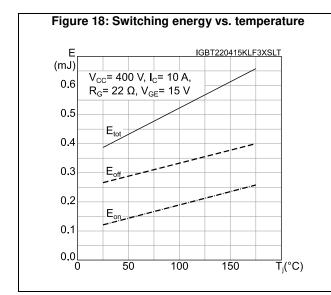


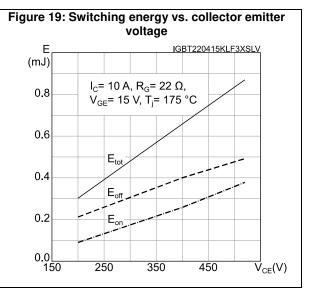






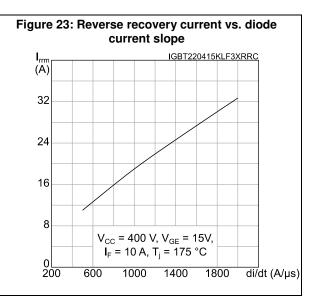


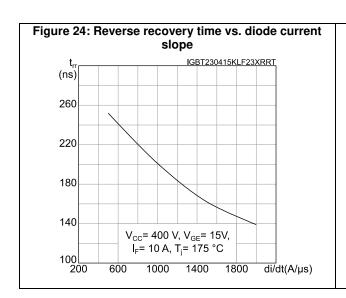


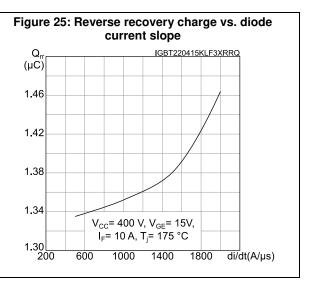


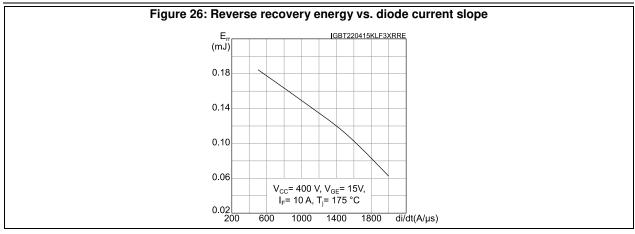
STGP10M65DF2 Electrical characteristics

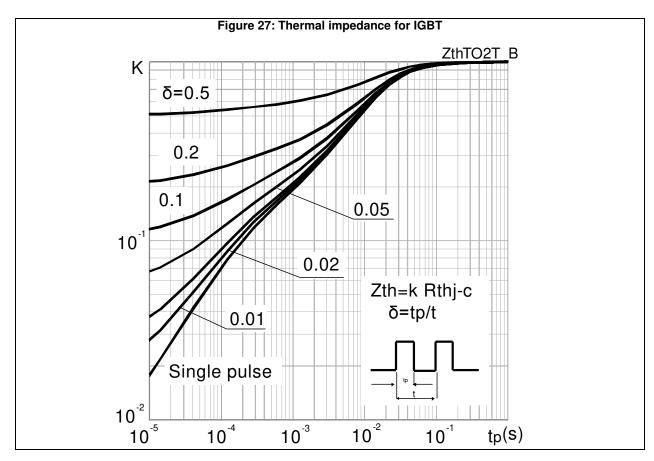
Figure 20: Short-circuit time and current vs. V<sub>GE</sub> IGBT220415KLF3XSCV Isc (A) t<sub>sc</sub> (µs) V<sub>CC</sub>≤ 400 V 20 65 T<sub>i</sub>≤ 150 °C 15 50 35 10 20 5 Isc 0L  $\overrightarrow{\mathsf{V}}_{\mathsf{GE}}(\mathsf{V})$ 12 13

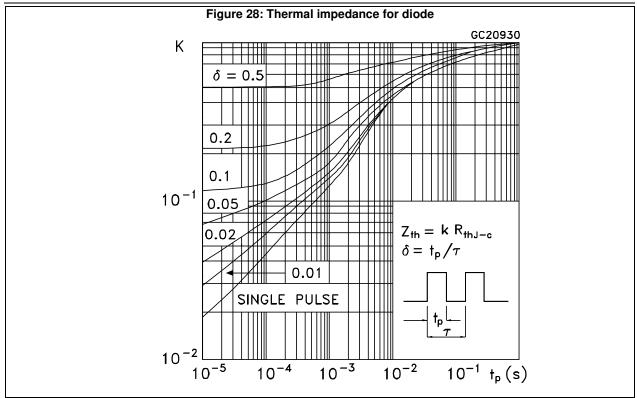






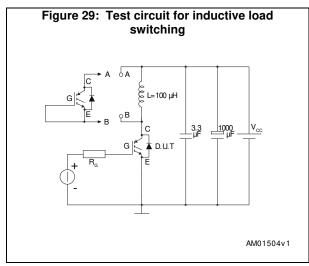


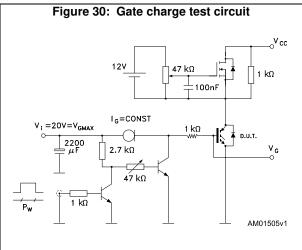


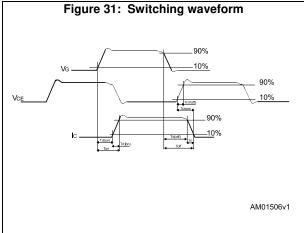


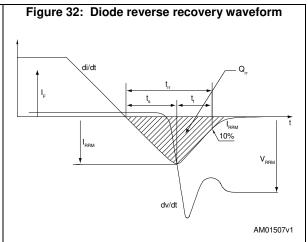
Test circuits STGP10M65DF2

## 3 Test circuits









STGP10M65DF2 Package information

# 4 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: **www.st.com**. ECOPACK® is an ST trademark.



# 4.1 TO-220 type A package information

Figure 33: TO-220 type A package outline

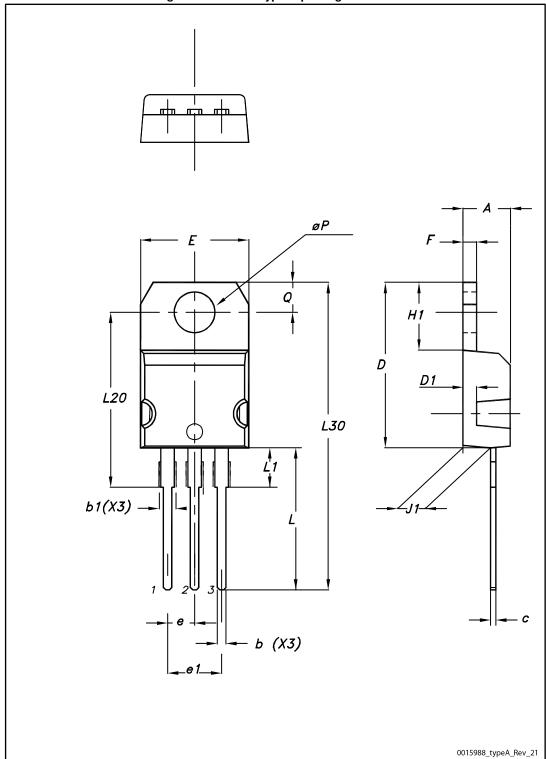


Table 8: TO-220 type A mechanical data

Dim.		mm	
Diiii.	Min.	Тур.	Max.
Α	4.40		4.60
b	0.61		0.88
b1	1.14		1.55
С	0.48		0.70
D	15.25		15.75
D1		1.27	
Е	10.00		10.40
е	2.40		2.70
e1	4.95		5.15
F	1.23		1.32
H1	6.20		6.60
J1	2.40		2.72
L	13.00		14.00
L1	3.50		3.93
L20		16.40	
L30		28.90	
øΡ	3.75		3.85
Q	2.65		2.95

Revision history STGP10M65DF2

# 5 Revision history

**Table 9: Document revision history** 

Date	Revision	Changes
10-Feb-2015	1	First release.
23-Apr-2015	2	Minor text edits throughout document Document status promoted to 'Production data' In Section 2 Electrical characteristics: - updated Table 4: Static characteristics - updated Table 5: Dynamic characteristics - updated Table 6: IGBT switching characteristics (inductive load) - updated Table 7: Diode switching characteristics (inductive load) Added Section 2.1 Electrical characteristics (curves)
31-Jul-2015	3	Updated Table 7: Diode switching characteristics (inductive load)
19-Oct-2015	4	Updated Table 5: "Dynamic characteristics" and Table 6: "IGBT switching characteristics (inductive load)". Updated Figure 8: "Collector current vs. switching frequency".
07-Apr-2017	5	Modified title, features and applications on cover page  Modified Table 4: "Static characteristics", Table 6: "IGBT switching characteristics (inductive load)" and Table 7: "Diode switching characteristics (inductive load)"  Minor text changes.

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