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STGW30N120KD STGWA30N120KD

30 A, 1200 V short circuit rugged IGBT with Ultrafast diode

Features

- Low on-losses
- High current capability
- Low gate charge
- Short circuit withstand time 10 μs
- IGBT co-packaged with Ultrafast free-wheeling diode

Applications

■ Motor control

Description

This high voltage and short-circuit rugged IGBT utilizes the advanced PowerMESH™ process resulting in an excellent trade-off between switching performance and low ON-state behavior.

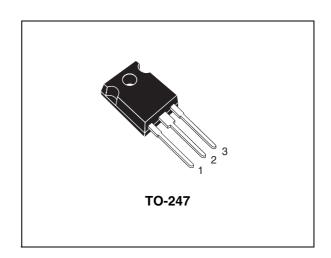


Figure 1. Internal schematic diagram

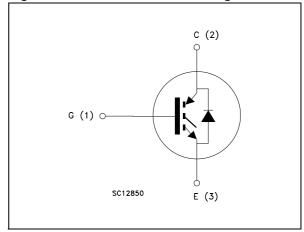


Table 1. Device summary

Order codes Markings		Package	Packaging
STGW30N120KD	GW30N120KD	TO-247	Tube
STGWA30N120KD	GWA30N120KD	TO-247 long leads	Tube

Contents

1	Electrical ratings
2	Electrical characteristics
	2.1 Electrical characteristics (curves)
3	Test circuit
4	Package mechanical data
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1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{CES}	Collector-emitter voltage (V _{GE} = 0)	1200	V
I _C ⁽¹⁾	Collector current (continuous) at 25 °C	60	Α
I _C ⁽¹⁾	Collector current (continuous) at 100 °C	30	Α
I _{CL} (2)	Turn-off latching current	100	Α
I _{CP} (3)	Pulsed collector current	100	Α
V _{GE}	Gate-emitter voltage	±25	V
t _{scw}	Short circuit withstand time, $V_{CE} = 0.5 V_{(BR)CES}$ $T_j = 125 ^{\circ}C, R_G = 10 \Omega, V_{GE} = 12 V$	10	μs
P _{TOT}	Total dissipation at T _C = 25 °C	220	W
I _F	Diode RMS forward current at T _C = 25 °C	30	Α
I _{FSM}	Surge non repetitive forward current t _p = 10 ms sinusoidal	100	Α
Tj	Operating junction temperature	- 55 to 125	°C

1. Calculated according to the iterative formula:

$$I_{C}(T_{C}) = \frac{T_{j(max)} - T_{C}}{R_{thj-c} \times V_{CE(sat)(max)}(T_{j(max)}, I_{C}(T_{C}))}$$

- 2. Vclamp = 80% of V_{CES}, T_j =125 °C, R_G=10 Ω , V_{GE}=15 V
- 3. Pulse width limited by max. junction temperature allowed

Table 3. Thermal resistance

Symbol	Parameter	Value	Unit
R _{thj-case}	Thermal resistance junction-case IGBT max.	0.45	°C/W
R _{thj-case}	Thermal resistance junction-case diode max.	1.6	°C/W
R _{thj-amb}	Thermal resistance junction-ambient IGBT max.	50	°C/W

2 Electrical characteristics

T_{CASE}=25 °C unless otherwise specified.

Table 4. Static

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{(BR)CES}	Collector-emitter breakdown voltage (V _{GE} = 0)	I _C = 1 mA	1200			٧
V _{CE(sat)}	Collector-emitter saturation voltage	V_{GE} = 15 V, I_{C} = 20 A V_{GE} = 15 V, I_{C} = 20 A, T_{C} =125 °C		2.8	3.85	V V
V _{GE(th)}	Gate threshold voltage	V _{CE} = V _{GE} , I _C = 1mA	4.5		6.5	V
I _{CES}	Collector cut-off current (V _{GE} = 0)	V _{CE} =1200 V V _{CE} =1200 V, Tc=125 °C			500 10	μA mA
I _{GES}	Gate-emitter leakage current (V _{CE} = 0)	V _{GE} =± 20 V			± 100	nA
9 _{fs}	Forward transconductance	V _{CE} = 25 V _, I _C = 20 A		20		S

Table 5. Dynamic

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
C _{ies} C _{oes} C _{res}	Input capacitance Output capacitance Reverse transfer capacitance	V _{CE} = 25 V, f = 1 MHz, V _{GE} =0		2520 170 33		pF pF pF
Q _g Q _{ge} Q _{gc}	Total gate charge Gate-emitter charge Gate-collector charge	V _{CE} = 960 V, I _C = 20 A,V _{GE} =15 V		105 21 56		nC nC nC

Table 6. Switching on/off (inductive load)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t _{d(on)} t _r (di/dt) _{on}	Turn-on delay time Current rise time Turn-on current slope	V_{CC} = 960 V, I_{C} = 20 A R_{G} = 10 Ω , V_{GE} = 15 V, (see Figure 17)		36 22 840		ns ns A/µs
t _{d(on)} t _r (di/dt) _{on}	Turn-on delay time Current rise time Turn-on current slope	$V_{CC} = 960 \text{ V}, I_{C} = 20 \text{ A}$ $R_{G} = 10 \Omega, V_{GE} = 15 \text{ V},$ $T_{C} = 125 ^{\circ}\text{C} \text{ (see Figure 17)}$		35 22 760		ns ns A/µs
$t_r(V_{off})$ $t_d(_{off})$ t_f	Off voltage rise time Turn-off delay time Current fall time	V_{CC} = 960 V, I_{C} = 20 A R_{G} = 10 Ω , V_{GE} = 15 V, (see Figure 17)		70 251 260		ns ns ns
$t_r(V_{off})$ $t_d(_{off})$ t_f	Off voltage rise time Turn-off delay time Current fall time	$V_{CC} = 960 \text{ V}, I_{C} = 20 \text{ A}$ $R_{G} = 10 \Omega, V_{GE} = 15 \text{ V},$ $T_{C} = 125 ^{\circ}\text{C} \text{ (see Figure 17)}$		140 324 432		ns ns ns

Table 7. Switching energy (inductive load)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Eon ⁽¹⁾	Turn-on switching losses	$V_{CC} = 960 \text{ V}, I_{C} = 20 \text{ A}$		2.4		mJ
E _{off} (2)	Turn-off switching losses	R_{G} = 10 Ω , V_{GE} = 15 V,		4.3		mJ
E _{ts}	Total switching losses	(see Figure 17)		6.7		mJ
Eon ⁽¹⁾	Turn-on switching losses	$V_{CC} = 960 \text{ V}, I_{C} = 20 \text{ A}$		3.9		mJ
E _{off} (2)	Turn-off switching losses	R_G = 10 Ω , V_{GE} = 15 V ,		5.8		mJ
E _{ts}	Total switching losses	Tc= 125 °C (see Figure 17)		9.7		mJ

Eon is the turn-on losses when a typical diode is used in the test circuit in Figure 17. If the IGBT is offered
in a package with a co-pack diode, the co-pack diode is used as external diode. IGBTs and diode are at the
same temperature (25°C and 125°C)

Table 8. Collector-emitter diode

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _F	Forward on-voltage	I _F = 20 A I _F = 20 A, T _C = 125 °C		1.9 1.7		V V
t _{rr} Q _{rr} I _{rrm}	Reverse recovery time Reverse recovery charge Reverse recovery current	$I_F = 20 \text{ A}, V_R = 45 \text{ V},$ di/dt = 100 A/ μ s (see Figure 20)		84 235 5.6		ns nC A
t _{rr} Q _{rr} I _{rrm}	Reverse recovery time Reverse recovery charge Reverse recovery current	$I_F = 20 \text{ A}, V_R = 45 \text{ V},$ $Tc = 125 ^{\circ}\text{C},$ $di/dt = 100 \text{ A/}\mu\text{s}$ (see Figure 20)		152 722 9		ns nC A

^{2.} Turn-off losses include also the tail of the collector current

2.1 Electrical characteristics (curves)

Figure 2. Output characteristics

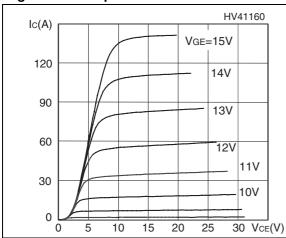


Figure 3. Transfer characteristics

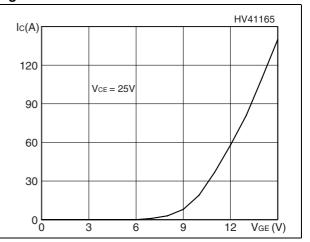
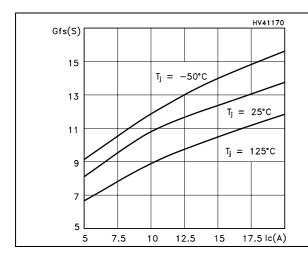


Figure 4. Transconductance

Figure 5. Collector-emitter on voltage vs. temperature



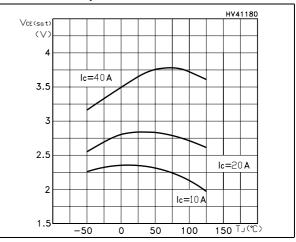
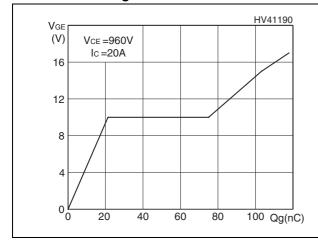
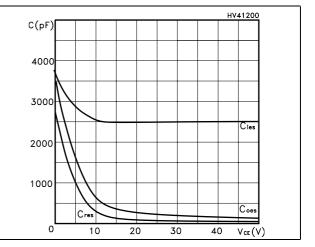


Figure 6. Gate charge vs. gate-source voltage

Figure 7. Capacitance variations





4

Collector-emitter on voltage vs. Figure 8. Normalized gate threshold voltage Figure 9. collector current vs. temperature

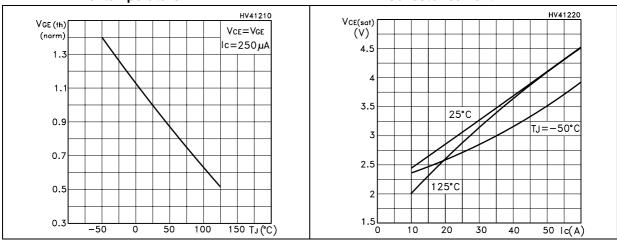


Figure 10. Normalized breakdown voltage vs. Figure 11. Switching losses vs. temperature temperature

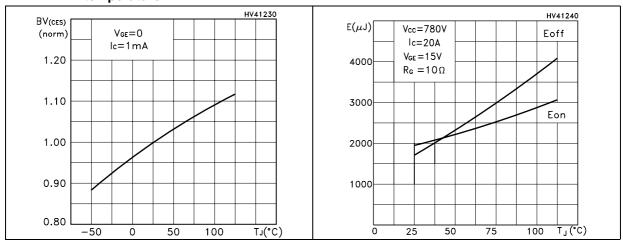


Figure 12. Switching losses vs. gate resistance

 $E(\mu J)$

8000

6000

4000

2000

Eoff

Eon

50

100

150

200

 $R_G(\Omega)$

HV41250 HV41260 E (µJ) Vcc=780V lc=20A Eoff Vcc = 780V $V_{GE} = 15V$ $V_{GE} = 15V$ 4000 $T_J = 125^{\circ}C$ $R_{\text{G}}=10\Omega$ $T_J = 125^{\circ}C$ 3000 Eon 2000 1000

0 L

Figure 13. Switching losses vs. collector current

10

15

20

Ic (A)

10⁻³

Figure 14. Thermal impedance

K $\delta = 0.5$ 0.2
0.01 0.050.02
0.01 $Z_{th} = k R_{thJ-c}$ $\delta = t_p/\tau$ $-t_p$

Figure 15. Turn-off SOA

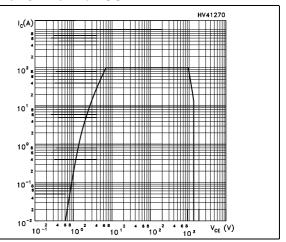


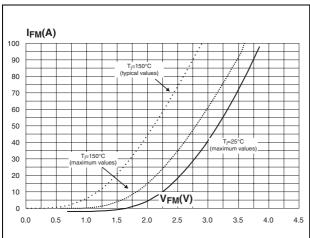
Figure 16. Forward voltage drop vs. forward current

10⁻³

10-2

10⁻¹ t_p(s)

10-4



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3 Test circuit

Figure 17. Test circuit for inductive load switching

Figure 18. Gate charge test circuit

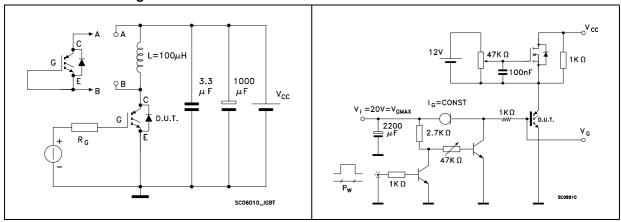
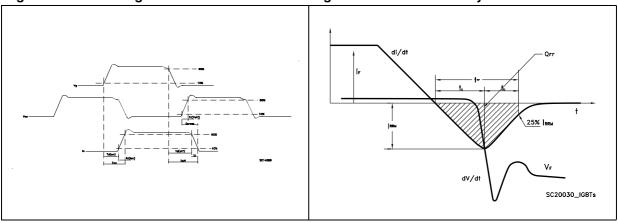


Figure 19. Switching waveform

Figure 20. Diode recovery time waveform



4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK® is an ST trademark.

Table 9. TO-247 mechanical data

Dim.		mm.	
Dilli.	Min.	Тур.	Max.
А	4.85		5.15
A1	2.20		2.60
b	1.0		1.40
b1	2.0		2.40
b2	3.0		3.40
С	0.40		0.80
D	19.85		20.15
E	15.45		15.75
е	5.30	5.45	5.60
L	14.20		14.80
L1	3.70		4.30
L2		18.50	
ØP	3.55		3.65
ØR	4.50		5.50
S	5.30	5.50	5.70

HEAT-SINK PLANE

BACK VIEW 0075325, G

Figure 21. TO-247 drawing dimensions

Table 10. TO-247 long leads mechanical data

Dim		mm	
Dim. –	Min.	Тур.	Max.
Α	4.90		5.15
D	1.85		2.10
Е	0.55		0.67
F	1.07		1.32
F1	1.90		2.38
F2	2.87		3.38
G		10.90 BSC	
Н	15.77		16.02
L	20.82		21.07
L1	4.16		4.47
L2	5.49		5.74
L3	20.05		20.30
L4	3.68		3.93
L5	6.04		6.29
М	2.27		2.52
V		10°	
V1		3°	
V3		20°	
Dia.	3.55		3.66

HEAT-SINK PLANE -D L5 L 1 L'3 F2 - E BACK VIEW 7395426_E V1

Figure 22. TO-247 long leads drawing

5 Revision history

Table 11. Document revision history

Date	Pate Revision Changes	
29-Jan-2008	1	Initial release
18-Jun-2008	2	Update values in <i>Table 2</i>
02-Dec-2008	3	Update P _{TOT} and R _{thj-case} value (see <i>Table 2</i> and <i>Table 3</i>)
17-Jan-2012	4	Added order code STGWA30N120KD <i>Table 1 on page 1</i> , mechanical data TO-247 long leads <i>Table 10 on page 12</i> and <i>Figure 22 on page 13</i> .
27-Feb-2012	5	Modified: Description on page 1.

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