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STPS10120C

Power Schottky rectifier

Table 1. Main product characteristics

<u> </u>	
I _{F(AV)}	2 x 5 A
V _{RRM}	120 V
T _{j(max)}	175° C
V _{F(typ)}	0.64 V

Feature and benefits

- High junction temperature capability
- Good trade-off between leakage current and forward voltage drop
- Low leakage current
- Avalanche capability specified
- Insulated package
 - TO-220FPAB
 Insulating voltage = 2000 V
 Typical package capacitance 12 pF

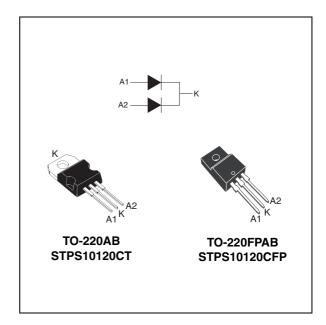


Table 2. Order code

Part number	Marking
STPS10120CT	STPS10120CT
STPS10120CFP	STPS10120CFP

Description

Dual center tap Schottky rectifier suited for high frequency switch mode power supplies.

Table 3. Absolute ratings (limiting values, per diode)

Symbol	Parameter				Value	Unit	
V_{RRM}	Repetitive peak reverse voltage				120	V	
I _{F(RMS)}	RMS forward current				30	Α	
	Average forward current, $\delta = 0.5$	TO-220AB	T _c = 160° C	Per diode	5		
I		10-220AB	T _c = 150° C	Per device	10	А	
IF(AV)		TO-220FPAB	T _c = 150° C	Per diode	5		
			T _c = 135° C	Per device	10		
I _{FSM}	Surge non repetitive forwar	t _p = 10 ms Sinusoidal		120	Α		
P _{ARM}	Repetitive peak avalanche power $t_p = 1 \mu s T_j = 25^{\circ} C$			3000	W		
T _{stg}	Storage temperature range				-65 to + 175	° C	
T _j	Maximum operating junction temperature ⁽¹⁾			175	° C		
dV/dt	Critical rate of rise of reverse voltage			10000	V/µs		

^{1.} $\frac{dPtot}{dT_i} < \frac{1}{Rth(i-a)}$ condition to avoid thermal runaway for a diode on its own heatsink

Characteristics STPS10120C

1 Characteristics

Table 4. Thermal parameters

Symbol	Parameter			Value	Unit
	TO-220AB		Per diode	3.8	
Ь	Junction to case	10-220AB	Total	2.3	
$R_{th(j-c)}$	Junction to case	TO-220FPAB	Per diode	6.6	
		10-220FPAB	Total	5.2	° C/W
В	R _{th(c)} Coupling TO-220AB TO-220FPAB	Total	0.7		
□th(c)		TO-220FPAB	างเลเ	3.7	

When the diodes 1 and 2 are used simultaneously:

 $T_j(diode\ 1) = P(diode\ 1) \times R_{th(j-c)}(per\ diode) + P(diode\ 2) \times R_{th(c)}$

Table 5. Static electrical characteristics (per diode)

Symbol	Test conditions			Min.	Тур.	Max.	Unit
I _R ⁽¹⁾	Reverse leakage current	T _j = 25° C	V _R = V _{RRM}			6	μΑ
		T _j = 125° C			1	3	mA
	Forward voltage drop	T _j = 25° C	I _F = 5 A			0.85	
V _E ⁽²⁾		T _j = 125° C			0.64	0.7	V
v F` ′		T _j = 25° C	I _F = 10 A			0.96	V
		T _j = 125° C			0.73	0.8	

^{1.} Pulse test : tp = 5 ms, δ < 2%

To evaluate the maximum conduction losses use the following equation :

$$P = 0.60 \times I_{F(AV)} + 0.02 I_{F}^{2}_{(RMS)}$$

^{2.} Pulse test : tp = 380 μ s, δ < 2%

STPS10120C Characteristics

Figure 1. Average forward power dissipation Figure 2. Average forward current versus awbient temperature (per diode) $(\delta = 0.5, \text{ per diode})$

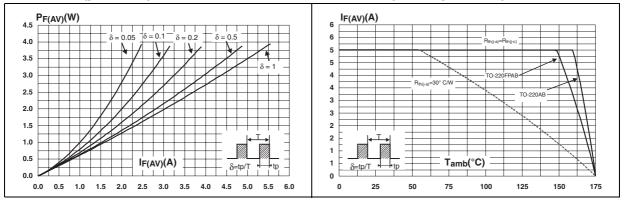


Figure 3. Normalized avalanche power derating versus pulse duration

Figure 4. Normalized avalanche power derating versus junction temperature

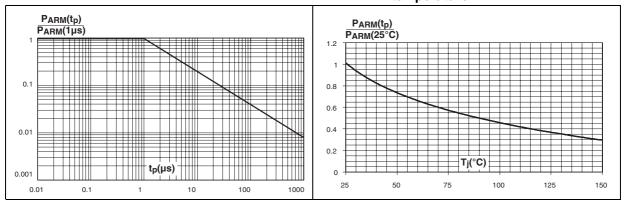
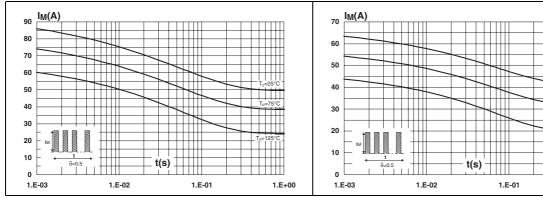


Figure 6.

Figure 5. Non repetitive surge peak forward current versus overload duration (maximum values, per diode) (TO-220AB)

Non repetitive surge peak forward current versus overload duration (maximum values, per diode) (TO-220FPAB)

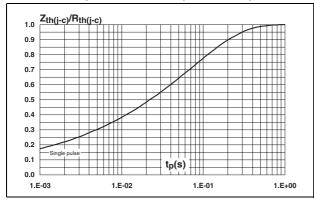


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Characteristics STPS10120C

Figure 7. Relative variation of thermal impedance junction to case versus pulse duration (TO-220AB)

Figure 8. Relative variation of thermal impedance junction to case versus pulse duration (TO-220FPAB)



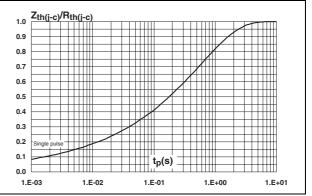
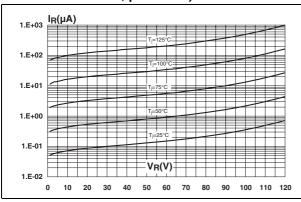


Figure 9. Reverse leakage current versus reverse voltage applied (typical values, per diode)

Figure 10. Junction capacitance versus reverse voltage applied (typical values, per diode)



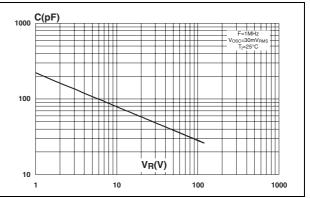
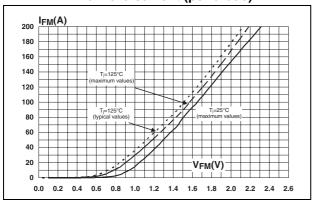


Figure 11. Forward voltage drop versus forward current (per diode)



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STPS10120C Package information

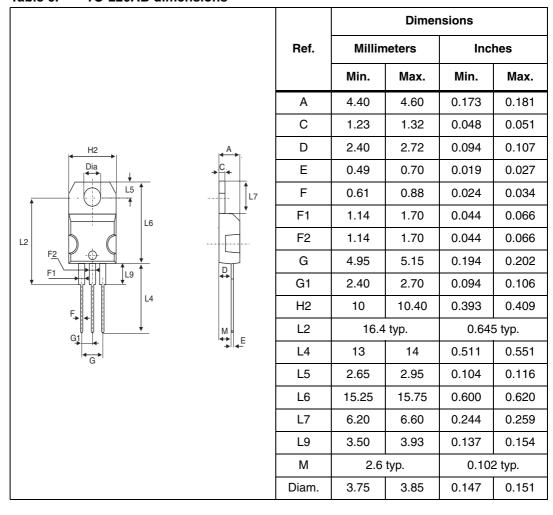
2 Package information

Epoxy meets UL94, V0

• Cooling method: by conduction (C)

Recommended torque value: 0.4 to 0.6 Nm

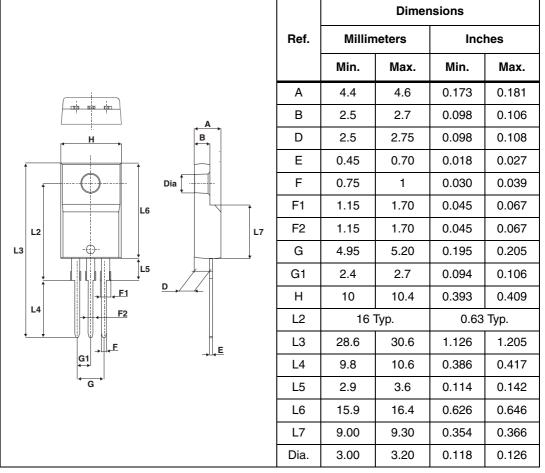
Table 6. TO-220AB dimensions



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Package information STPS10120C

Table 7. TO-220FPAB dimensions



In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

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3 Ordering information

Table 8. Ordering information

Part number	Marking	Package	Weight	Base qty	Delivery mode
STPS10120CT	STPS10120CT	TO-220AB	2.2 g	50	Tube
STPS10120CFP	STPS10120CFP	TO-220FPAB	2.0 g	50	Tube

4 Revision history

Table 9.Revision history

Date	Revision	Changes
11-Jul-2007	1	First issue

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