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## HIGH VOLTAGE POWER SCHOTTKY RECTIFIER

**Table 1: Main Product Characteristics**

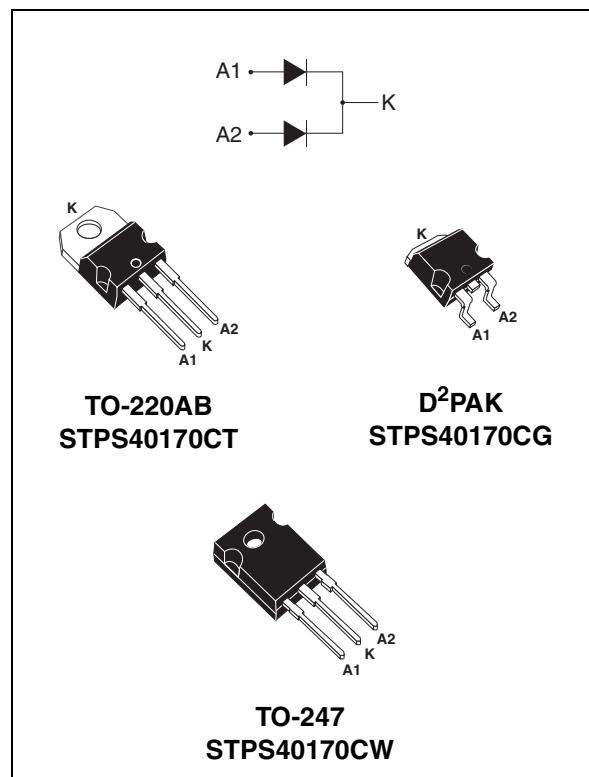
$I_{F(AV)}$	2 x 20 A
$V_{RRM}$	170 V
$T_j$	175 °C
$V_F(\text{max})$	0.75 V

**FEATURES AND BENEFITS**

- High junction temperature capability
- Low leakage current
- Good trade off between leakage current and forward voltage drop
- Low thermal resistance
- High frequency operation
- Avalanche specification

**DESCRIPTION**

Dual center tab Schottky rectifier suited for High Frequency Switched Mode Power Supplies. Packaged in TO-220AB, D2PAK and TO-247, these devices are intended for use to enhance the reliability of the application.

**Table 2: Order Codes**

Part Numbers	Marking
STPS40170CT	STPS40170CT
STPS40170CG	STPS40170CG
STPS40170CG-TR	STPS40170CG
STPS40170CW	STPS40170CW

## STPS40170C

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**Table 3: Absolute Ratings** (limiting values, per diode)

Symbol	Parameter			Value	Unit
V <sub>RRM</sub>	Repetitive peak reverse voltage			170	V
I <sub>F(RMS)</sub>	RMS forward current			60	A
I <sub>F(AV)</sub>	Average forward current		T <sub>c</sub> = 150 °C δ = 0.5	Per diode	20
				Per device	40
I <sub>FSM</sub>	Surge non repetitive forward current		t <sub>p</sub> = 10 ms sinusoidal	250	A
P <sub>ARM</sub>	Repetitive peak avalanche power		t <sub>p</sub> = 1 μs T <sub>j</sub> = 25 °C	14100	W
T <sub>stg</sub>	Storage temperature range			-65 to + 175	°C
T <sub>j</sub>	Maximum operating junction temperature *			175	°C
dV/dt	Critical rate of rise of reverse voltage			10000	V/μs

\* :  $\frac{dP_{tot}}{dT_j} < \frac{1}{R_{th}(j-a)}$  thermal runaway condition for a diode on its own heatsink

**Table 4: Thermal Parameters**

Symbol	Parameter			Value	Unit
R <sub>th(j-c)</sub>	Junction to case		Per diode	1.2	°C/W
		Total	0.85		
R <sub>th(c)</sub>		Coupling	0.5		

When the diodes 1 and 2 are used simultaneously:

$$\Delta T_j(\text{diode 1}) = P(\text{diode 1}) \times R_{th(j-c)}(\text{Per diode}) + P(\text{diode 2}) \times R_{th(c)}$$

**Table 5: Static Electrical Characteristics** (per diode)

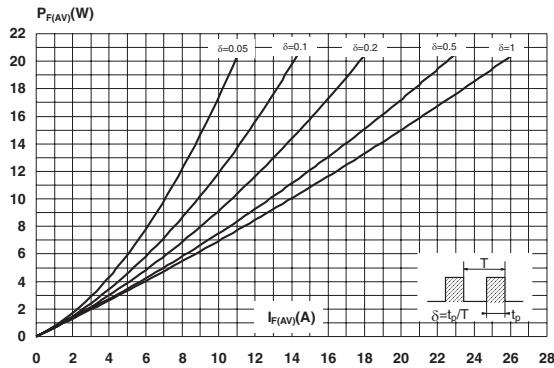
Symbol	Parameter	Tests conditions		Min.	Typ	Max.	Unit
I <sub>R</sub> *	Reverse leakage current	T <sub>j</sub> = 25 °C	V <sub>R</sub> = V <sub>RRM</sub>			30	μA
		T <sub>j</sub> = 125 °C			7	30	mA
V <sub>F</sub> **	Forward voltage drop	T <sub>j</sub> = 25 °C	I <sub>F</sub> = 20A			0.92	V
		T <sub>j</sub> = 125 °C			0.69	0.75	
		T <sub>j</sub> = 25 °C	I <sub>F</sub> = 40A			1.00	
		T <sub>j</sub> = 125 °C			0.79	0.86	

Pulse test: \* tp = 5 ms, δ < 2%

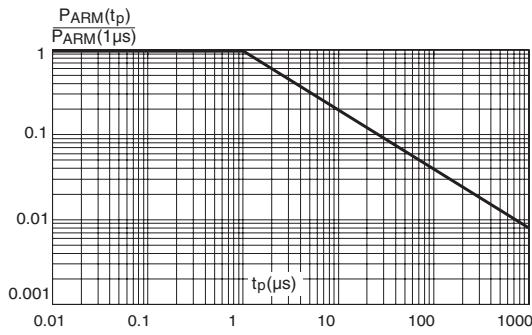
\*\* tp = 380 μs, δ < 2%

To evaluate the conduction losses use the following equation: P = 0.64 × I<sub>F(AV)</sub> + 0.055 I<sub>F</sub><sup>2</sup> (RMS)

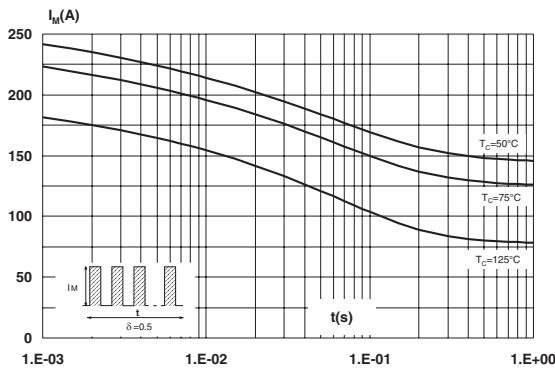
**Figure 1: Average forward power dissipation versus average forward current (per diode)**



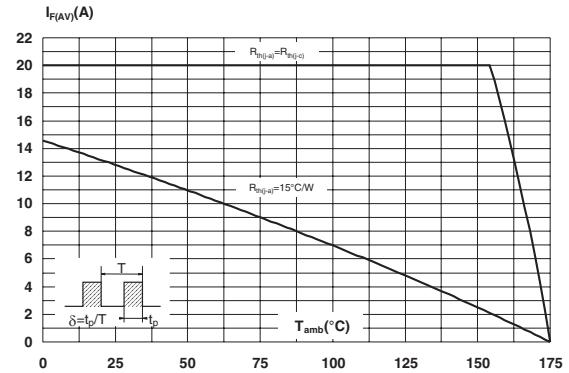
**Figure 3: Normalized avalanche power derating versus pulse duration**



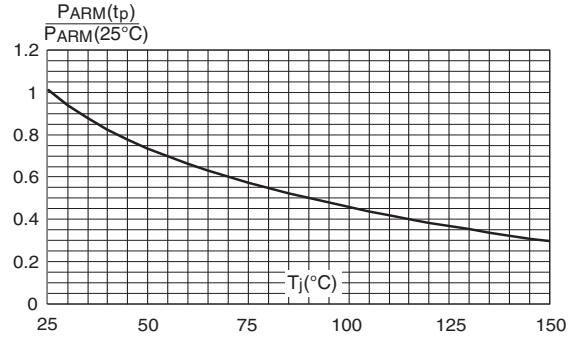
**Figure 5: Non repetitive surge peak forward current versus overload duration (maximum values, per diode)**



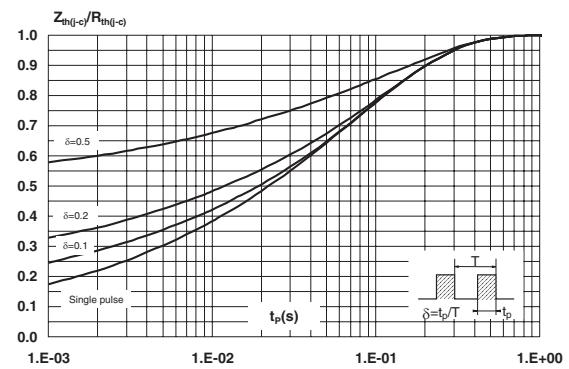
**Figure 2: Average forward current versus ambient temperature ( $\delta = 0.5$ , per diode)**



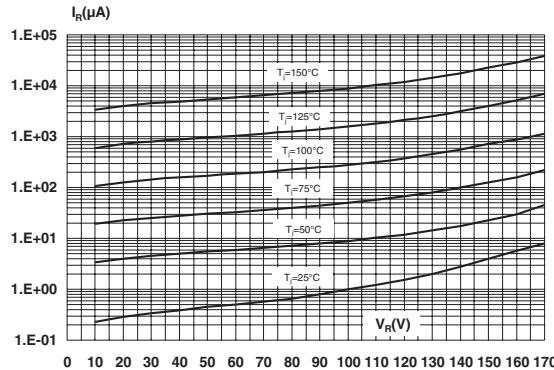
**Figure 4: Normalized avalanche power derating versus junction temperature**



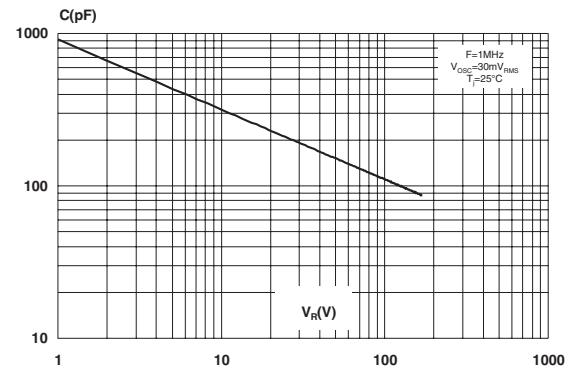
**Figure 6: Relative variation of thermal impedance junction to case versus pulse duration**



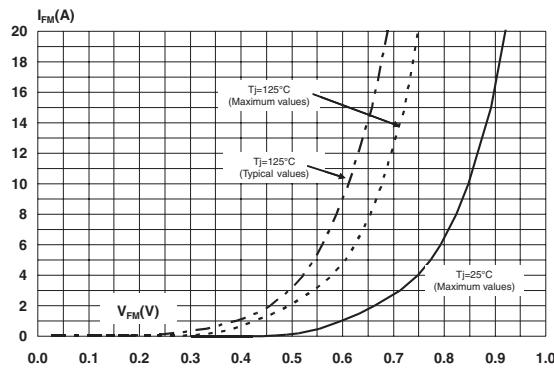
**Figure 7: Reverse leakage current versus reverse voltage applied (typical values, per diode)**



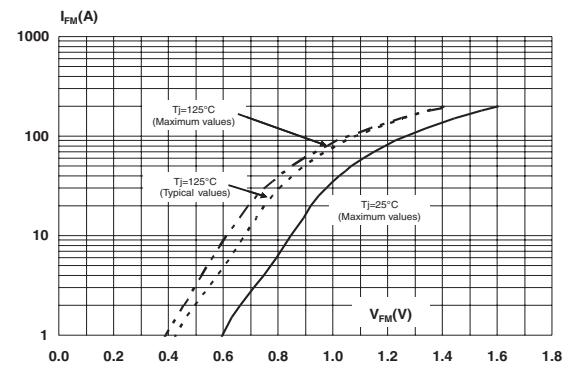
**Figure 8: Junction capacitance versus reverse voltage applied (typical values, per diode)**



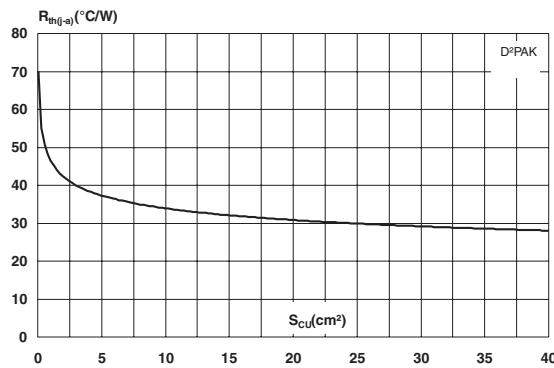
**Figure 9: Forward voltage drop versus forward current (per diode, low level)**



**Figure 10: Forward voltage drop versus forward current (per diode, high level)**



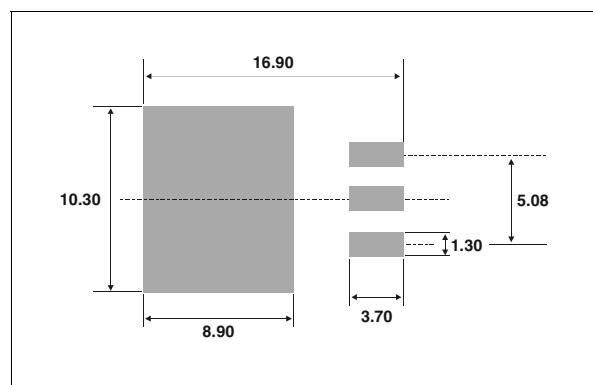
**Figure 11: Thermal resistance junction to ambient versus copper surface under tab (epoxy printed board FR4, Cu = 35μm) (D<sup>2</sup>PAK)**



**Figure 12: D<sup>2</sup>PAK Package Mechanical Data**

**DIMENSIONS**

REF.	DIMENSIONS			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	4.40	4.60	0.173	0.181
A1	2.49	2.69	0.098	0.106
A2	0.03	0.23	0.001	0.009
B	0.70	0.93	0.027	0.037
B2	1.14	1.70	0.045	0.067
C	0.45	0.60	0.017	0.024
C2	1.23	1.36	0.048	0.054
D	8.95	9.35	0.352	0.368
E	10.00	10.40	0.393	0.409
G	4.88	5.28	0.192	0.208
L	15.00	15.85	0.590	0.624
L2	1.27	1.40	0.050	0.055
L3	1.40	1.75	0.055	0.069
M	2.40	3.20	0.094	0.126
R	0.40 typ.		0.016 typ.	
V2	0°	8°	0°	8°

**Figure 13: Foot Print Dimensions (in millimeters)**

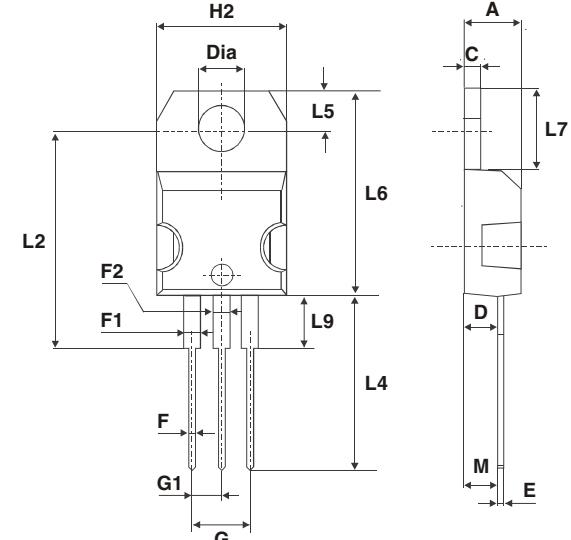
## STPS40170C

Figure 14: TO-247 Package Mechanical Data

The technical drawing illustrates the TO-247 package with two views: a top view showing lead spacing and height, and a side view showing lead thickness and overall height. Key dimensions include L (height), L1-L5 (lead spacing), D (lead diameter), A (lead thickness), and various feature widths (F1-F4) and heights (H, V1, V2, G).

REF.	DIMENSIONS					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.85		5.15	0.191		0.203
D	2.20		2.60	0.086		0.102
E	0.40		0.80	0.015		0.031
F	1.00		1.40	0.039		0.055
F1		3.00			0.118	
F2		2.00			0.078	
F3	2.00		2.40	0.078		0.094
F4	3.00		3.40	0.118		0.133
G		10.90			0.429	
H	15.45		15.75	0.608		0.620
L	19.85		20.15	0.781		0.793
L1	3.70		4.30	0.145		0.169
L2		18.50			0.728	
L3	14.20		14.80	0.559		0.582
L4		34.60			1.362	
L5		5.50			0.216	
M	2.00		3.00	0.078		0.118
V		5°			5°	
V2		60°			60°	
Dia.	3.55		3.65	0.139		0.143

Figure 15: TO-220AB Package Mechanical Data



REF.	DIMENSIONS			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	4.40	4.60	0.173	0.181
C	1.23	1.32	0.048	0.051
D	2.40	2.72	0.094	0.107
E	0.49	0.70	0.019	0.027
F	0.61	0.88	0.024	0.034
F1	1.14	1.70	0.044	0.066
F2	1.14	1.70	0.044	0.066
G	4.95	5.15	0.194	0.202
G1	2.40	2.70	0.094	0.106
H2	10	10.40	0.393	0.409
L2	16.4 typ.		0.645 typ.	
L4	13	14	0.511	0.551
L5	2.65	2.95	0.104	0.116
L6	15.25	15.75	0.600	0.620
L7	6.20	6.60	0.244	0.259
L9	3.50	3.93	0.137	0.154
M	2.6 typ.		0.102 typ.	
Diam.	3.75	3.85	0.147	0.151

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect . The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: [www.st.com](http://www.st.com).

Table 6: Ordering Information

Ordering type	Marking	Package	Weight	Base qty	Delivery mode
STPS40170CT	STPS40170CT	TO-220AB	2.20 g	50	Tube
STPS40170CG	STPS40170CG	D <sup>2</sup> PAK	1.48 g	50	Tube
STPS40170CG-TR	STPS40170CG			1000	Tape & reel
STPS40170CW	STPS40170CW	TO-247	4.4 g	30	Tube

- Epoxy meets UL94, V0
- Cooling method: by conduction (C)
- TO-220 - Recommended torque value: 0.55 Nm, Maximum torque value: 0.7 Nm.
- TO-247 - Recommended torque value: 0.8 Nm, Maximum torque value: 1.0 Nm.

Table 7: Revision History

Date	Revision	Description of Changes
16-Sep-2005	1	First issue.

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