

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



## Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

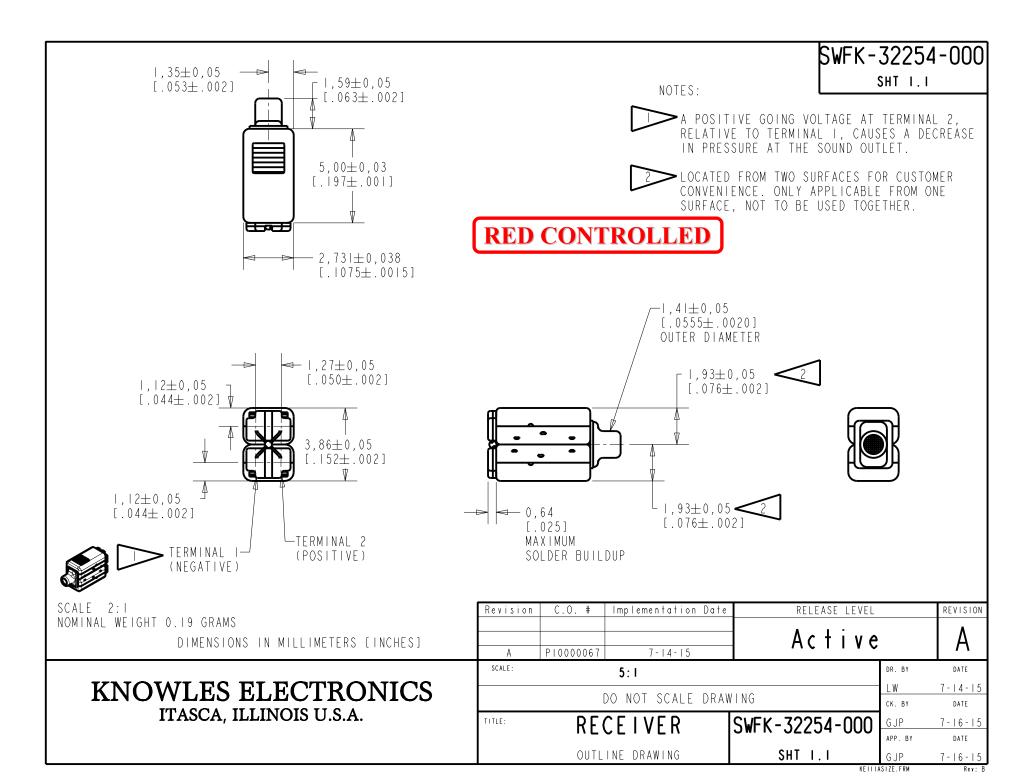
Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









TYPE I

SWFK-32254-000 SHEET 2.1

NOTE: SPECIFICATIONS FOLLOWED BY AN ASTERISK (\*) ARE 100% TESTED.

CONSTANT VOLTAGE DRIVE CONDITIONS.



## ACOUSTICAL

SENSITIVITY

DEVICE WILL PRODUCE THE SPL LISTED BELOW UNDER TEST CONDITIONS DESCRIBED IN TABLE 3. NOMINAL SENSITIVITY AT IkHz IS dB RELATIVE TO 20μPα. ALL OTHER VALUES IN dB RELATIVE TO THE SENSITIVITY AT IKHZ.

FREQUENCY (Hz)	MINIMUM	NOMINAL	MAXIMUM
200	- 3.0	-1.0	+   . 0
500	-2.0	0.0	+2.0
1000	-2.0	97.0	+2.0
3000-4000 PEAK	+5.0	+8.0	+  .0
6600-7600 PEAK	0.0	+3.0	+6.0

TABLE I

MECHANICAL PORT LOCATION: 12S

SOLDER TYPE: SAC305 TEMPERATURE

OPERATING: SENSITIVITY WILL NOT VARY MORE

THAN +1 / -3 dB FROM -17°C TO 63°C.
STORAGE: -40°C TO 63°C.

TOTAL HARMONIC DISTORTION

DEVICE WILL NOT EXCEED TOTAL HARMONIC DISTORTION LEVELS LISTED BELOW.

FREQUENCY (Hz)	AC DRIVE (V rms)	DC BIAS (V)	LIMIT (%)
1166	. 094	0	5
1750	. 0 9 4	0	5
1750	. 265	0	10

TABLE 2

## TEST CONDITIONS

TEOT CONDITIONS	
NOMINAL SOURCE VOLTAGE	0.94 V rms, 0 mA DC BIAS
SOURCE IMPEDANCE	<0.2 Ohm
TUBING	10 mm [.394"] LONG,   mm [.039"]   .D.
COUPLER CAVITY	2 CM <sup>3</sup> , SIMULATED ANSI S3.7 TYPE HA-3 (IEC 60318-5)

TABLE 3

## ELECTRICAL

DC RESISTANCE	6.3 Ohms ± 10%	
IMPEDANCE @ 500 Hz	6.8 Ohms $\pm$ 20%	MEASURED WITH 5.8 mArms, Odc DRIVE
IMPEDANCE @ IkHz	7.2 Ohms ± 20%	MEASURED WITH 5.8 mArms, Odc DRIVE

TITLE:

TABLE 4

ISOLATION: CASE WILL BE ELECTRICALLY ISOLATED FROM THE COIL CIRCUIT.

Revision	C.O. #	Implementation Date	RELEASE LEVEL		REVISION
			Active		Δ
А	P10000067	7 - 14 - 15	761170		/ \
			INSPECTION ACCEPTANCE/REJECTION	DR. BY	DATE
CRITERIA, (	CORRELATION (	DE LEST EQUIPMENT WITH KN	OWLES IS ALSO REQUIRED FOR	1 107	7   1   5

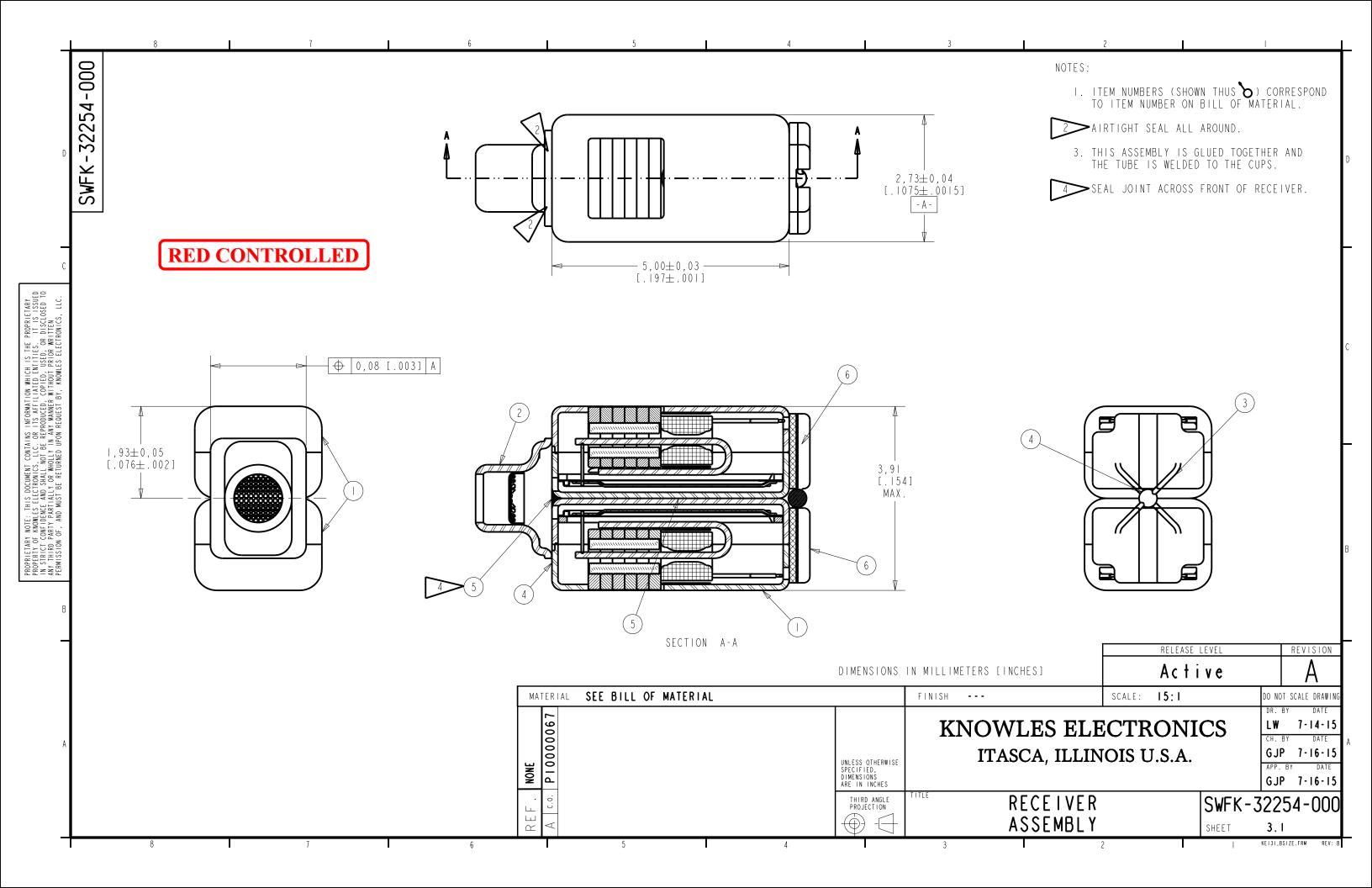
**KNOWLES ELECTRONICS** ITASCA, ILLINOIS U.S.A.

ELIMINATION OF EQUIPMENT AND TEST METHOD VARIATION

RECEIVER

PERFORMANCE SPECIFICATION

7 - | 4 - | 5 DATE SWFK-32254-000 GJP 7-16-15 DATE SHT 2.1 GJP 7-16-15



SWFK-32254-000		SWFK-32254-000
Sheet 4.1	ITASCA, ILLINOIS U.S.A.	Sheet <b>4.</b> I
By Date Dr. LW 7-14-15 Ch. GJP 7-16-15 App. GJP 7-16-15	RECEIVER ASSEMBLY	Release Level Active
COL. I	2 3	4 0 0 0
Part or Dwg. no.	Tot. Nomenclature:	Applicable Specs ♥ 2 0 0 0 Remarks
WBFK-31142-000	2 RECEIVER ASSEMBLY	1 1
FH-172-022	TUBE ASSEMBREEN ASSEMBLY	2 2
103-012	A/R WIRE	3 3
13-825-3 45	A/R CEMENT	4 4
13-637	LOCTITE 496 CEMENT	5 5
285-007-221	SOLDER LEAD FREE	6 6

# RED CONTROLLED

SWFK-32254-000 Sheet 4.1

#### PRODUCTION TEST INFORMATION

SOFTWARE REVISION PXI FCAAT	SOFTWARE 2.5 MIN.
HARDWARE REVISION PXI FCAAT	1065E.008 MIN
TEST BOARD	T4530
TEST COUPLER	T8049

#### PHASE TEST

FREQUENCY	AC DRIVE	DEGREE LIMIT		
(Hz)	(mArms)	MIN	MAX	
500	5.8	140	220	

## RED CONTROLLED

Revision	C.O. #	Implementation Date	RELEASE LEVEL		REVISION
					A
			Active I	ΙΔ	
А	P10000067	7 - 14 - 15	7.6.1.10		7.1
WHEN TEST	LIMITS ARE US	SED TO ESTABLISH INCOMING	INSPECTION ACCEPTANCE/REJECTION	DR. BY	DATE

KNOWLES ELECTRONICS ITASCA, ILLINOIS U.S.A.

WHEN TEST LIMITS ARE USED TO ESTABLISH INCOMING INSPECTION ACCEPTANCE/REJECTION DR. BY CRITERIA, CORRELATION OF TEST EQUIPMENT WITH KNOWLES IS ALSO REQUIRED FOR LW

LW 7-14-15
CK. BY DATE

GJP 7-16-15
APP. BY DATE

GJP 7-16-15

RECEIVER SWFK-32254-000
PERFORMANCE SPECIFICATION SHT 5.1