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# Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







## NOT RECOMMENDED FOR NEW DESIGNS



16:1 MULTIPLEXER SY10E164 SY100E164

### **FEATURES**

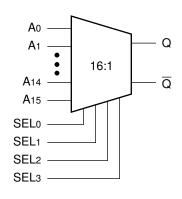
- 850ps Data Input to Output
- Extended 100E VEE range of -4.2V to -5.5V
- **■** Differential output
- Fully compatible with industry standard 10KH, 100K ECL levels
- Internal 75K $\Omega$  input pull-down resistors
- Fully compatible with Motorola MC10E/100E164
- Available in 28-pin PLCC package

## **DESCRIPTION**

The SY10/100E164 are 16:1 multiplexers with a differential output. The select inputs (SEL0,1,2,3) control which one of the sixteen data inputs (A0-A15) is propagated to the output.

Special attention to the design layout results in a typical skew between the 16 inputs of only 50ps.

### **BLOCK DIAGRAM**

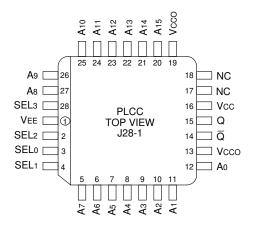


### **PIN NAMES**

Pin	Function
A0 — A15	Data Inputs
SEL[0:3]	Select Inputs
Q, Q	Outputs
Vcco	Vcc to Output

M9999-032006 hbwhelp@micrel.com or (408) 955-1690 Rev.: G Amendment:/0 Issue Date: March 2006

## PACKAGE/ORDERING INFORMATION



28-Pin PLCC (J28-1)

# Ordering Information<sup>(1)</sup>

Part Number	Package Type	Operating Range	Lead Finish			
SY10E164JC	J28-1	Commercial	SY10E164JC	Sn-Pb		
SY10E164JCTR <sup>(2)</sup>	J28-1	Commercial	SY10E164JC	Sn-Pb		
SY100E164JC	J28-1	Commercial	Commercial SY100E164JC			
SY100E164JCTR <sup>(2)</sup>	J28-1	Commercial	Sn-Pb			
SY10E164JZ <sup>(3)</sup>	J28-1	Commercial	SY10E164JZ with Pb-Free bar-line indicator	Matte-Sn		
SY10E164JZTR <sup>(2, 3)</sup>	J28-1	Commercial	SY10E164JZ with Pb-Free bar-line indicator	Matte-Sn		
SY100E164JZ <sup>(3)</sup>	J28-1	Commercial	SY100E164JZ with Pb-Free bar-line indicator	Matte-Sn		
SY100E164JZTR <sup>(2, 3)</sup>	J28-1	Commercial	SY100E164JZ with Pb-Free bar-line indicator	Matte-Sn		

#### Notes

- 1. Contact factory for die availability. Dice are guaranteed at  $T_A$  = 25°C, DC Electricals only.
- 2. Tape and Reel.
- 3. Pb-Free package is recommended for new designs.

## **TRUTH TABLE**

SEL3	SEL2	SEL <sub>1</sub>	SEL <sub>0</sub>	Data	
L	L	L	L	<b>A</b> 0	
L	L	L	Н	<b>A</b> 1	
L	L	Н	L	<b>A</b> 2	
L	L	Н	Н	Аз	
L	Н	L	L	<b>A</b> 4	
L	Н	L	Н	<b>A</b> 5	
L	Н	Н	L	<b>A</b> 6	
L	Н	Н	Н	<b>A</b> 7	

SEL3	SEL <sub>2</sub>	SEL <sub>1</sub>	SEL <sub>0</sub>	Data
Н	L	L	L	<b>A</b> 8
Н	L	L	Н	<b>A</b> 9
Н	L	Н	L	<b>A</b> 10
Н	L	Н	Н	<b>A</b> 11
Н	Н	L	L	<b>A</b> 12
Н	Н	L	Н	<b>A</b> 13
Н	Н	Н	L	<b>A</b> 14
Н	Н	Н	Н	<b>A</b> 15

## DC ELECTRICAL CHARACTERISTICS

VEE = VEE (Min.) to VEE (Max.); VCC = VCCO = GND

		TA = 0°C			Ta = +25°C			TA = +85°C				
Symbol	Parameter	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Unit	Condition
Iн	Input HIGH Current	_	_	150			150			150	μΑ	
IEE	Power Supply Current										mA	_
	10E	_	59	71	_	59	71	_	59	71		
	100E	_	59	71	_	59	71	_	68	81		

## **AC ELECTRICAL CHARACTERISTICS**

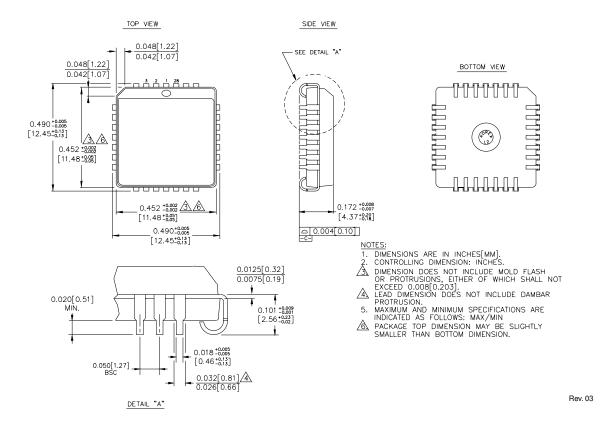
VEE = VEE (Min.) to VEE (Max.); VCC = VCCO = GND

		TA = 0°C			TA = +25°C			TA = +85°C				
Symbol	Parameter	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Unit	Condition
tPD	Propagation Delay to Output A Input SEL0 SEL1 SEL2 SEL3	350 500 400 400 400	600 700 675 675 550	850 900 900 900 700	350 500 400 400 400	600 700 675 675 550	850 900 900 900 700	350 500 400 400 400	600 700 675 675 550	850 900 900 900 700	ps	_
tskew	Within-Device Skew	_	50	_	_	50	_	_	50	_	ps	1
tr tf	Rise/Fall Times 20–80%	275	400	550	275	400	550	275	400	550	ps	_

#### Note:

1. Within-device skew is defined as the difference in the A to Q delay between the 16 different A inputs.

### 28-PIN PLCC (J28-1)



### MICREL, INC. 2180 FORTUNE DRIVE SAN JOSE, CA 95131 USA

TEL + 1 (408) 944-0800 FAX + 1 (408) 474-1000 WEB http://www.micrel.com

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