imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China





QUINT 2-INPUT AND/NAND GATE

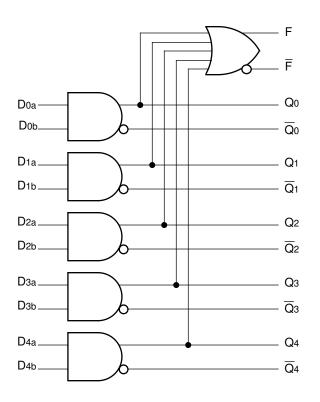
FEATURES

- 600ps max. propagation delay
- Extended 100E VEE range of -4.2V to -5.5V
- True and complementary outputs
- OR/NOR function outputs
- Fully compatible with Industry standard 10KH, 100K I/O levels
- Internal 75KΩ input pulldown resistors
- Fully compatible with Motorola MC10E/100E104
- Available in 28-pin PLCC package

DESCRIPTION

The SY10/100E104 are quint 2-input AND/NAND gates designed for use in new, high-performance ECL systems. The E104 also features a function output, F, which is the OR of all five AND gate outputs, while \overline{F} is the NOR. Both true and complementary outputs are provided.

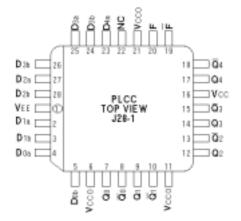
BLOCK DIAGRAM



PIN NAMES

Pin	Function
Dna, Dnb	Data Inputs
Q0-Q4	AND Outputs
$\overline{Q}0-\overline{Q}4$	NAND Outputs
F	OR Output
F	NOR Output
Vcco	Vcc to Output

PACKAGE/ORDERING INFORMATION



28-Pin PLCC (J28-1)

Ordering Information⁽¹⁾

Part Number	Package Type	Operating Range	Package Marking	Lead Finish		
SY10E104JI	J28-1	Industrial	SY10E104JI	Sn-Pb		
SY10E104JITR ⁽²⁾	J28-1	Industrial	SY10E104JI	Sn-Pb		
SY100E104JI	J28-1	Industrial	SY100E104JI	Sn-Pb		
SY100E104JITR ⁽²⁾	J28-1	Industrial	SY100E104JI	Sn-Pb		
SY10E104JC	J28-1	Commercial	SY10E104JC	Sn-Pb		
SY10E104JCTR ⁽²⁾	J28-1	Commercial	SY10E104JC	Sn-Pb		
SY100E104JC	J28-1	Commercial	SY100E104JC	Sn-Pb		
SY100E104JCTR ⁽²⁾	J28-1	Commercial	SY100E104JC	Sn-Pb		
SY10E104JY ⁽³⁾	J28-1	Industrial	SY10E104JY with Pb-Free bar-line indicator	Matte-Sn		
SY10E104JYTR ^(2, 3)	J28-1	Industrial	SY10E104JY with Pb-Free bar-line indicator	Matte-Sn		
SY100E104JY ⁽³⁾	J28-1	Industrial	SY100E104JY with Pb-Free bar-line indicator	Matte-Sn		
SY100E104JYTR ^(2, 3)	J28-1	Industrial	SY100E104JY with Pb-Free bar-line indicator	Matte-Sn		

Notes:

1. Contact factory for die availability. Dice are guaranteed at $T_A = 25^{\circ}C$, DC Electricals only.

2. Tape and Reel.

3. Pb-Free package is recommended for new designs.

LOGIC EQUATION

 $\mathsf{F} = (\mathsf{D0a} \bullet \mathsf{D0b}) + (\mathsf{D1a} \bullet \mathsf{D1b}) + (\mathsf{D2a} \bullet \mathsf{D2b}) + (\mathsf{D3a} \bullet \mathsf{D3b}) + (\mathsf{D4a} \bullet \mathsf{D4b})$

DC ELECTRICAL CHARACTERISTICS⁽¹⁾

VEE = VEE (Min.) to VEE (Max.); VCC = VCCO = GND

		TA = −40°C		TA = 0°C			TA = +25°C			TA = +85°C				
Symbol	Parameter	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Unit
Іін	Input HIGH Current	_	_	200	_	_	200	_	_	200			200	μA
IEE	Power Supply Current 10E 100E		38 38	46 46		38 38	46 46		38 38	46 46		38 44	46 53	mA

Note:

1. Specification for packaged product only.

AC ELECTRICAL CHARACTERISTICS⁽²⁾

VEE = VEE (Min.) to VEE (Max.); VCC = VCCO = GND

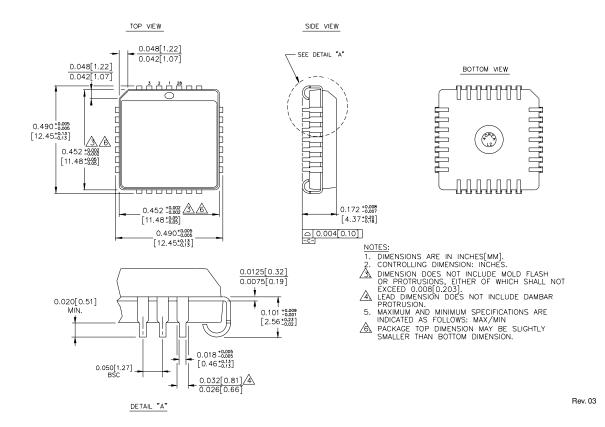
		TA	TA = -40°C			TA = 0°C			TA = +25°C			TA = +85°C		
Symbol	Parameter	Min.	Тур.	Max.	Unit									
tpd	Propagation Delay to Output D to C D to F		385 725	600 1000	225 500	385 725	600 1000	225 500	385 725	600 1000	225 500	385 725	600 1000	ps
tskew	Within-Device Skew, D to $Q^{(1)}$	_	75	_	_	75			75			75	_	ps
tr tf	Rise/Fall Time 20% to 80% C	275 300	425 475	700 700	ps									

Notes:

1. Within-device skew is defined as identical transitions on similar paths through a device.

2. Specification for packaged product only.

28-PIN PLCC (J28-1)



MICREL, INC. 2180 FORTUNE DRIVE SAN JOSE, CA 95131 USA

TEL + 1 (408) 944-0800 FAX + 1 (408) 474-1000 WEB http://www.micrel.com

The information furnished by Micrel in this data sheet is believed to be accurate and reliable. However, no responsibility is assumed by Micrel for its use. Micrel reserves the right to change circuitry and specifications at any time without notification to the customer.

Micrel Products are not designed or authorized for use as components in life support appliances, devices or systems where malfunction of a product can reasonably be expected to result in personal injury. Life support devices or systems are devices or systems that (a) are intended for surgical implant into the body or (b) support or sustain life, and whose failure to perform can be reasonably expected to result in a significant injury to the user. A Purchaser's use or sale of Micrel Products for use in life support appliances, devices or systems is at Purchaser's own risk and Purchaser agrees to fully indemnify Micrel for any damages resulting from such use or sale.

© 2006 Micrel, Incorporated.