

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









50mm (2.0 INCH) 5x7 DOT MATRIX DISPLAY

Part Number: TBA20-12EGWA

High Efficiency Red

Green

Features

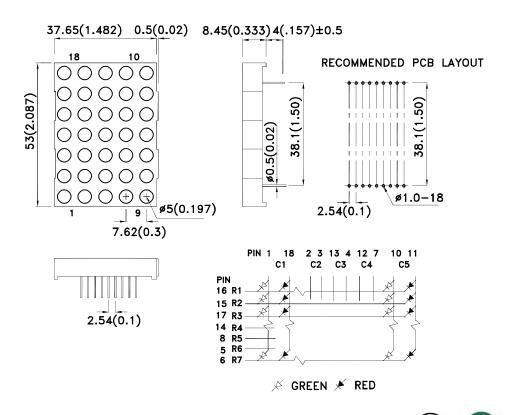
- 2.0 inch matrix height.
- Dot size 5mm.
- Low current operation.
- High contrast and light output.
- Stackable horizontally.
- Easy mounting on P.C. boards or sockets.
- Categorized for luminous intensity.
- Mechanically rugged.
- Standard : gray face, white dot.
- RoHS compliant.

Description

The High Efficiency Red source color devices are made with Gallium Arsenide Phosphide on Gallium Phosphide Orange Light Emitting Diode.

The Green source color devices are made with Gallium Phosphide Green Light Emitting Diode.

Package Dimensions & Internal Circuit Diagram



Notes

1. All dimensions are in millimeters (inches), Tolerance is ±0.25(0.01")unless otherwise noted.

2. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

SPEC NO: DSAD2452 REV NO: V.8

APPROVED: WYNEC CHECKED: Joe Lee

DATE: APR/02/2011 DRAWN: J.Yu PAGE: 1 OF 7 ERP: 1332000289

Selection Guide

Part No.	Dice	Lens Type	lv (ucd) [1] @ 10mA		Description
			Min.	Тур.	·
TBA20-12EGWA	High Efficiency Red (GaAsP/GaP)	White Diffused	5600	10000	Column Anode
	Green (GaP)	Willie Dillused	5600	15000	Column Anode

Electrical / Optical Characteristics at TA=25°C

Symbol	Parameter	Device	Тур.	Max.	Units	Test Conditions
λpeak	Peak Wavelength	High Efficiency Red Green	627 565		nm	I==20mA
λD [1]	Dominant Wavelength	High Efficiency Red Green	625 568		nm	I==20mA
Δλ1/2	Spectral Line Half-width	High Efficiency Red Green	45 30		nm	I==20mA
С	Capacitance	High Efficiency Red Green	15 15		pF	VF=0V;f=1MHz
VF [2]	Forward Voltage	High Efficiency Red Green	2.0 2.2	2.5 2.5	V	I==20mA
lR	Reverse Current	High Efficiency Red Green		10	uA	VR=5V

Notes:

Absolute Maximum Ratings at TA=25°C

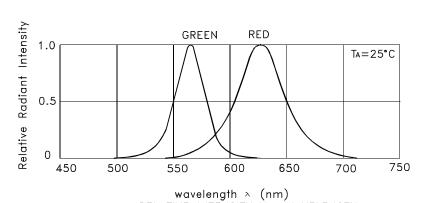
Parameter	High Efficiency Red	Green	Units		
Power dissipation	75	62.5	mW		
DC Forward Current	30	25	mA		
Peak Forward Current [1]	160	140	mA		
Reverse Voltage	ţ	V			
Operating/Storage Temperature	-40°C To +85°C				
Lead Solder Temperature [2]	260°C For 3-5 Seconds				

- 1. 1/10 Duty Cycle, 0.1ms Pulse Width.
 2. 2mm below package base.

DATE: APR/02/2011 SPEC NO: DSAD2452 **REV NO: V.8** PAGE: 2 OF 7 APPROVED: WYNEC **CHECKED:** Joe Lee DRAWN: J.Yu ERP: 1332000289

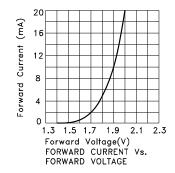
Note: 1. Luminous intensity/ luminous Flux: +/-15%.

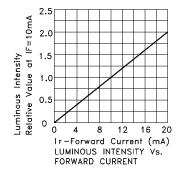
^{1.}Wavelength: +/-1nm. 2. Forward Voltage: +/-0.1V.

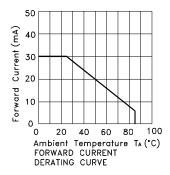


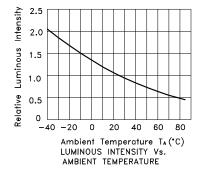
RELATIVE INTENSITY Vs. WAVELENGTH

TBA20-12EGWA High Efficiency Red



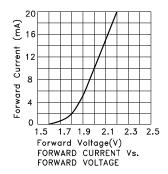


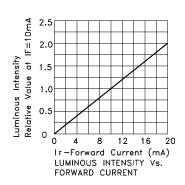


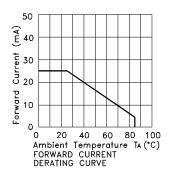


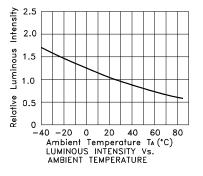
SPEC NO: DSAD2452 REV NO: V.8 DATE: APR/02/2011 PAGE: 3 OF 7
APPROVED: WYNEC CHECKED: Joe Lee DRAWN: J.Yu ERP: 1332000289

Green

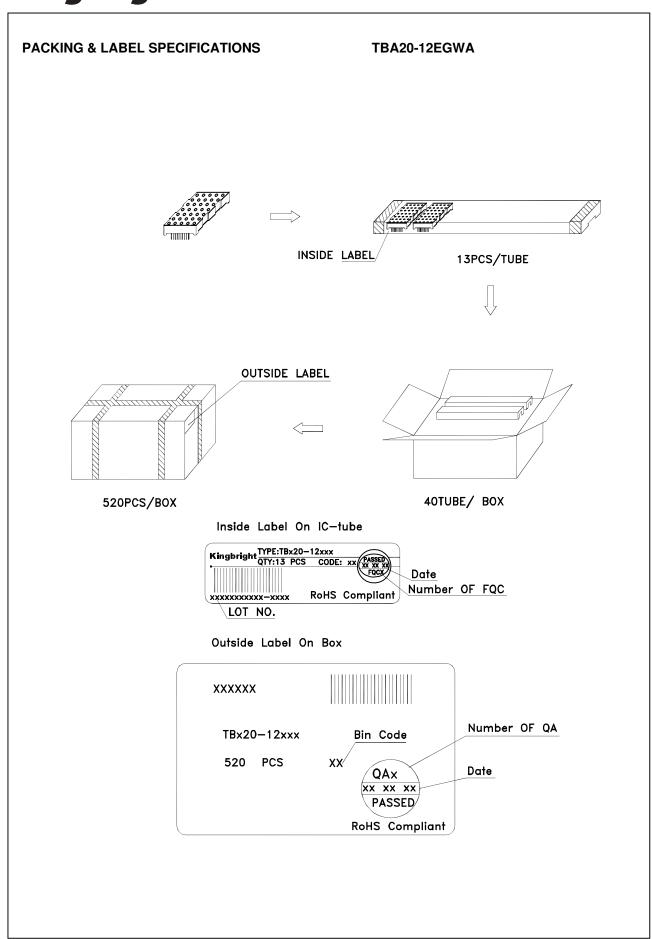








SPEC NO: DSAD2452 REV NO: V.8 DATE: APR/02/2011 PAGE: 4 OF 7
APPROVED: WYNEC CHECKED: Joe Lee DRAWN: J.Yu ERP: 1332000289

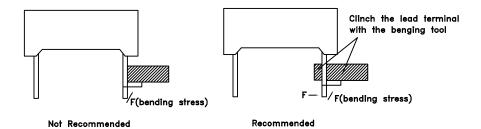


SPEC NO: DSAD2452 APPROVED: WYNEC REV NO: V.8 CHECKED: Joe Lee DATE: APR/02/2011 DRAWN: J.Yu PAGE: 5 OF 7 ERP: 1332000289

THROUGH HOLE DISPLAY MOUNTING METHOD

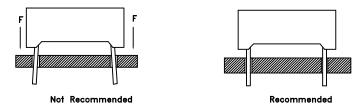
Lead Forming

Do not bend the component leads by hand without proper tools. The leads should be bent by clinching the upper part of the lead firmly such that the bending force is not exerted on the plastic body.



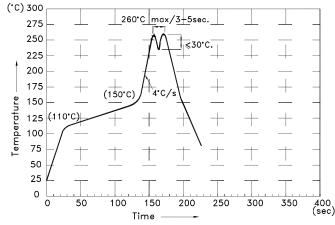
Installation

- 1. The installation process should not apply stress to the lead terminals.
- 2. When inserting for assembly, ensure the terminal pitch matches the substrate board's hole pitch to prevent spreading or pinching the lead terminals.



DISPLAY SOLDERING CONDITIONS

Wave Soldering Profile For Lead-free Through-hole LED.



NOTES:

- 1.Recommend the wave temperature 245°C~260°C.The maximum soldering temperature should be less than 260°C.
- 2.Do not apply stress on epoxy resins when temperature is over 85°C.
- 3. The soldering profile apply to the lead free soldering (Sn/Cu/Ag alloy).
- 4.During wave soldering , the PCB top-surface temperature should be kept below $105^{\circ}\mathrm{C}$

5.No more than once.

SPEC NO: DSAD2452 REV NO: V.8 DATE: APR/02/2011 PAGE: 6 OF 7
APPROVED: WYNEC CHECKED: Joe Lee DRAWN: J.Yu ERP: 1332000289

Soldering General Notes:

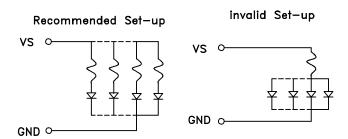
- a. Through—hole displays are incompatible with reflow soldering.
- b. If components will undergo multiple soldering processes, or other processes where the components may be subjected to intense heat, please check with Kingbright for compatibility.

CLEANING

- 1.Mild "no-clean" fluxes are recommended for use in soldering.
- 2. If cleaning is required, Kingbright recommends to wash components with water only. Do not use harsh organic solvents for cleaning, because they may damage the plastic parts .And the devices should not be washed for more than one minute.

CIRCUIT DESIGN NOTES

- 1.Protective current-limiting resistors may be necessary to operate the Displays.
- 2.LEDs mounted in parallel should each be placed in series with its own current—limiting resistor.



SPEC NO: DSAD2452 APPROVED: WYNEC REV NO: V.8 CHECKED: Joe Lee DATE: APR/02/2011 DRAWN: J.Yu PAGE: 7 OF 7 ERP: 1332000289