



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



## Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China

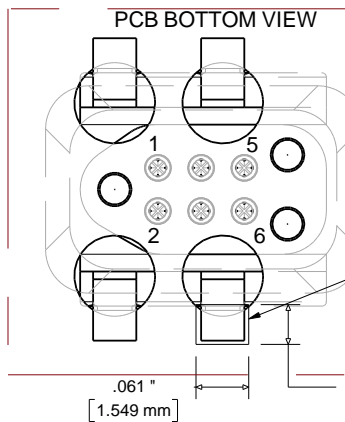
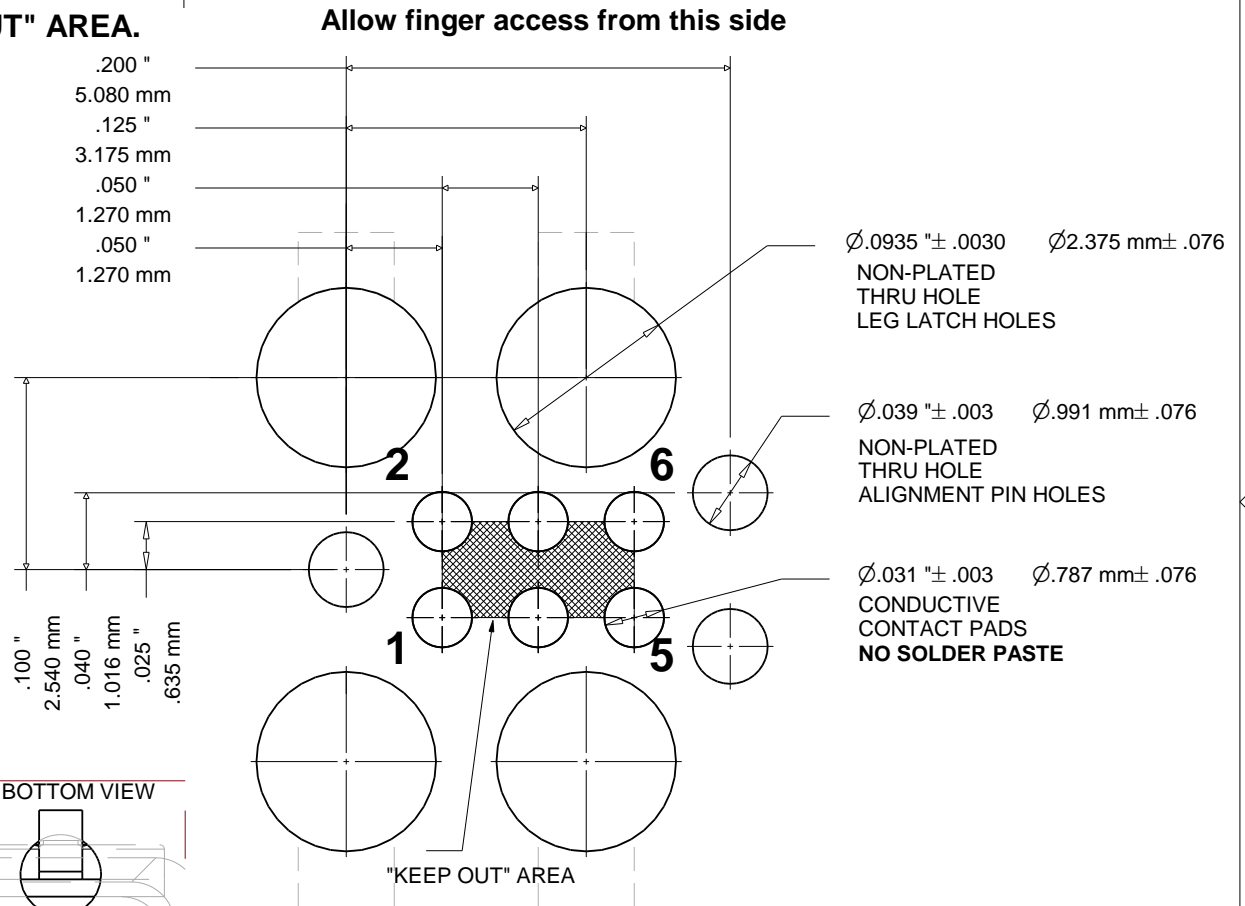


# Notes:

1. Allow clearance for finger squeeze access from the two sides shown.
2. No tracks or vias in the shaded "KEEP OUT" AREA.
3. No other track or signal within .02" of any contact pad.
4. Do not allow solder paste on the contact pads. It is essential to eliminate any hole in the solder paste-mask (solder stencil) layer. If this is not possible make the contact pads thru-hole with a finished hole size of .008" or less.
5. Leg and alignment holes may be plated and used as vias but ensure minimum finished hole size meets specified tolerances.
6. To prevent purchasing confusion, please specify DNL in your BOM.
7. If using a proto-board fab that plates all through holes, make sure all holes are defined as plated in the footprint.

## FOOTPRINT FOR TC2030-IDC PLUG-OF-NAILS™ CABLE

ZONE	REV	TC2030-IDC-FP	5/17/11	MS
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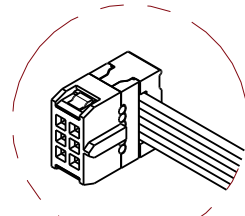
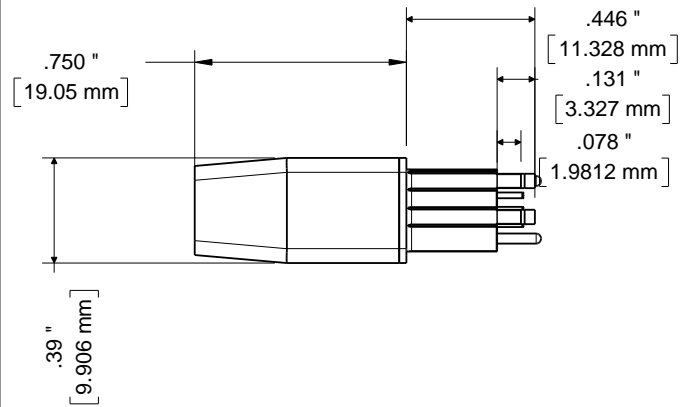
ALLOW A .061 X .046 SQUARE AREA OF CLEARANCE FOR EACH FOOT

SIZE <b>A</b>	FSCM NO.	DWG NO. <b>FOOTPRINT FOR TC2030-IDC-FP</b>	REV <b>A</b>
SCALE	www.TAG-CONNECT.com		SHEET 1 of 3

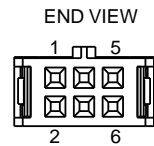
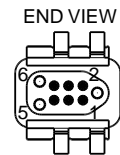
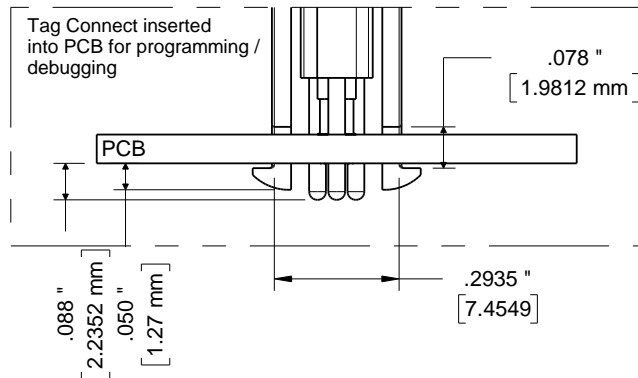
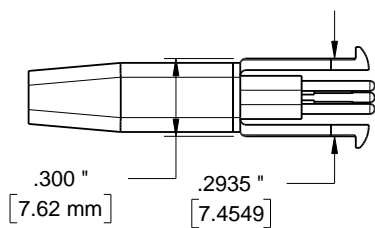
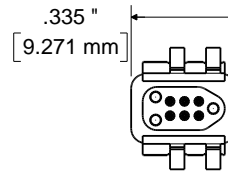
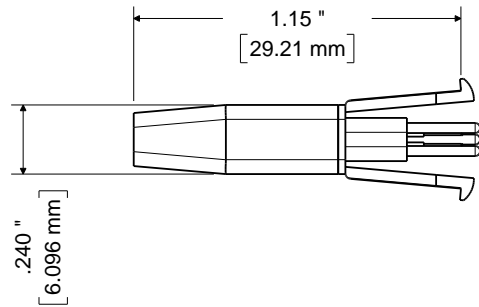
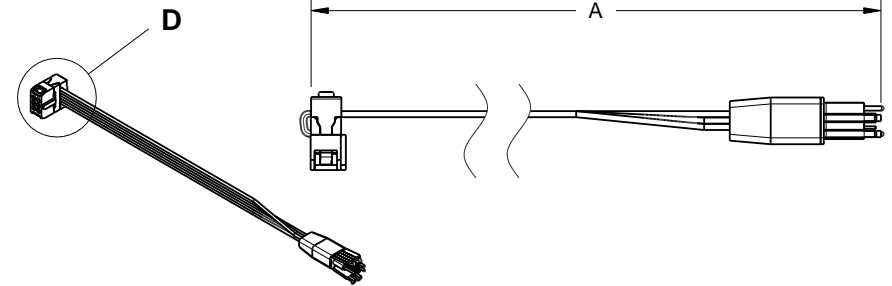
**NOTE:**  
Dimensions subject to change

# TC2030-IDC PLUG-OFF-NAILS™ CABLE

ZONE	A	TC2030-IDC	5/17/11	MS
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Detail D



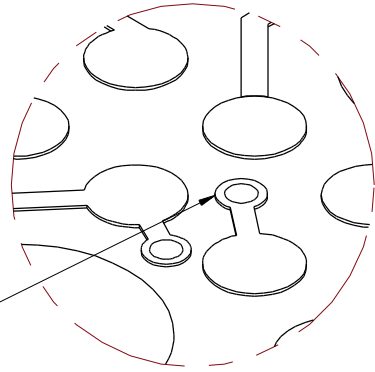
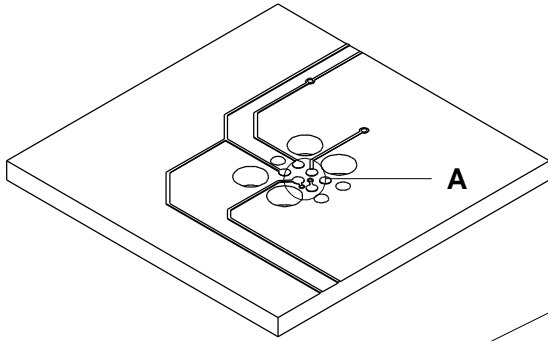
TAG 6 PIN	IDC 6pin
1	1
2	2
3	3
4	4
5	5
6	6

PART NUMBER	A	
	IN	MM
TC2030-IDC	6	152.4
TC2030-IDC-10	10	254

SIZE <b>A</b>	FSCM NO.	DWG NO. <b>TC2030-IDC</b>	REV <b>A</b>
SCALE	www.TAG-CONNECT.com		SHEET 2 Of 3

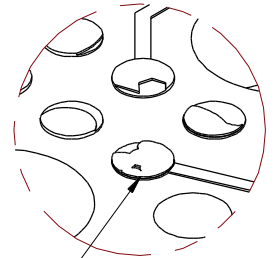
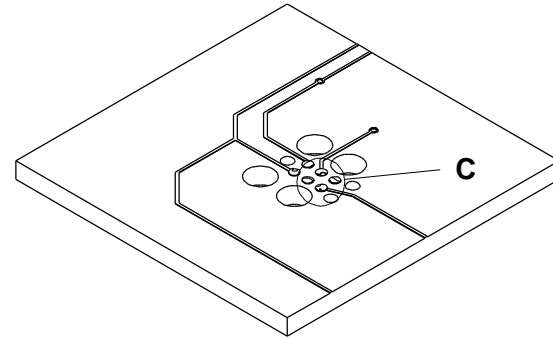


# Layout Problems to Avoid



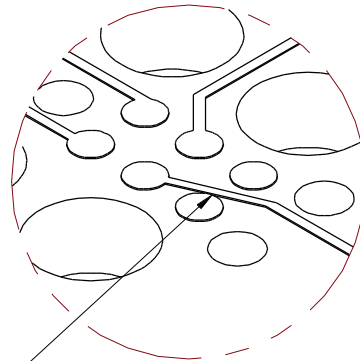
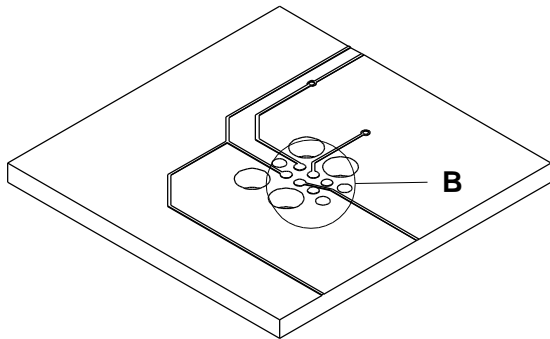
Detail A

Vias between contact pads



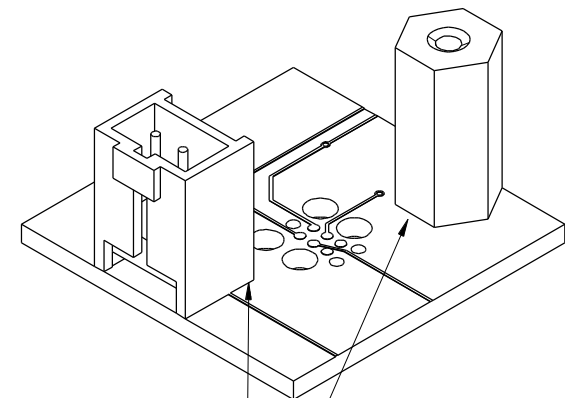
Detail C

Do not deposit solder paste on contact pads. View Solder Paste Mask (Solder Stencil) layer in pad-stack and eliminate any hole.



Detail B

Traces less than .02" away from contact pad or in "KEEP OUT" area



Large componets blocking finger access to the Tag-Connect leg clips