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TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SU04F, TC7SU04FU

Inverter

The TC7SU04 is a high speed C^2MOS Inverter fabricated with silicon gate C^2MOS technology.

It achieves high speed operation similar to equivalent LSTTL while maintaining the $\rm C^2MOS$ low power dissipation.

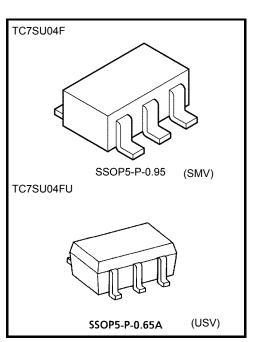
The internal circuit is composed of single stages inverter, it can be applied for crystal oscillation.

The input is equipped with protection circuits against static discharge or transient excess voltage.

Output currents are 1/2 compared to TC74HC series models.

Features

- High speed: $t_{pd} = 7 \text{ ns}$ (typ.) at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 1 \ \mu A \ (max)$ at $Ta = 25^{\circ}C$
- High noise immunity: $V_{\text{NIH}} = V_{\text{NIL}} = 10\% V_{\text{CC}}$ (min)
- Output drive capability: 5 LSTTL loads
- Symmetrical output impedance: $|I_{OH}| = I_{OL} = 2 \text{ mA} (min)$
- Balanced propagation delay time: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: V_{CC} (opr) = 2 to 6 V



Weight SSOP5-P-0.95: 0.016 g (typ.) SSOP5-P-0.65A: 0.006 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Supply voltage range	V _{CC}	-0.5 to 7	V	
DC input voltage	V _{IN}	-0.5 to V_{CC} + 0.5	V	
DC output voltage	V _{OUT}	-0.5 to V_{CC} + 0.5	V	
Input diode current	I _{IK}	±20	mA	
Output diode current	IOK	±20	mA	
DC output current	IOUT	±12.5	mA	
DC V _{CC} /ground current	ICC	±25	mA	
Power dissipation	PD	200	mW	
Storage temperature range	T _{stg}	-65 to 150	°C	
Lead temperature (10 s)	ΤL	260	°C	

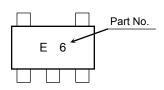
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

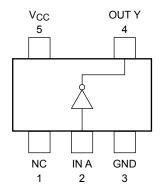
Start of commercial production 1987-09

TOSHIBA

Marking



Pin Configuration (top view)



Logic Diagram



А	Y
L	Н
Н	L

Truth Table

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2 to 6	V
Input voltage	V _{IN}	0 to V _{CC}	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature range	T _{opr}	-40 to 85	°C

Electrical Characteristics

DC Electrical Characteristics

Characteristics Symbol Test Condition		Svmbol	Symbol Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
		$V_{CC}(V)$	Min	Тур.	Max	Min	Max	Unit			
					2.0	1.7	_	_	1.7	_	
Input voltage —	High level	VIH	_		4.5	3.6	_	_	3.6	_	
					6.0	4.8	_	_	4.8	_	V
			_		2.0		_	0.3	_	0.3	V
	Low level	VIL			4.5		_	0.9	_	0.9	
					6.0		_	1.2	_	1.2	
Output voltage	High level V _C			I _{OH} = −20 μA	2.0	1.8	2.0	_	1.8	—	
		Vон	$V_{IN} = V_{IL}$		4.5	4.0	4.5	_	4.0	—	
					6.0	5.5	5.9	_	5.5	_	
				I _{OH} = -2 mA	4.5	4.18	4.31	_	4.13	—	
				I _{OH} = -2.6 mA	6.0	5.68	5.80	_	5.63	—	V
	Low level V _{OL}		V _{OL} V _{IN} = V _{IH}	I _{OL} = 20 μA	2.0	_	0	0.2	_	0.2	
					4.5	_	0	0.2	_	0.5	
		V _{OL}			6.0	_	0	0.5	_	0.5	
				I _{OL} = 2 mA	4.5	-	0.17	0.26	_	0.33	
				I _{OL} = 2.6 mA	6.0		0.18	0.26		0.33	
Input leakage	Input leakage current I _{IN} V _{IN} = V _{CC} or GND		6.0	_	_	±0.1		±1.0	μA		
Quiescent supply current I _{CC} V _{IN} = V _{CC} or GND		6.0	_	_	1.0	-	10.0	μA			

AC Electrical Characteristics (C_L = 15 pF, input $t_r = t_f = 6$ ns, V_{CC} = 5 V)

Characteristics	Symbol	Test Condition	-	l lucit		
		Test Condition	Min	Тур.	Max	Unit
Output transition time	t _{TLH} t _{THL}	_		5	10	ns
Propagation delay time	t _{pLH} t _{pHL}	_	_	7	15	ns

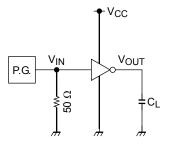
AC Electrical Characteristics ($C_L = 50 \text{ pF}$, input $t_r = t_f = 6 \text{ ns}$)

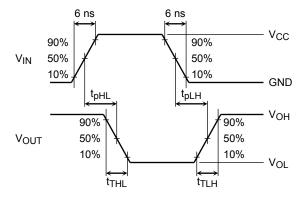
Characteristics	O makes l	Test Condition		Ta = 25°C			Ta = -40	L Insite	
Characteristics	Symbol		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Output transition time	tтін tтні	_	2.0	_	50	125	—	155	ns
			4.5	_	14	25	—	31	
			6.0	—	12	21	—	26	
Propagation delay time	^t pLH t _{pHL}	_	2.0	_	48	100	—	125	ns
			4.5	_	12	20	—	25	
			6.0	_	9	17	—	21	
Input capacitance	C _{IN}	—		_	5	10	—	10	pF
Power dissipation capacitance	C _{PD}		(Note)	_	10	_	_	_	pF

Note: C_{PD} defined as the value of internal equivalent capacitance of IC which is calculated from the operating current consumption without load (refer to test circuit).

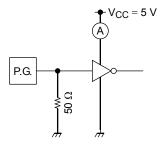
Average operating current can be obtained by the equation hereunder. $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

Switching Characteristics Test Circuit





I_{CC (opr)} Test Circuit



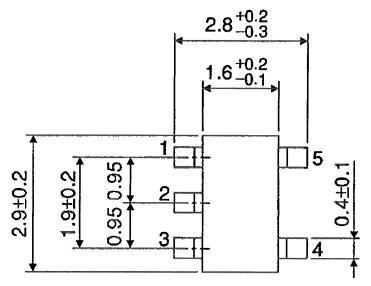
Input waveform is the same as that in case of switching characteristics test.

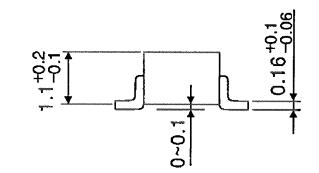
TOSHIBA

Package Dimensions

SSOP5-P-0.95

Unit : mm

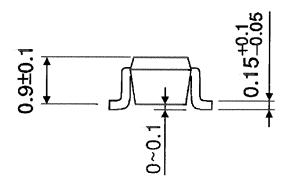




Weight: 0.016 g (typ.)

Package Dimensions

SSOP5-P-0.65A 2.1±0.1 1.25±0.1 0.65 5 1-EE 2.0 ± 0.2 1.3±0. 2-EE N Ö -3-EE 0.65 $\overline{4}$



Weight: 0.006 g (typ.)

Unit : mm

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