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Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







SPEC

Spec No.	TQ3C-8EAF0-E1DDP48-00
Date	May 22, 2008

TYPE: TCG057VGLBA-G20

< 5.7 inch VGA transmissive color TFT with LED backlight, Constant current circuit for LED backlight >

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Issued
Date: May 28,2008
KYDCERA
Hayato LCD Division

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

This specification is subject to change without notice.

Consult Kyocera before ordering.

Original	Original Designed by: Engineering dept.				Confirmed by: QA dept.	
Issue Date	Prepared	Checked	Approved	Checked	Approved	
May 22, 2008	O. Ajisaka	7d Johnson	G Matsumoto	J. Sakaguchi	Ho , Suf	

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Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnity, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.



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Revision record

Date		Designe		Engineering of		Confirmed by	: QA dept.
	Date	Prepa	red	Checked	Approved	Checked	Approved
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1. Application

This document defines the specification of TCG057VGLBA-G20. (RoHS Compliant)

2. Construction and outline

LCD : Transmissive color dot matrix type TFT

Backlight system : LED

Polarizer : Glare treatment

Additional circuit : Timing controller, Power supply (3.3V input)

(with constant current circuit for LED Backlight)

3. Mechanical specifications

Item	Specification	
Outline dimensions	127.2 (W)× 100.4 (H) × 5.7 (D)	mm
Active area	115.2 (W) × 86.4 (H) (14.4cm / 5.7 inch (Diagonal))	mm
Dot format	640×(B,G,R) (W) × 480 (H)	dot
Dot pitch	0.06 (W) × 0.18 (H)	mm
Base color *1	Normally White	-
Mass	110	g

^{*1} Due to the characteristics of the LCD material, the color varies with environmental temperature.



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4. Absolute maximum ratings

4-1. Electrical absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage for logic	$V_{ m DD}$	0	4.0	V
Input signal voltage *1	$V_{\rm IN}$	-0.3	6.0	V
Supply voltage for backlight	$V_{\rm IN}B$	0	6.0	V
Backlight ON-OFF	BLEN	0	$V_{\mathrm{IN}}\mathrm{B}$	V
Brightness adjust voltage	VBRT	0	V _{IN} B	V

^{*1} Input signal: CK, R0 ~ R5, G0 ~ G5, B0 ~ B5, Hsync, Vsync, ENAB, R/L, U/D

4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	*1	Тор	-20	70	$^{\circ}\mathrm{C}$
Storage temperature	*2	T_{STO}	-30	80	$^{\circ}\mathrm{C}$
Operating humidity	*3	Нор	10	*4	%RH
Storage humidity	*3	Нѕто	10	*4	%RH
Vibration		-	*5	*5	-
Shock		-	*6	*6	-

^{*1} Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.

Store LCD panels at normal temperature/humidity. Keep them free from vibration and shock. An LCD panel that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.

(Please refer to "Precautions for Use" for details.)

Temp. > 40°C, Absolute humidity shall be less than 85%RH at 40°C.

*5

Frequency	10 ~ 55 Hz	Acceleration value
Vibration width	0.15mm	$(0.3 \sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz 1 minutes

2 hours in each direction X, Y, Z (6 hours total) EIAJ ED-2531

*6 Acceleration: 490 m/s², Pulse width: 11 ms

3 times in each direction: $\pm X$, $\pm Y$, $\pm Z$

 $\hbox{EIAJ ED-}2531$



^{*2} Temp. = -30°C < 48h , Temp. = 80°C < 168h

^{*3} Non-condensing

^{*4} Temp. ≤ 40°C, 85%RH Max.

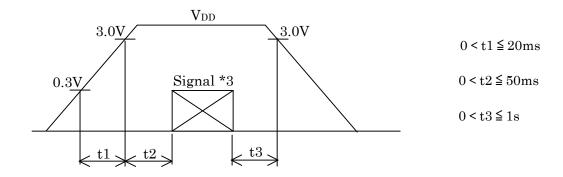
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5. Electrical characteristics

Temp. = $-20 \sim 70^{\circ}$ C

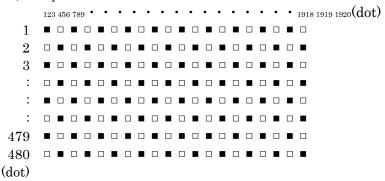
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage for logic *1	$ m V_{DD}$	-	3.0	3.3	3.6	V
Current consumption for logic	${ m I}_{ m DD}$	*2	-	170	220	mA
Permissive input ripple voltage	V_{RP}	-	-	-	100	mVp-p
I	$V_{ m IL}$	"Low" level	0	-	$0.3V_{\mathrm{DD}}$	V
Input signal voltage *3	V_{IH}	"High" level	$0.7 V_{\mathrm{DD}}$	-	V_{DD}	V

*1 $V_{\rm DD}$ -turn-on conditions



*2 Display pattern:

$$V_{DD} = 3.3V$$
, Temp. = 25°C



*3 Input signal : CK, R0 ~ R5, G0 ~ G5, B0 ~ B5, H_{SYNC} , V_{SYNC} , ENAB, R/L, U/D



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6. Optical characteristics

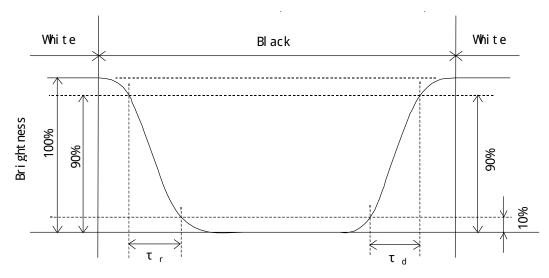
Measuring spot $= \varphi$ 6.0mm, Temp. = 25°C

Item		Symbol	Condition	Min.	Тур.	Max.	Unit
D .:	Rise	τr	θ =φ =0°	-	10	-	ms
Response time	Down	τd	θ =φ =0°	-	25	-	ms
T7: 1				-	80	-	1
Viewing angle View direction	_	θ _{LOWER}	CR ≧ 5	-	80	-	deg.
: 6 o'cloc (Gray in		ф сегт	UN ≦ 0	-	80	-	d
(Gray III	version)	ϕ right		-	80	-	deg.
Contrast ratio		CR	θ =φ =0°	300	500	-	-
Brightness		L	IF=15mA/Line	175	250	-	cd/m²
	Red	X	θ =φ =0°	0.56	0.61	0.66	
	Rea	У	υ –ψ –υ	0.32	0.37	0.42	
	Conser	X	θ =φ =0°	0.29	0.34	0.39	
Chromaticity	Green	У	υ –φ –υ	0.52	0.57	0.62	_
coordinates	Blue	X	θ =φ =0°	0.09	0.14	0.19	-
	Diue	У	υ -ψ -υ	0.06	0.11	0.16	
	White	X	θ =φ =0°	0.27	0.32	0.37	
	willte	У	υ -ψ -υ	0.29	0.34	0.39	

6-1. Definition of contrast ratio

CR(Contrast ratio) = Brightness with all pixels "White"
Brightness with all pixels "Black"

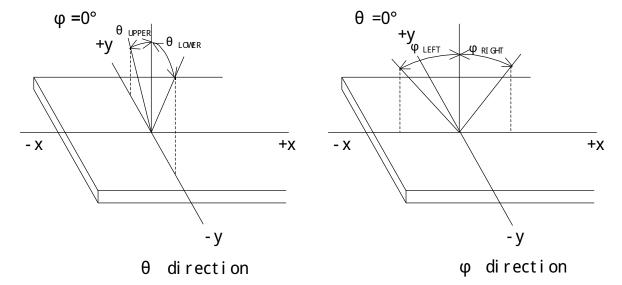
6-2. Definition of response time



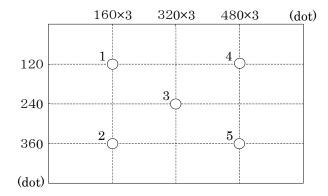


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6-3. Definition of viewing angle



6-4. Brightness measuring points



- 1) Rating is defined on the average in the viewing area.
- 2) Measured 30 minutes after the LED is powered on. (Ambient temp. = 25°C)



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7. Interface signals

No.	Symbol	Description	I/O	Note
1	GND	GND	-	
2	CK	Clock signal for sampling each data signal	I	
3	H_{SYNC}	Horizontal synchronous signal (negative)	I	
4	$V_{ m SYNC}$	Vertical synchronous signal (negative)	I	
5	GND	GND	-	
6	R0	RED data signal (LSB)	I	
7	R1	RED data signal	I	
8	R2	RED data signal	I	
9	R3	RED data signal	I	
10	R4	RED data signal	I	
11	R5	RED data signal (MSB)	I	
12	GND	GND	-	
13	G0	GREEN data signal (LSB)	I	
14	G1	GREEN data signal	I	
15	G2	GREEN data signal	I	
16	G3	GREEN data signal	I	
17	G4	GREEN data signal	I	
18	G5	GREEN data signal (MSB)	I	
19	GND	GND	-	
20	В0	BLUE data signal (LSB)	I	
21	B1	BLUE data signal	I	
22	B2	BLUE data signal	I	
23	В3	BLUE data signal	I	
24	B4	BLUE data signal	I	
25	В5	BLUE data signal (MSB)	I	
26	GND	GND	-	
27	ENAB	Signal to settle the horizontal display position (positive)	I	*1
28	$V_{ m DD}$	3.3V power supply	-	
29	V_{DD}	3.3V power supply	-	
30	R/L	Horizontal display mode select signal L: Normal , H: Left / Right reverse mode	I	*2
31	U/D	Vertical display mode select signal H : Normal , L : Up / Down reverse mode	I	
32	NC	No connect	-	
33	$V_{\rm IN}B$	Power supply for LED backlight	-	
34	$V_{\rm IN}B$	Power supply for LED backlight	-	
35	$V_{\mathrm{IN}}\mathrm{B}$	Power supply for LED backlight	-	
36	BLEN	Backlight ON-OFF (H:ON, L:OFF)	-	
37	VBRT	Brightness adjust voltage	-	
38	GNDB	GND for LED backlight	-	
39	GNDB	GND for LED backlight	-	
40	GNDB	GND for LED backlight	-	

LCD connector : IMSA-9681S-40A-GF (IRISO)

Recommended matching FFC or FPC : 0.5mm pitch



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*1 The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined.

Don't keep ENAB "High" during operation.

*2



R/L = LU/D = H



R/L = HU/D = H



$$R/L = L$$
$$U/D = L$$



$$R/L = H$$
$$U/D = L$$

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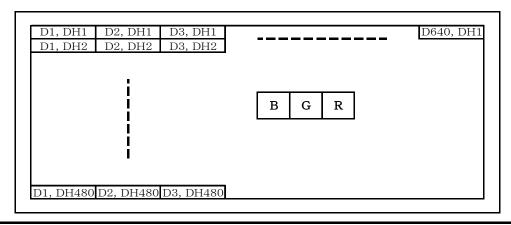
8. Input timing characteristics

8-1. Timing characteristics

	Item	Symbol	Min	Тур	Max	Unit	Note
Clock	Frequency	1/Tc	22.66	25.18	27.69	MHz	
Clock	Duty ratio	Tch/Tc	40	50	60	%	
.	Set up time	Tds	10	_	-	ns	
Data	Hold time	Tdh	10	-	-	ns	
	G 1	TH	30.0	31.8	-	μs	
	Cycle	111	770	800	850	clock	
Horizontal sync. signal	Pulse width	ТНр	5	30	-	clock	
Signar	Set up time	THs	10	_	-	ns	
	Hold time	THh	10	-	-	ns	
	Cycle	TV	515	525	535	line	ENAB=L
			515	525	560	line	with ENAB
Vertical sync. signal	Pulse width	TVp	1	3	5	line	
Signar	Set up time	TVs	10	-	-	ns	
	Hold time	TVh	10	_	-	ns	
	Pulse width	ТЕр	640			clock	
Enable signal (ENAB)	Set up time	TEs	10	_	-	ns	
(EIVID)	Hold time	TEh	10	_	-	ns	
H _{SYNC} - Enable s	ignal phase difference	THE	112	144	175	clock	
$H_{\rm SYNC}$ - $V_{\rm SYNC}$ signal phase difference		/DIIX/	0	_	4	clock	ENAB=L
		THV	10	_	-	ns	with ENAB
Vertical sync. sig	gnal start position	TVE	2	35	76	line	
Horizontal displa	ay period	THd		640		clock	
Vertical display	period	TVd	480			line	

^{*}When ENAB is fixed at "Low", the horizontal display starts from the data of C144 (clock) as shown in 8-3.

8-2. Input Data Signals and Display position on the screen



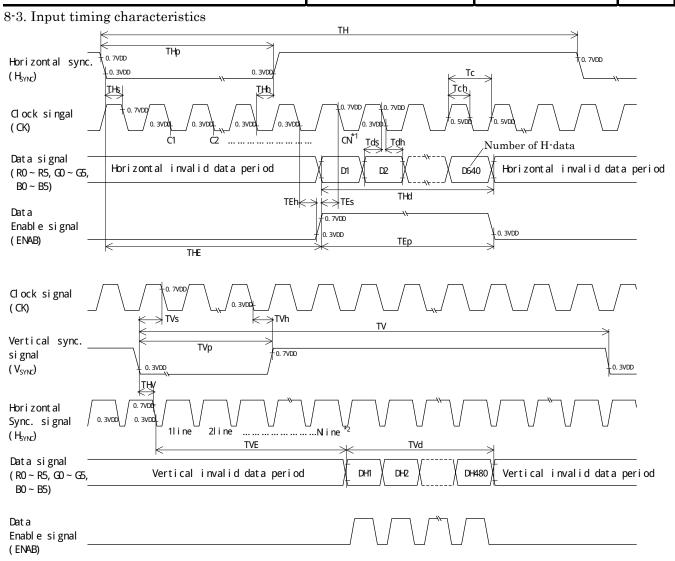


^{*}When ENAB is fixed at "Low", the vertical sync. signal start position is 35 (line) as shown in 8-3.

^{*}In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

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^{*1} When ENAB is fixed at "Low", the horizontal display starts from the data of C144 (clock).

^{*2} When ENAB is fixed at "Low", the vertical sync. signal start position is 35 (line).

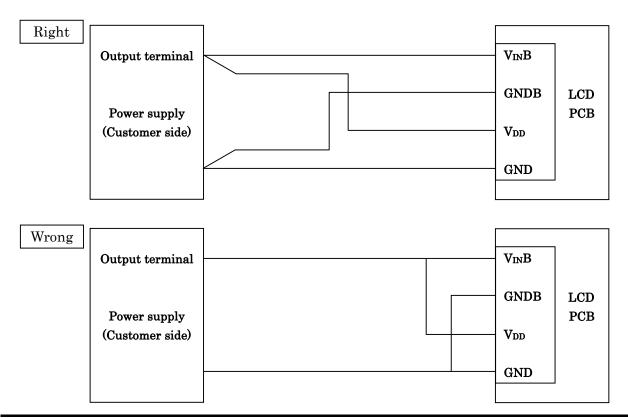
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9. Backlight characteristics

Temp.=25°C

Item	Symbol	Min.	Тур.	Max.	Unit	Note
Supply voltage	$V_{\rm IN} B$	3.0	-	5.5	V	Ta=-20 ~ 70°C
ON-OFF (H)	DLEN	0.8V _{IN} B	-	$V_{\rm IN}B$	V	Ta=-20 ~ 70°C
ON-OFF (L)	BLEN	0	-	$0.2 \mathrm{V_{IN}B}$	V	Ta=-20 ~ 70°C
LED former d armore	IF	14	15	16	4	VBRT=0 ~ 1.4V
LED forward current *1, *2		2.8	3.0	3.2	mA	VBRT=2.8V
Consideration	I D	-	440	570	mA	V _{IN} B=3.3V, IF=15mA
Supply current	$I_{IN}B$	-	280	360	mA	V _{IN} B=5.0V, IF=15mA
Operating life time *3, *4	Т	-	40,000	-	h	IF=15mA, Ta=25°C

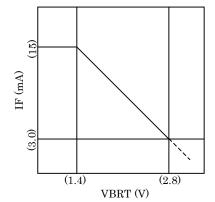
- *1 For each "LED"
- *2 An input current below 5.0mA may reduce the brightness uniformity of the LED backlight. This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.
- ${}^{*}3$ When brightness decrease 50% of initial brightness.
- *4 Life time is estimated data. (Condition: IF=15mA, Ta=25°C in chamber)
- * When you start-up, please charge in sequence of V_{IN}B->BLEN, or VBRT. When you shut-down, please stop in sequence of BLEN and/or VBRT->V_{IN}B.
- * Please do not connect the other than our backlight to this output connector on the PCB.
- * In case V_{DD} and V_{IN}B are supplied by a single power source, V_{DD} & V_{IN}B, and GND are connected directly and separately from the output on the power source. If the common wire are used for V_{DD} & V_{IN}B, and for GND, and are split near the PCB, and connect to each LCD driving circuit and backlight driving circuit, a flicker might be occurred due to a ripple between the both circuit.





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* VBRT-IF characteristics





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10. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

No1. - No5. above indicate

- 1. Year code
- 2. Month code
- 3. Date
- 4. Version Number
- 5. Country of origin (Japan or China)

Year	2008	2009	2010	2011	2012	2013
Code	8	9	0	1	2	3

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	X	Y	Z

11. Warranty

11-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

11-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



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12. Precautions for use

12-1. Installation of the LCD

- 1) A transparent protection plate shall be added to protect the LCD and its polarizer.
- 2) The LCD shall be installed so that there is no pressure on the LSI chips.
- 3) The LCD shall be installed flat, without twisting or bending.
- 4) A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.

12-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

12-3. LCD operation

1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.

12-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

12-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a little Ethanol.
- 4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD module because it will result in damage.
- 7) This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



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13. Reliability test data

Test item	Test condition	Test time	Judgement		
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect	
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect	
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect : No defect : No defect	
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	: No defect: No defect: No defect	
High temp. operation	70°C	500h	Display function Display quality Current consumption	: No defect : No defect : No defect	

^{*} Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

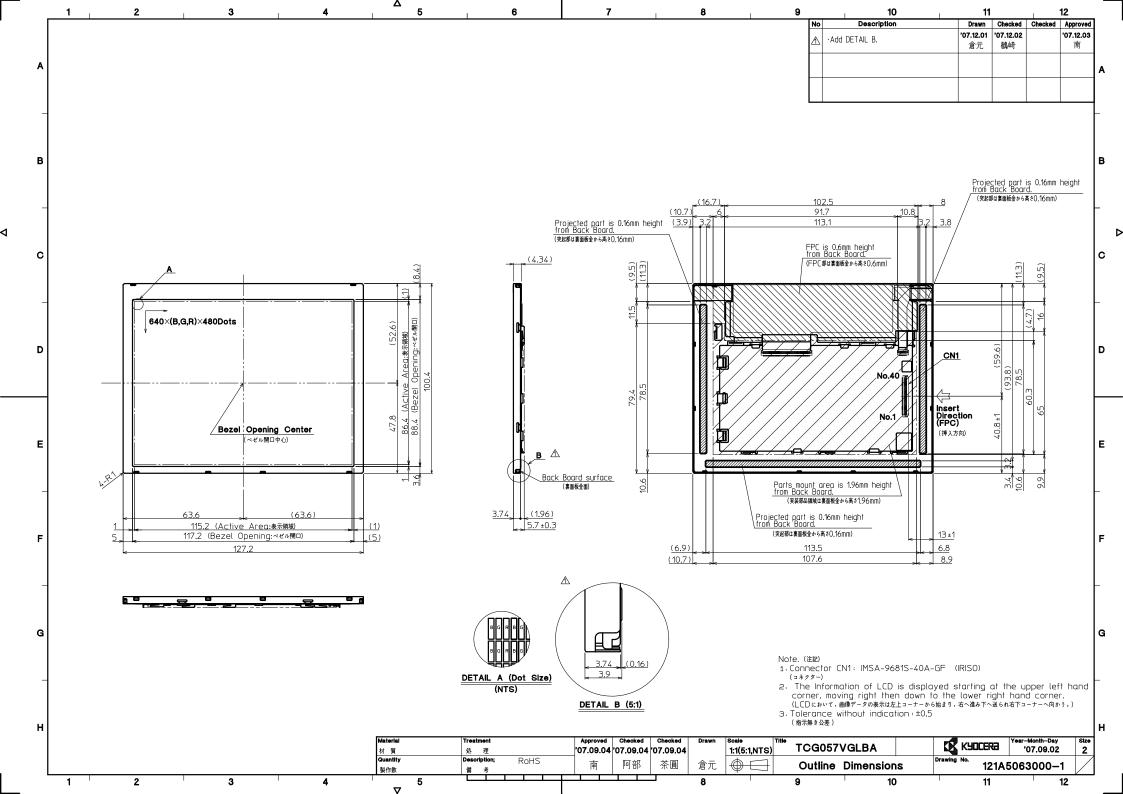


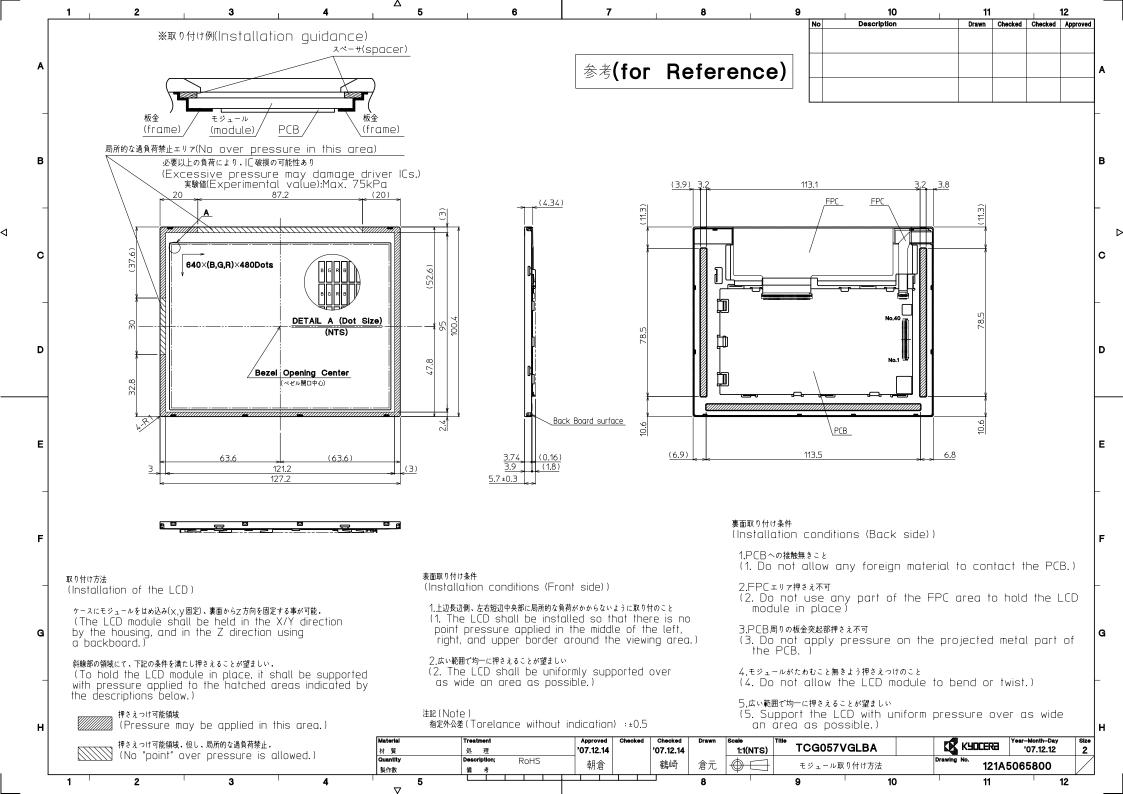
^{*} The LCD is tested in circumstances in which there is no condensation.

^{*} The reliability test is not an out-going inspection.

^{*} The result of the reliability test is for your reference purpose only.

The reliability test is conducted only to examine the LCD's capability.





Spec No.	TQ3C-8EAF0-E2DDP48-00
Date	May 22, 2008

KYOCERA INSPECTION STANDARD

TYPE: TCG057VGLBA-G20

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

Original	Designed by:	Engineering de	pt.	Confirmed by	: QA dept.
Issue Date	Prepared	Checked	Approved	Checked	Approved
May 22, 2008	O. Ajisaka	7d Topuson	G. Matrumoto	J. Sakaguchi	Ho , Suf



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Revision record

	Data	Designe	igned by: Engineering dept.		Confirmed by : QA dept.		
	Date	Prepa	red	Checked	Approved	Checked	Approved
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Visuals specification

1) Note

1) Note		Note						
G 1	1 0	Note						
General	1. Customer identified anomalies not defined within this inspection standard shall be							
	reviewed by Kyocera, and an additional standard shall be determined by mutual							
	consent.							
	2. This inspection standard about the image quality shall be applied to any defect within							
	the active area and shall not be applicable to outside of the area.							
	3. Inspection conditions							
	Lumina	ance	: 500 Lux min.					
	Inspect	ion distance	: 300 mm.					
	Temper	rature	:25 ± 5℃					
	Direction		: Directly above					
Definition of	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the					
inspection item			LCD, even when all "Black" data sent to the screen.					
			Inspection tool: 5% Transparency neutral density filter.					
			Count dot: If the dot is visible through the filter.					
			Don't count dot: If the dot is not visible through the					
			filter.					
			RGBRGB					
			R G B R G B R G B dot defect					
			RGBRGB					
		Black dot defect	The dot is constantly "off" when power applied to the					
			LCD, even when all "White" data sent to the screen.					
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot					
			defects or black dot defects.					
			RGBRGBRGB					
			R G B R G B R G B					
			R G B R G B R G B dot defect					
			Kalakalakala					
	External	Bubble, Scratch,	Visible operating (all pixels "Black" or "White") and non					
	inspection	Foreign particle	operating.					
		(Polarizer, Cell,						
		Backlight)						
		Appearance	Does not satisfy the value at the spec.					
		inspection						
	Others	LED wire	Damaged to the LED wires, connector, pin, functional					
			failure or appearance failure.					
	Definition	Definition of circle size Definition of linear size						
	of size							
		 						
		d =(a +	b)/2					



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2) Standard

2) Standa		ı		Г				
Classification		Inspection item		Judgement standard				
Defect	Dot	Bright dot defect				: 4		
(in LCD	defect					or more		
glass)	glass)		defect	Acceptable number : 5				
				Bright dot spacing : 5 m		: 5 mm	or more	
		2 dot join	Bright dot defect	Acceptable number		: 2		
			Black dot defect	Acceptable number		: 3		
		3 or more dots join		Acceptable number : 0				
		Total dot defects		Acceptable number : 5 Max			X	
	Others		Dark dot	respective				
		(Circle)		Size (mm	n)	Ac	ceptable number	
		, , , , , , , , , , , , , , , , , , , ,		d ≦		110	(Neglected)	
				0.2 < d ≦			5	
				0.4 < d ≦	0.5		3	
				0.5 < d			0	
External	l inspection	Dolowizon ((Caratah)					
(Defect or		Polarizer (Scratch)		Width (mm) Length (mm) A		Acceptable number		
Polarizer				Width (IIIII) W ≤ 0.1	Length (IIIII)		(Negl ect ed)	
					L	≦ 5.0	(Negl ect ed)	
between Polarizer				$0.1 < W \le 0.3$	5.0 < L		0	
and LCD glass)				0.3 < W	-		0	
		Polarizer (Bubble)						
		Folarizer (Bubble)		Size (mm)		Accontable number		
				$d \leq 0.2$		Acceptable number (Neglected)		
				$0.2 < d \le 0.3$		(ivegrecteu)		
				$0.3 < d \le 0.5$		3		
				0.5 < d		0		
		Foreign na	rticle			•		
		Foreign particle (Circular shape)		Size (mm) Acceptable number			contable number	
				d ≤ 0.2		(Neglected)		
				$0.2 < d \le 0.4$		5		
				0.4 < d ≤ 0.5		3		
				0.5 < d		0		
		Foreign particle (Linear shape) Scratch		XX7: 1.1 ()	T (1	()	A . 11 1	
				$\begin{array}{c c} & \text{Width (mm)} \\ & \text{W} \leq 0.03 \end{array}$	Length	(mm)	Acceptable number	
				W ≥ 0.03	_ L ≦ 2.0		(Negl ect ed) (Negl ect ed)	
				$0.03 < W \le 0.1$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		(Negrected)	
] 5.55	$\frac{2.0 < L}{4.0 < L}$		0	
				0.1 < W	- 1.0 \ 1		(According to	
							circular shape)	
							circular sitape/	

