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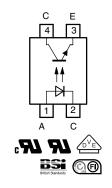
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TCLT100. Series

Vishay Semiconductors

Optocoupler, Phototransistor Output, SOP-4L, Long Mini-Flat Package





DESCRIPTION

The TCLT100. series consists of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 4-lead SOP4L package.

APPLICATIONS

- Switchmode power supplies
- Computer peripheral interface
- Microprocessor system interface

FEATURES

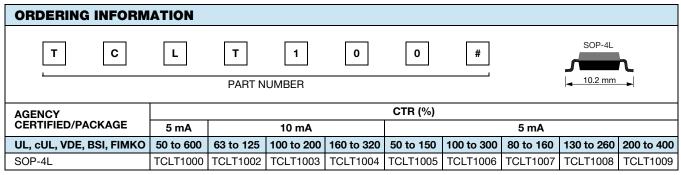
- SMD low profile 4 lead package
- V_{IORM} = 1050 V
- CTR flexibility available see order information
- Special construction
- Extra low coupling capacitance
- DC input with transistor output
- Creepage distance > 8 mm
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

AGENCY APPROVALS

- UL1577, file no. E76222
- CSA (cUL) 22.2 bulletin 5A recognized file no. E-76222
- BSI: BS EN 41003, BS EN 60065 (BS 415), BS EN 60950 (BS 7002), certificate number 7081 and 7402
- DIN EN 60747-5-5 (VDE 0884), available with option 1
- FIMKO (SETI): EN 60950
- CQC

Note

• See the safety standard approval list "Agency Table" for more detailed information.



Note

• Available only on tape and reel.

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RoHS COMPLIANT

(5-2008)



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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
INPUT							
Reverse voltage		V _R	6	V			
Forward current		l _F	60	mA			
Forward surge current	$t_p \le 10 \ \mu s$	I _{FSM}	1.5	А			
Power dissipation		P _{diss}	100	mW			
Junction temperature		Tj	125	°C			
OUTPUT							
Collector emitter voltage		V _{CEO}	70	V			
Emitter collector voltage		V _{ECO}	7	V			
Collector current		Ι _C	50	mA			
Collector peak current	t_p/T = 0.5, $t_p \leq$ 10 ms	I _{CM}	100	mA			
Power dissipation		P _{diss}	150	mW			
Junction temperature		Tj	125	°C			
COUPLER							
Isolation test voltage (RMS)		V _{ISO}	5000	V _{RMS}			
Total power dissipation		P _{tot}	250	mW			
Operating ambient temperature range		T _{amb}	- 55 to + 100	°C			
Storage temperature range		T _{stg}	- 55 to + 125	°C			
Soldering temperature		T _{sld}	260	°C			

Note

• Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT							
Forward voltage	I _F = 50 mA	V _F		1.25	1.6	V	
Junction capacitance	V _R = 0 V, f = 1 MHz	Cj		50		pF	
OUTPUT							
Collector emitter voltage	I _C = 1 mA	V _{CEO}	70			V	
Emitter collector voltage	I _E = 100 μA	V _{ECO}	7			V	
Collector emitter cut-off current	$V_{CE} = 20 \text{ V}, \text{ I}_{F} = 0 \text{ A}$	I _{CEO}		10	100	nA	
COUPLER							
Collector emitter saturation voltage	I _F = 10 mA, I _C = 1 mA	V _{CEsat}			0.3	V	
Cut-off frequency	$\label{eq:VCE} \begin{array}{l} V_{CE} = 5 \; V, \; I_{F} = 10 \; mA, \\ R_{L} = 100 \; \Omega \end{array}$	f _c		110		kHz	
Coupling capacitance	f = 1 MHz	C _k		0.3		pF	

Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.



TCLT100. Series

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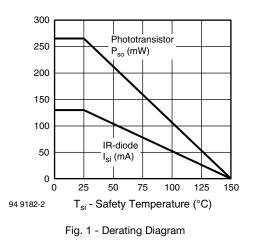
CURRENT TRANSFER RATIO ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
	$V_{CE} = 5 \text{ V}, \text{ I}_{F} = 5 \text{ mA}$	TCLT1000	CTR	50		600	%	
	V _{CE} = 5 V, I _F = 10 mA	TCLT1002	CTR	63		125	%	
		TCLT1003	CTR	100		200	%	
		TCLT1004	CTR	160		320	%	
I _C /I _F	V_{CE} = 5 V, I _F = 1 mA	TCLT1002	CTR	22	45		%	
		TCLT1003	CTR	34	70		%	
		TCLT1004	CTR	56	100		%	
		TCLT1005	CTR	50		150	%	
		TCLT1006	CTR	100		300	%	
	$V_{CE} = 5 \text{ V}, \text{ I}_{F} = 5 \text{ mA}$	TCLT1007	CTR	80		160	%	
		TCLT1008	CTR	130		260	%	
		TCLT1009	CTR	200		400	%	

SAFETY AND INSULATION RATED PARAMETERS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Partial discharge test voltage - routine test	100 %, t _{test} = 1 s	V _{pd}	2			kV	
Partial discharge test voltage -	t _{Tr} = 60 s, t _{test} = 10 s,	V _{IOTM}			8	kV _{peak}	
lot test (sample test)	(see figure 2)	V _{pd}			1.68	kV _{peak}	
Insulation resistance	V _{IO} = 500 V	R _{IO}	10 ¹²			Ω	
	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$	R _{IO}	10 ¹¹			Ω	
	V _{IO} = 500 V, T _{amb} = 150 °C (construction test only)	R _{IO}	10 ⁹			Ω	
Forward current		I _{si}	130			mA	
Power dissipation		P _{so}	265			mW	
Rated impulse voltage		V _{IOTM}	8			kV	
Safety temperature		T _{si}	150			°C	
Clearance distance			8.0			mm	
Creepage distance			8.0			mm	
Insulation distance (internal)			0.40			mm	

Note

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According to DIN EN 60747-5-2 (VDE 0884) (see figure 2). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.



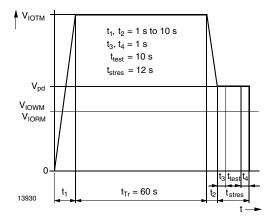


Fig. 2 - Test Pulse Diagram for Sample Test according to DIN EN 60747-5-2 (VDE 0884); IEC60747-5-5

3 For technical questions, contact: <u>optocoupleranswers@vishay.com</u>

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SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Delay time	$\label{eq:VS} \begin{array}{l} V_S = 5 \ V, \ I_C = 2 \ mA, \ R_L = 100 \ \Omega, \\ (see \ figure \ 3) \end{array}$	t _d		3		μs	
Rise time	$\label{eq:VS} \begin{array}{l} V_S = 5 \ V, \ I_C = 2 \ mA, \ R_L = 100 \ \Omega, \\ (see \ figure \ 3) \end{array}$	t _r		3		μs	
Fall time	$\label{eq:VS} \begin{array}{l} V_S = 5 \mbox{ V}, \mbox{ I}_C = 2 \mbox{ mA}, \mbox{ R}_L = 100 \ \Omega, \\ (see figure 3) \end{array}$	t _f		4.7		μs	
Storage time	$\label{eq:VS} \begin{array}{l} V_S = 5 \ V, \ I_C = 2 \ mA, \ R_L = 100 \ \Omega, \\ (see \ figure \ 3) \end{array}$	ts		0.3		μs	
Turn-on time	$\label{eq:VS} \begin{array}{l} V_S = 5 \ V, \ I_C = 2 \ mA, \ R_L = 100 \ \Omega, \\ (see \ figure \ 3) \end{array}$	t _{on}		6		μs	
Turn-off time	$\label{eq:VS} \begin{array}{l} V_S = 5 \; V, \; I_C = 2 \; mA, \; R_L = 100 \; \Omega, \\ (\text{see figure 3}) \end{array}$	t _{off}		5		μs	
Turn-on time	$\label{eq:VS} \begin{array}{l} V_S = 5 \ V, \ I_F = 10 \ mA, \ R_L = 1 \ k\Omega, \\ (see \ figure \ 4) \end{array}$	t _{on}		9		μs	
Turn-off time	$\label{eq:VS} \begin{array}{l} V_S = 5 \ V, \ I_F = 10 \ mA, \ R_L = 1 \ k\Omega, \\ (see \ figure \ 4) \end{array}$	t _{off}		10		μs	

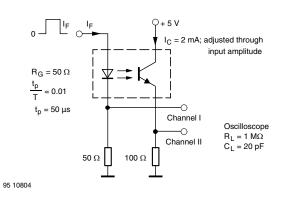


Fig. 3 - Test Circuit, Non-Saturated Operation

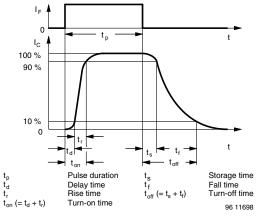


Fig. 5 - Switching Times

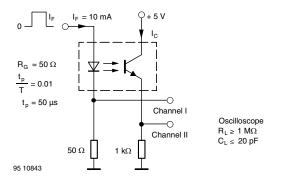


Fig. 4 - Test Circuit, Saturated Operation

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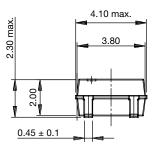
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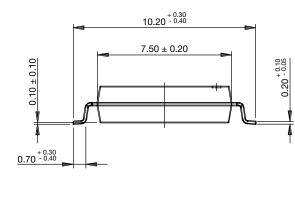
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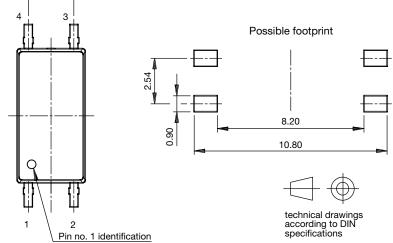
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PACKAGE DIMENSIONS in millimeters



2.54 nom.





PACKAGE MARKING

22533

TCLT1003 V YWW 68



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