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Reflective Sensor for Touchless Switch



84606 3D

DESCRIPTION

TCND3000 is a reflective optical sensor for applications using the HALIOS® (High Ambient Light Independent Optical System) principle. It consists of an infrared emitter and a photodetector forming the optical sensing path. According to the HALIOS principle a second infrared emitter is used for compensation of disturbing ambient light. Optoelectronic parameters of the sensor are matched to the corresponding integrated circuit E909.01, manufactured by ELMOS Semiconductor AG (www.elmos.de).

FEATURES

Package type: surface mount

• Detector type: pin photodiode

Dimensions (L x W x H in mm): 4.83 x 2.54 x 2.21

• Peak operating distance: 20 mm

• Peak operating range: 10 mm to 20 mm

• Typical output current under test: $I_C > 5.6 \mu A$

· Lead (Pb)-free soldering released

 Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC

• Emitter wavelength: 885 nm

· Daylight blocking filter

• Touch distance: 10 mm (1)

Proximity distance: 20 mm ⁽¹⁾

• High ambient light suppression for sunlight: ≤ 200 klx

• High ambient light suppression for CIE standard illuminant $A: \leq 100 \text{ klx}$

Note

 $^{(1)}$ Using E909.01 interface ASIC and Kodak grey card with 20 % diffuse reflection

APPLICATIONS

· Optical switches for general purpose

PRODUCT SUMMARY					
PART NUMBER	DISTANCE FOR MAXIMUM CTR _{rel} ⁽¹⁾ (mm)	DISTANCE RANGE FOR RELATIVE I _{out} > 20 % (mm)	TYPICAL OUTPUT CURRENT UNDER TEST (2) (mA)	DAYLIGHT BLOCKING FILTER INTEGRATED	
TCND3000	0	Not applicable	IC interface	Yes	

Notes

 $^{(1)}$ CTR: current transfere ratio, I_{out}/I_{in}

(2) Conditions like in table basic charactristics/sensors

ORDERING INFORMATION					
ORDERING CODE	PACKAGING	VOLUME (1)	REMARKS		
TCND3000	Tape and reel	MOQ: 800 pcs, 800 pcs/reel	Drypack		

Note

(1) MOQ: minimum order quantity

Reflective Sensor for Touchless Switch



ABSOLUTE MAXIMUM RATINGS (1)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
SENSOR							
Power dissipation	T _{amb} ≤ 25 °C	P _V	180	mW			
Storage temperature range		T _{stg}	- 40 to + 100	°C			
Ambient temperature range		T _{amb}	- 40 to + 85	°C			
Thermal resistance junction/ambient		R _{thJA}	450	K/W			
Soldering temperature	Acc. fig. 7	T _{sd}	260	°C			
IR EMITTER LEDS (TRANSMITT	ER)						
Reverse voltage		V_{RS}	5	V			
Forward current		I _{FS}	50	mA			
Peak forward current	$t_{ps} = 4 \mu s, t_{S} = 8 \mu s$	I _{FS}	100	mA			
Junction temperature		T _{jS}	105	°C			
IR EMITTER LEDC (COMPENSA	TION)						
Reverse voltage		V _{RC}	5	V			
Forward current		I _{FC}	50	mA			
Peak forward current	t _{pc} = 4 μs, t _S = 8 μs	I _{FC}	100	mA			
Junction temperature		T _{jC}	105	°C			
DETECTOR		·					
Reverse voltage		V _{RD}	5	V			
Junction temperature		T _{jD}	105	°C			

Note

ABSOLUTE MAXIMUM RATINGS

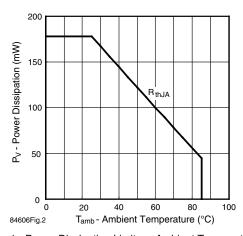


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

BASIC CHARACTERISTICS (1)						
PARAMETER TEST CONDITION		SYMBOL	MIN.	TYP.	MAX.	UNIT
SENSOR	SENSOR					
Light Current	Kodak grey card, 20 % diffuse reflection, distance: 1 cm, I _{FS} = 10 mA	I _{CA}		1.2		μΑ
Optical crosstalk sensing path	no reflective medium, I _{FS} = 10 mA	I _{CA}		0.9		μΑ
Compensation current	I _{FC} = 2 mA	I _{CR}		5		μΑ

 $^{^{(1)}}$ T_{amb} = 25 °C, unless otherwise specified



Reflective Sensor for Touchless Switch

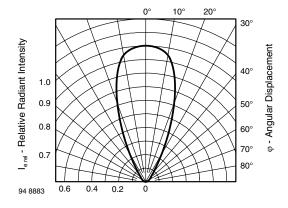
Vishay Semiconductors

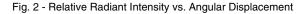
BASIC CHARACTERISTICS (1)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
IR EMITTER LEDS (TRANSMITT	ER)					
Forward voltage	$I_{FS} = 10 \text{ mA}, t_p = 20 \text{ ms}$	V_{FS}		1.3		V
Reverse voltage	$I_{RS} = 10 \mu A$	V _{RS}	5			V
Junction capacitance		C _{js}		50		pF
Radiant intensity	$I_{FS} = 10 \text{ mA}, t_p = 20 \text{ ms}$	I _e		2	22	mW/sr
Angle of half intensity		φs		± 20		deg
Peak wavelength	$I_{FS} = 10 \text{ mA}$	λ _{pS}	875	885		nm
Spectral bandwidth	I _{FS} = 10 mA	Δλ _S		42		nm
Virtual source diameter	DIN EN ISO 1146/1:2005	d		1.4		mm
IR EMITTER LEDC (COMPENSA	TION)					
Forward voltage	$I_{FC} = 10 \text{ mA}, t_{pC} = 20 \text{ ms}$	V _{FC}		1.3		V
Reverse voltage	I _{RC} = 10 μA	V _{RC}	5			V
Junction capacitance		C _{jC}		50		pF
Peak wavelength	I _{FC} = 10 mA	λ_{pC}		885		nm
Spectral bandwidth	I _{FC} = 10 mA	$\Delta\lambda_{C}$		42		nm
DETECTOR						
Forward voltage	I _{FD} = 50 mA	V_{FD}		1	1.3	V
Breakdown voltage	$I_{RD} = 100 \mu A, E = 0 Ix$	V_{BR}	5			V
Reverse dark current	V _{RD} = 10 V, E = 0 lx	I _{r0}		1	10	nA
Reverse light current	$E_e = 1 \text{ mW/cm}^2$, $\lambda = 870 \text{ nm}$, $V_{RD} = 5 \text{ V}$	I _{ra}		5.6		μА
Temperature coefficient of I _{ra}	$\lambda = 870 \text{ nm}, V_{RD} = 5 \text{ V}$	TK _{ira}		0.2		%/K
Angle of half intensity		ФD		± 20		deg
Wavelength of peak sensitivity		λ _P		910		nm
Range of spectral bandwidth		λ _{0.5}		790 to 1020		nm

Note

BASIC CHARACTERISTICS

 T_{amb} = 25 °C, unless otherwise specified





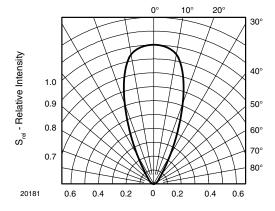


Fig. 3 - Relative Radiant Sensitivity vs. Angular Displacement

 $^{^{(1)}}$ T_{amb} = 25 $^{\circ}$ C, unless otherwise specified

Reflective Sensor for Touchless Switch



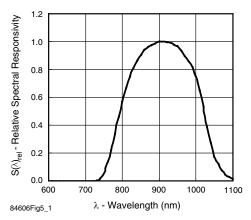


Fig. 4 - Relative Spectral Sensitivity vs. Wavelength

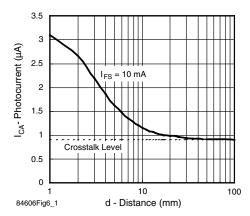


Fig. 5 - Photocurrent vs. Distance

APPLICATION CIRCUIT

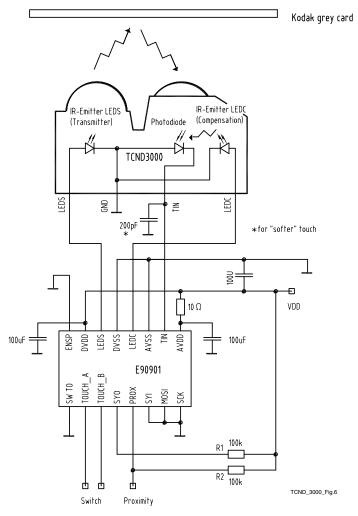


Fig. 6 - Test Circuit

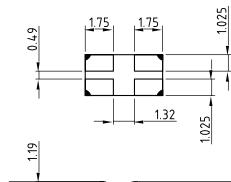


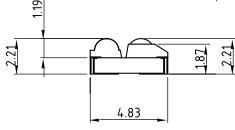
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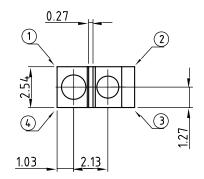
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PACKAGE DIMENSIONS in millimeters

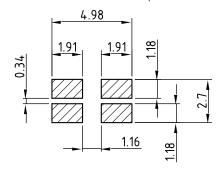






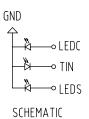


Recommened PCB Footprint



PIN	l ID	FUNCTION	DESCRIPTION
1	1	LEDS	Transmit LED
2	2	TIN	Receiver Output
3	3	LEDC	Compensation LED
4	4	GND	Ground







Not indicated tolerances ±0.2 Drawing-No.: 6.550-5265.01-4 Issue: 2; 25.10.04

ISSUE: 2; 25.10.04 84606 Dimensions

Reflective Sensor for Touchless Switch



REFLOW SOLDER PROFILES

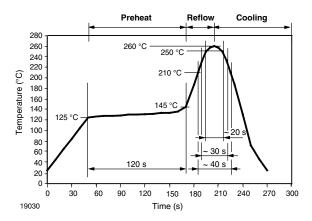


Fig. 7 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

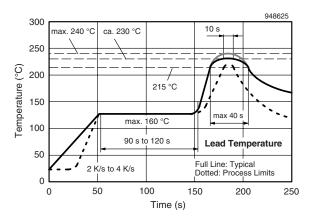


Fig. 8 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 4 weeks

Conditions: T_{amb} < 30 °C, RH < 60 %

Moisture sensitivity level 2a, acc. to J-STD-020.

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 $^{\circ}$ C (+ 5 $^{\circ}$ C), RH < 5 $^{\circ}$ C.



Vishay

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