imall

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TDCG1050M, TDCG1060M, TDCR1050M, TDCR1060M



16770

Four digit display, with 10 mm digit charactersize. Designed

as clock display with active colon between digit two and

DESCRIPTION

three.

Vishay Semiconductors

Clock Display

FEATURES

- High efficient AllnGAP technology
- · Dark surface, white segments
- Common anode (TDC.1050M)
- Common cathode (TDC.1060M)
- Multiplex mode
- Recommended viewing distance up to 7 m
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

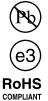
· Clock modules for video / audio equipment, instrumentation, set top boxes

PRODUCT GROUP AND PACKAGE DATA

- Product group: display
- Package: 10 mm clock
- · Product series: standard
- Angle of half intensity: ± 50°

PARTS TABLE														
PART	COLOR	LUMINOUS INTENSITY (µcd)		at WAVELENGTH		at I _F	FORWARD VOLTAGE (V)		at I _F	CIRCUITRY				
		MIN.	TYP.	MAX.	(mA)	MIN.	TYP.	MAX.	(mA)	MIN.	TYP.	MAX.	(mA)	
TDCG1050M	Green	2800	4000	-	10	562	573	575	20	-	2	2.4	20	Common anode
TDCG1060M	Green	2800	4000	-	10	562	573	575	20	-	2	2.4	20	Common cathode
TDCR1050M	Red	4000	6000	-	10	-	631	-	20	-	2	2.4	20	Common anode
TDCR1060M	Red	4000	6000	-	10	-	631	-	20	-	2	2.4	20	Common cathode

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified) TDCG1050M, TDCG1060M, TDCR1050M, TDCR1060M									
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT					
Reverse voltage per segment		V _R	5	V					
DC forward current per segment		l _F	25	mA					
Peak forward current per segment	Duty 1/10 at 1 kHz	I _{FM}	160	mA					
Power dissipation		Pv	60	mW					
Operating temperature range		T _{amb}	-40 to +85	°C					
Storage temperature range		T _{stg}	-40 to +100	°C					
Soldering temperature		T _{sd}	260 ± 5	°C					



TDCG1050M, TDCG1060M, TDCR1050M, TDCR1060M

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OPTICAL AND ELECTRICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified) TDCG1050M, TDCG1060M, GREEN											
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT				
	L = 2 mA	TDCG1050M	- I _V	-	1000	-	μcd				
Lumineus intensity per compart (1)	I _F = 2 mA	TDCG1060M									
Luminous intensity per segment ⁽¹⁾	10 10	TDCG1050M		2800	4000	MAX. - - - - 575 - - 575 - -	und				
	l _F = 10 mA	TDCG1060M	I _V	2800	4000		µcd				
	I _F = 2 mA	TDCG1050M	- I _V	-	200	-	μcd				
		TDCG1060M									
Luminous intensity of colon	1 10	TDCG1050M		500	1200	- - - 575 - - 2.4	μcd				
	l _F = 10 mA	TDCG1060M	I _V	500	1200						
Dominant wavelength	I _F = 20 mA		λ _d	562	573	575	nm				
Peak wavelength	I _F = 20 mA		λρ	-	575	-	nm				
Spectral bandwidth	I _F = 20 mA	TDCG1050M, TDCG1060M	Δ_{λ}	-	20	-	nm				
Forward voltage per segment or DP	I _F = 20 mA		V _F	-	2	2.4	V				
Reverse current per segment or DP	V _R = 5 V		I _R	-	-	10	μA				

Note

⁽¹⁾ $I_{Vmin,}$ and I_V groups are mean values of all segments (a to g, D1 to D4), matching factor within segments is \ge 0.5, excluding decimal points and colon

OPTICAL AND ELECTRICAL CHARACTERISTICS ($T_{amb} = 25$ °C, unless otherwise specified) **TDCR1050M, TDCR1060M, RED**

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
	L 0 m 4	TDCR1050M			1500		und
Luminous intensity new compart (1)	I _F = 2 mA	TDCR1060M	I _V	-	1500	-	µcd
Luminous intensity per segment ⁽¹⁾	10	TDCR1050M		4000	6000		
	l _F = 10 mA	TDCR1060M	I _V	4000	6000	-	µcd
	1 0 m 4	TDCR1050M	- I _V	-	400	-	μcd
Luminous intensity of colon	I _F = 2 mA	TDCR1060M					
	10 10	TDCR1050M		500	800		und
	I _F = 10 mA	TDCR1060M	I _V	500	800	-	µcd
Dominant wavelength	I _F = 20 mA		λ _d	-	631	-	nm
Peak wavelength	I _F = 20 mA	TDCR1050M, TDCR1060M	λρ	-	639	-	nm
Spectral bandwidth	I _F = 20 mA		Δ_{λ}	-	20	-	nm
Forward voltage per segment or DP	I _F = 20 mA		V _F	-	2	2.4	V
Reverse current per segment or DP	V _R = 5 V		I _R	-	-	10	μA

Note

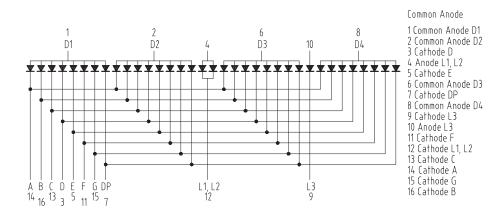
(1) $I_{Vmin.}$ and I_V groups are mean values of all segments (a to g, D1 to D4), matching factor within segments is \ge 0.5, excluding decimal points and colon

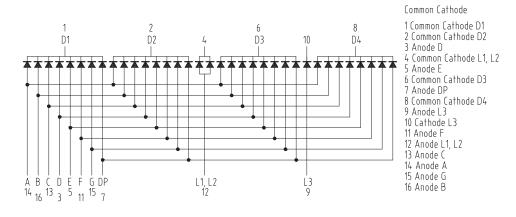


TDCG1050M, TDCG1060M, TDCR1050M, TDCR1060M

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PINNING



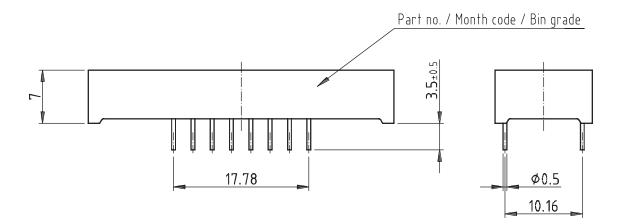


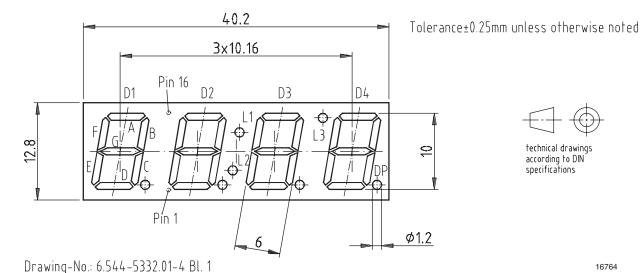
Drawing-No.: 6.544-5332.01-4 Bl. 2 Issue: 1; 20.02.02

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TDCG1050M, TDCG1060M, TDCR1050M, TDCR1060M VISHAY www.vishay.com **Vishay Semiconductors**

PACKAGE DIMENSIONS in millimeters







technical drawings according to DIN specifications

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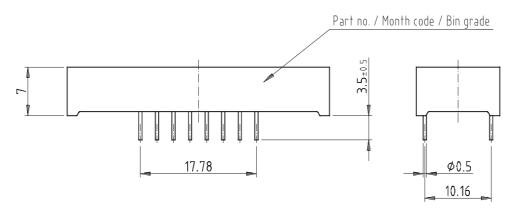
Issue: 3; 27.02.02

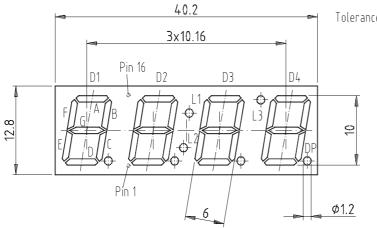


Vishay Semiconductors

Display-10 mm Clock Multiplex

Package Dimensions in mm





Tolerance±0.25mm unless otherwise noted



technical drawings according to DIN specifications

Drawing-No.: 6.544-5332.01-4 Bl. 1 Issue: 3; 27.02.02

16764

Display-10 mm Clock Multiplex

Vishay Semiconductors



Ozone Depleting Substances Policy Statement

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operatingsystems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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