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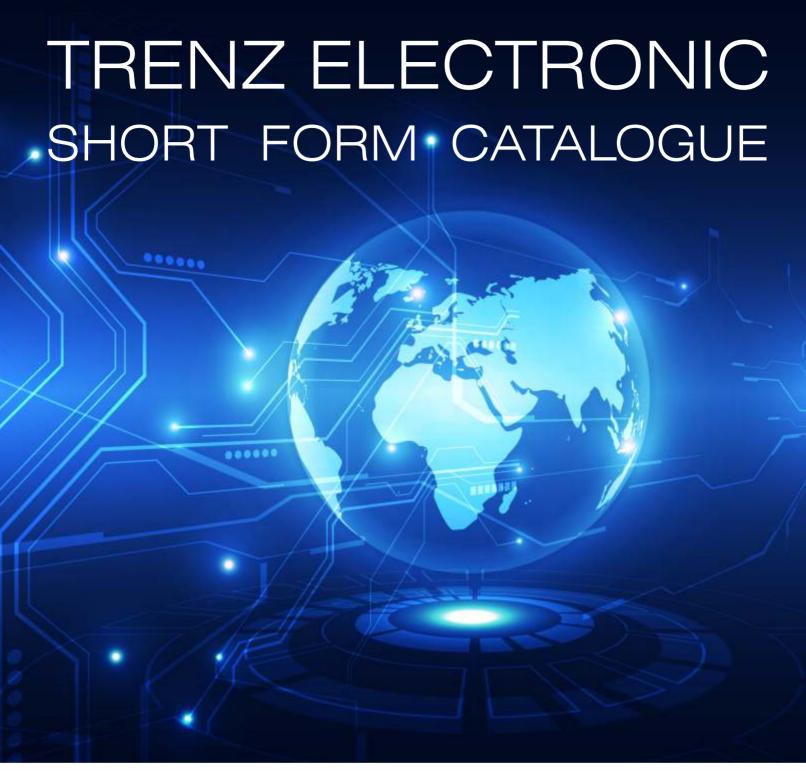
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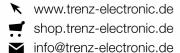








updated march 2017





Company Profile

Since 1992, Trenz Electronic GmbH successfully operates as a development service enterprise in the electrobranch. Our services include nics design-in support as well as turnkey design which typically cover all steps from product specification, hardware and software design up to prototyping and production.



We are particularly specialized in the design of high-speed data acquisition, high-accuracy measurement and embedded digital signal processing systems based on FPGA and ARM architectures.

We maintain long-term customer relationships, characterized by flexibility and technical competence.

Hardware Design

- System Architecture and Design
- Hardware Integration (Design-In)
- Ultrafast Digital Logic
- Analog and Mixed Signal
- Digital Signal Processing
- Schematic Capture and PCB Layout

HDL Design

- FPGA and System-On-Chip Design
- System Design and Synthesis
- HDL Design (VHDL, Verilog)
- Integration of Soft-Cores (Xilinx MicroBlaze, ARM Cortex ...)
- USB, PCI-Express, Gigabit Ethernet
- Ultrafast ADC/DAC Interfaces

Software Development

- Device Driver and Application Software development
- Software and Firmware development







ISO 9001:2008 (quality management) certified

ISO 14001:2004 (environmental management) certified

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TE0820 Series Xilinx Zyng UltraScale+, DDR4, SPI Boot Flash, eMMC





Key Features (preliminary)

- Xilinx Zynq UltraScale+ MPSoC 784 pin package (ZU3EG, option ZU5EV)
- Memory:
 - 32-Bit DDR4 4 GByte max
 - SPI Boot Flash dual parallel 512 MByte max
 - 4 GByte eMMC (up to 64 GByte)
- B2B connectors:

Plug-on module with 2 x 100-pin and 1 x 60-pin highspeed hermaphroditic strips

- 14 x MIO, 132 I/O's x HP (3 banks)
- Serial transceiver: PS GTR 4
- GT Reference clock input
- PLL for GT Clocks (optional external reference)
- 1 GBit Ethernet PHY
- USB 2.0 OTG PHY
- Real Time Clock
- Size: 40 x 50 mm
- All power supplies on board.

Other assembly options for cost or performance optimization plus high volume prices available on request.

Overview

The Trenz Electronic TE0820 are 4 x 5 standard footprint MPSoC modules integrating a Xilinx Zynq UltraScale+ with up to 4 GByte 32-Bit DDR4 SDRAM, and max. 512 MByte SPI Boot Flash memory for configuration and operation, and powerful switch-mode power supplies for all on-board voltages. A large number of configurable I/O's is provided via rugged high-speed stacking strips.

All modules in 4 x 5 cm form factor are fully mechanically and largely electrically compatible among them. All this on a tiny footprint, smaller than a credit card, at the most competitive price.

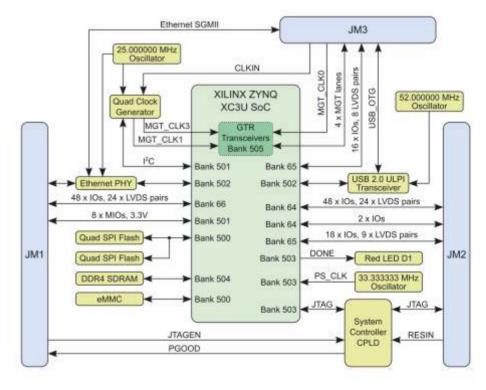
All modules produced by Trenz Electronic are developed and manufactured in Germany.



Rugged for industrial applications







TE0803 Series

Xilinx Zynq UltraScale+, DDR4, SPI Boot Flash, Serial transceiver





Key Features (preliminary)

- Xilinx Zynq UltraScale+ MPSoC 784 pin package (ZU3EG, optional ZU5EV)
- Memory:
 - 64-Bit DDR4 8 GByte max
 - SPI Boot Flash dual parallel 512 MByte max
- B2B connectors:

Plug-on module with 4 x 160-pin connectors

- 65 x MIO, 156 I/O's x HP (3 banks)
- Serial transceiver: PS GTR 4, PL GT 4 (ZU4, ZU5 only)
- GT Reference clock input
- PLL for GT Clocks (optional external reference)
- Size: 52 x 76 mm
- All power supplies on board.
- Other assembly options for cost or performance optimization plus high volume prices available on request.

Overview

The Trenz Electronic TE0803 is an industrial-grade MPSoC module integrating a Xilinx Zynq UltraScale+ with up to 8 GByte 64-Bit width DDR4 SDRAM, and max. 512 MByte SPI Boot Flash memory for configuration and operation, and powerful switch-mode power supplies for all on-board voltages. A large number of configurable I/O's is provided via rugged high-speed stacking connections.

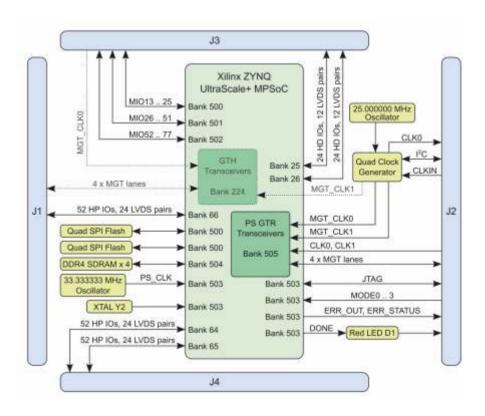
All this in a compact 5.2 x 7.6 cm form factor, at the most competitive price.













TEB0911 Series

Xilinx Zynq UltraScale+, 6 FMC Solts, Gigabit Ethernet



Key Features

- Zynq UltraScale+ MPSoC 1156 Package ZU9 (ZU6, ZU9, ZU15 Possible as assembly option)
- 64-Bit DDR4 SODIMM (PS connected)
- PS-GTR
 - M2 PCle SSD (internal, 1-Lane)
 - 2 x USB3 Host (from 4 port internal HUB)
 - 2 Lane DisplayPort output Monitor
- RJ45 GbE Ethernet PS connected, 88E1512 PHY
- 4 x FMC-HPC connector front
 - 4 GTH
 - 1 GT Clock
 - 68+4 HP or HD I/O
- FMC-HPC connector Back
 - 4 GTH
 - 1 GT Clock
 - 12 I/O
- FMC-HPC connector Back
 - 1 GTH
 - 1 GT Clock
 - 12 I/O
- 2 x SFP+ connected to 2 PL GTH,
- 1 x SFP+ connected to PL GTH
- Power: 24V

Overview

The Trenz Electronic TEB0911 "UltraRack+" is a high performance Zynq UltraScale+ MPSoC board with 6 FMC slots and Gigabit Ethernet.







TE0808 UltraSOM+ Series Xilinx Zyng UltraScale+, DDR4, Flash, 20 x Transceiver





Overview

The Trenz Electronic TE0808 is an industrial-grade MPSoC module integrating a Xilinx Zynq UltraScale+, max. 8 GByte DDR4 SDRAM with 64-Bit width, max. 512 MByte Flash memory for configuration and operation, 20 Gigabit transceivers, and powerful switch-mode power supplies for all on-board voltages. A large number of configurable I/O's is provided via rugged high-speed stacking connections.

All this in a compact $5.2 \times 7.6 \text{ cm}$ form factor, at the most competitive price.

All modules produced by Trenz Electronic are developed and manufactured in Germany

Key Features

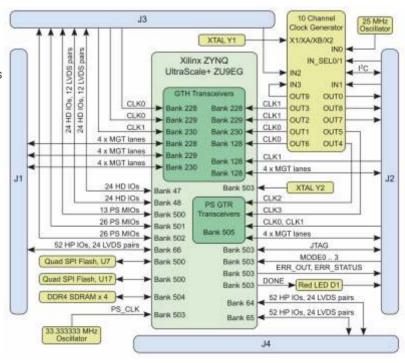
- SoC: ZYNQ UltraScale+ ZU9EG 900 pin package
- Memory
 - 4 x 512 MByte 64-Bit DDR4 (8 GByte max.)
 - 2 x 32 MByte SPI Boot Flash dual parallel (512 MByte max.)
- User I/O
 - 65 x MIO, 48 x HD (all), 156 x HP (3 banks)
 - Serial transceiver: GTR 4 (all) + GTH 16 (all)
 - GT clocks, I2C
 - PLL clock inputs and outputs
- Size: 52 x 76 mm
- 3 mm mounting holes for skyline heat spreader
- B2B connectors: 4 x 160 pin
- Si5345 10 output PLL
- All power supplies on board, single 3.3V Power required
 - 14 on-board DC/DC regulators and 13 LDO's
 - LP, FP, PL separately controlled power domains
- Support for all boot modes (except NAND) and scenarios
- Support for any combination of PS connected peripherals

Other assembly options for cost or performance optimization plus high volume prices available on request.











TEC0330 Virtex-7 PCIe FMC Carrier



Key Features

- FMC HPC
- 8 lane PCle Gen 2 capable
- Xilinx Virtex-7 XC7VX330T-2FFG1157C
- DDR3 SODIMM Socket
- 32 MByte SPI Flash
- LMK04828B Clock Synthesizer
- External Clock Input

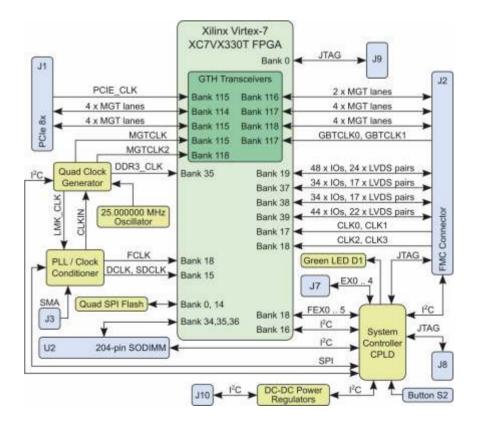
Other assembly options for cost or performance optimization plus high volume prices available on request.



and manufactured in Germany.



All modules produced by Trenz Electronic are developed





TEF1001 Series electronic PCIe FMC Carrier, Xilinx Kintex-7, SPI Flash, 4 Iane PCIe



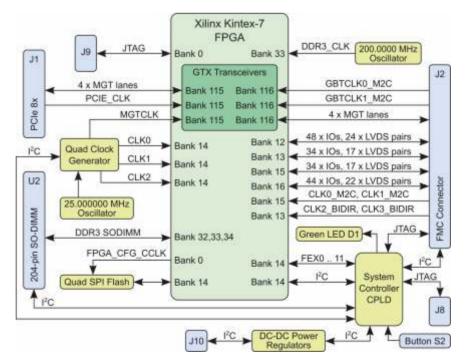
Key Features

- One Vita 57.1 FMC HPC Slot
- 4 lane PCle Gen 2
- Xilinx Kintex-7 XC7K160T-2FBG676I
- DDR3 SODIMM Socket
- 32 MByte SPI Flash
- Programmable clock generator Si5338
- 200 MHz Low-Jitter LVDS oscillator
- High performance DC-DC converters

Other assembly options for cost or performance optimization plus high volume prices available on request.









TE0745 Series Xilinx Zyng, 1 GByte DDR3, 32 MByte Flash, 1 GBit Ethernet





Key Features

- Xilinx Zyng 7030/7035/7045
- · Rugged for shock and high vibration
- 1 GByte 32-Bit wide DDR3/L
- 32 MByte SPI Flash
- Dimensions: 5.2 x 7.6 cm
- B2B Connectors with 3 x 160 pin
- 250 I/O's, all HR and HP I/O
- 1 GBit Ethernet PHY,
- USB 2.0 OTG PHY
- 8 x GTX (7030: 4 GT)
- 2 GT Reference Clock inputs (7030: 1 REFC
- Reference clock input for PLL (optional)
- 2 x PLL outputs
- I2C
- 6 MIO
- · Real Time Clock
- MAC Address EEPROM
- Evenly spread supply pins for good signal integrity

Other assembly options for cost or performance optimization plus high volume prices available on request.

Overview

The Trenz Electronic TE0745 is an industrial-grade SoC module integrating a Xilinx Zvng-7 (Z-7030, Z-7035, Z-7045). 1 GByte 32-Bit wide DDR3/L, 32 MByte SPI Flash memory for configuration and operation and powerful switch-mode power supplies for all on-board voltages. A large number of configurable I/O's is provided via rugged high-speed stacking strips.

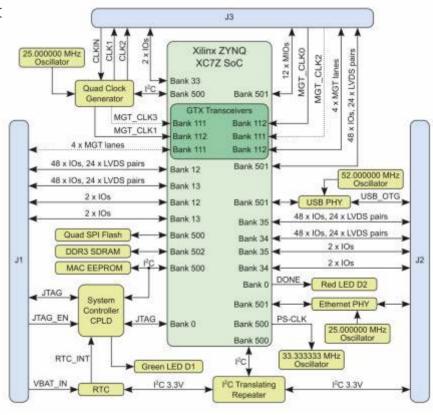
All this on a tiny footprint, smaller than a credit card, at the most competitive price.











TE0729 Series

Xilinx Zynq, 1x Gig ETH, 512 MByte DDR3, 2x 100MBit ETH, 4 GByte eMMC, 3.3V Only





Key Features

- Zyng XC7Z020-2CLG484I
- · Rugged for shock and high vibration
- 2 x ARM Cortex-A9
- 1 x 10/100/1000 Mbps Ethernet transceiver PHY
- 2 x 10/100 Mbps Ethernet transceiver PHYs
- 3 x MAC-Address EEPROMs
- 16-Bit wide 512 MByte DDR3 SDRAM
- 32 MByte QSPI-Flash-Memory
- 4 GByte e-NAND-Flash-Memory (embedded eMMC Memory)
- · USB 2.0 high-speed ULPI transceiver
- Plug-on module with 2 x 120-pin high-speed hermaphroditic strips
- 136 FPGA I/O's (58 LVDS pairs possible) and 14 MIO's available on
- · board-to-board connectors
- · On-board high-efficiency DC-DC converters
 - 4.0 A x 1.0 V power rail
 - 1.5 A x 1.5 V power rail
 - 1.5 A x 1.8 V power rail
 - 1.5 A x 2.5 V power rail
- · System management
- eFUSE bit-stream encryption
- · AES bit-stream encryption
- Temperature compensated RTC (real-time clock)
- · User LED
- Evenly spread supply pins for good signal integrity
- 3 mm mounting holes for Skyline heat spreader
- · Cooling Solution available

Other assembly options for cost or performance optimization plus high volume prices available on request.

Overview

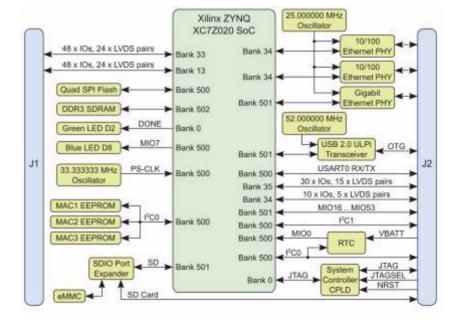
The Trenz Electronic TE0729 is an industrial-grade SoC module integrating a Xilinx Zyng-7020 with a Gigabit Ethernet transceiver, 2 x 100 MBit Ethernet, 512 MByte DDR3 SDRAM, 32 MByte Flash memory for configuration and operation, and powerful switch-mode power supplies for all on-board voltages. A large number of configurable I/O's is provided via rugged high-speed stacking strips.











TE0715 Series

Xilinx Zyng, Cortex-A9, DDR3, Flash, GBit Ethernet, 4 x Transceiver





Key Features

- Industrial-grade Xilinx Zynq-7000 (Z-7015, Z-7030) SoM, supported by the free Xilinx Vivado WebPACK tool
- Rugged for shock and high vibration
- ARM dual-core Cortex-A9
- 10/100/1000 tri-speed gigabit Ethernet transceiver (PHY) with SGMII
 - MAC Address EEPROM
- 32-Bit-wide 1 GByte DDR3 SDRAM
- 32 MByte QSPI Flash memory (with XiP support)
- Programmable clock generator
 - Transceiver clock (default 125 MHz)
- Plug-on module with 2×100 -pin and 1×60 -pin high-speed hermaphroditic strips
- 132 FPGA I/O's (65 LVDS pairs possible) and 14 PS-MIO available on board-to-board connectors
- 4 GTP/GTX (high-performance transceiver) lanes GTP/GTX (high-performance transceiver) clock input
- USB 2.0 high-speed ULPI transceiver
- On-board high-efficiency DC-DC converters
 - 4.0 A x 1.0 V power rail
 - 1.5 A x 1.5 V power rail
 - 1.5 A x 1.8 V power rail
- · System management
- eFUSE bit-stream encryption
- AES bit-stream encryption
- Temperature compensated RTC (real-time clock)
- User LED
- Evenly spread supply pins for good signal integrity

Other assembly options for cost or performance optimization plus high volume prices available on request.

Overview

Trenz Electronic TE0715 are industrial-grade SoC modules integrating a Xilinx Zynq-7000 SoC, a gigabit Ethernet transceiver (physical layer), 1 gigabyte DDR3 SDRAM with 32-Bit width, 32 megabyte Flash memory for configuration and operation, 4 transceivers, a USB ULPI transceiver, and powerful switch-mode power supplies for all onboard voltages. A large number of configurable I/O's is provided via rugged high-speed stacking strips. All modules in 4 x 5 cm form factor are mechanically compatible.

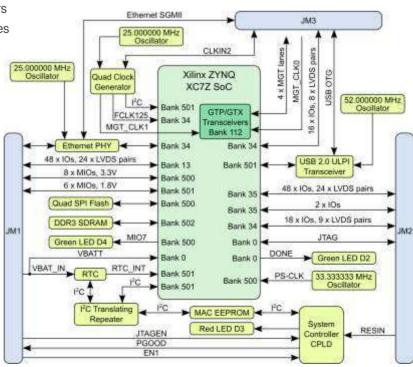
All this on a tiny fooprint, smaller than a credit card, at the most competitive price.











TE0720 Series

Xilinx Zyng, 2 × Cortex-A9, DDR3, Flash, GBit Ethernet, USB





Key Features

- Industrial-grade Xilinx Zyng 7020 SoM, supported by the free Xilinx Vivado WebPACK tool
- · Rugged for shock and high vibration
- ARM Dual Core Cortex-A9
- 10/100/1000 tri-speed gigabit Ethernet transceiver (PHY) with SGMII
 - MAC Address EEPROM
- 32-Bit-wide 1 GByte DDR3 SDRAM
- 32 MByte QSPI Flash memory (with XiP support)
- 4 GByte (up to 32 GB) e-NAND
- Plug-on module with 2×100 -pin and 1×60 -pin high-speed hermaphroditic strips
- 152 FPGA I/O's (75 LVDS pairs possible) and 14 PS-MIO available on board-to-board connectors
- USB 2.0 high-speed ULPI transceiver
- On-board high-efficiency DC-DC converters
 - 4.0 A x 1.0 V power rail
 - 1.5 A x 1.5 V power rail
 - 1.5 A x 1.8 V power rail
- · System management and power sequencing
- eFUSE bit-stream encryption
- AES bit-stream encryption
- Temperature compensated RTC (real-time clock)
- 3 user LEDs
- Optional MEMS sensor (3D accelerometer and 3D magnetometer)
- Evenly spread supply pins for good signal integrity

Other assembly options for cost or performance optimization plus high volume prices available on request.

Overview

Trenz Electronic TE0720 are industrial-grade SoC modules integrating a Xilinx Zynq-7000 SoC, a gigabit Ethernet transceiver, 1 gigabyte DDR3 SDRAM with 32-Bit width, 32 megabyte Flash memory for configuration and operation, a USB ULPI transceiver, and powerful switch-mode power supplies for all on-board voltages. A large number of configurable I/O's is provided via rugged high-speed stacking strips. All modules in 4 x 5 cm form factor are mechanically compatible.

All this on a tiny footprint, smaller than a credit card, at the most competitive price.

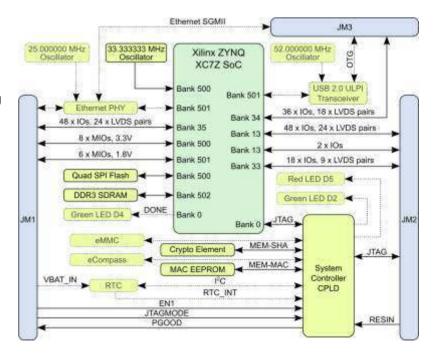
All modules produced by Trenz Electronic are developed and manufactured in Germany.



Rugged for industrial applications







TE0728 Series

Xilinx Zynq, 2 × Cortex-A9, DDR3, Flash, 2 × Ethernet, CAN, Automotive





Key Features

- Xilinx Zynq XA7Z020-1CLG484Q (Automotive)
- · Rugged for shock and high vibration
- 2 x ARM Cortex-A9 MPCore
- 2 x 100 MBit Ethernet transceiver (PHY)
- 16-Bit-wide 512 MByte DDR3 SDRAM
- 16 MByte QSPI Flash memory (with XiP support)
- Plug-on module with 3 x 80-pin high-reliability highspeed strips
- 76 single ended I/O, 24 LVDS pairs (48 I/O) and 42 MIO available on
- board-to-board connectors
- Board-to-board connectors
- CAN transceiver (PHY)
- Temperature compensated RTC (real-time clock)
- 12 V power supply with watchdog
- On-board high-efficiency DC-DC converters
- System management and power sequencir
- eFUSE bit-stream encryption
- AES bit-stream encryption
- 3 user LEDs
- Evenly spread supply pins for good signal integrity

Other assembly options for cost or performance optimization plus high volume prices available on request.

Overview

The Trenz Electronic TE0728-04-1Q is a SoC module integrating a Xilinx Automotive Zyng-7020, 512 MByte DDR3 SDRAM with 16-Bit width, 16 MByte Flash Memory for configuration and operation, two 100 Megabit Ethernet transceivers, and powerful switch-mode power supplies for all on-board voltages. A large number of con?gurable I/O's is provided via rugged high-speed stacking strips.

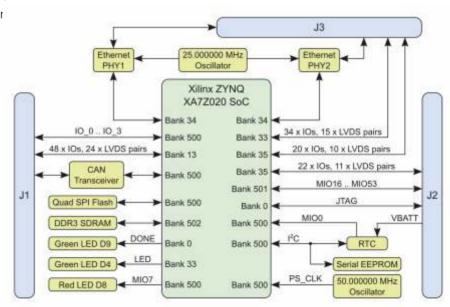
Within the complete module only Automotive components are installed. All this in a compact 6 x 6 cm form factor, at the most competitive price.











TE0782 Series

Xilinx Zyng, $2 \times$ Cortex-A9, DDR3, Flash, $2 \times$ GBit Ethernet, $2 \times$ USB, $16 \times$ Transceiver





Key Features

- Industrial-grade Xilinx Zynq-7 XC7Z035, XC7Z045 or XC7Z100 SOM
- Rugged for shock and high vibration
- Dual ARM Cortex-A9 MPCore
- · Real Time Clock
- 2 x Hi-Speed USB2.0 ULPI Transceiver PHY
- 2 x Gigabit Ethernet Transceiver PHY
- 2 x Ethernet MAC Address EEPROM
- 1 GByte DDR3 SDRAM
- 32 MByte QSPI Flash memory
- 4 GByte eMMC (optional up to 64 GByte)
- Optional 2 x 8 MByte HyperRAM (max 2 x 32 MByte HyperRAM)
- Si5338 PLL for GTX clocking
- Plug-on module with 3 x 160-pin high-speed strips
- 16 GTX high-performance tranceiver lanes, GTX high-performance
- Transceiver clock input
- 254 FPGA I/O's (125 LVDS pairs possible) available on board-to-board connectors
- On-board high-efficiency DC-DC converters
- · System management and power sequencing
- eFUSE bit-stream encryption
- AES bit-stream encryption
- Evenly spread supply pins for good signal integrity

Other assembly options for cost or performance optimization plus high volume prices available on request.

Overview

The Trenz Electronic TE0782 are industrial-grade SoC modules integrating a Xilinx Zyng-7 XC7Z035, XC7Z045 or XC7Z100, 1 GByte DDR3 SDRAM, 4 GByte eMMC, 16 GTX high-performance transceiver lanes, 32 MByte QSPI Flash memory for configuration and operation, and powerful switchmode power supplies for all on-board voltages.

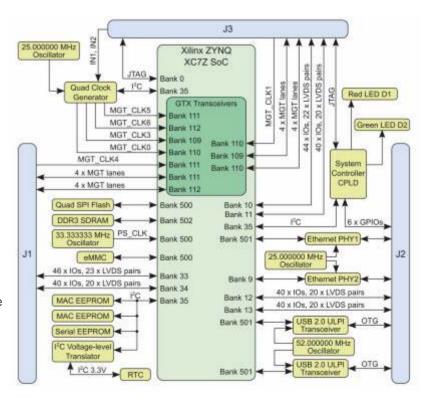
A large number of configurable I/O's is provided via rugged high-speed stacking strips. All this in a 8.5 x 8.5 cm form factor at the most competitive price.











TE0723 Series

Xilinx Zynq, 2x ARM Cortex A9, DDR3L Memory, USB OTG, On-board USB JTAG and UART





Key Features

- Xilinx XC7Z010-1CLG225C
- · Form Factor: Arduino Shield
- Dual ARM Cortex-A9
- Up to 512 MByte DDR3L
- 16 MByte SPI Flash memory
- 12 MHz MEMS Oscillator low power consumption
- · Hi-Speed USB2.0 ULPI Transceiver
- 23 FPGA I/O's available on board-to-board connectors
- MicroSD Card socket
- · Micro USB OTG
- RGB LED (PL I/O connected)
- "Done" LED (inverted polarity)
- On-board USB JTAG and UART
- CERN Open Hardware Licence 1.2

Other assembly options for cost or performance optimization available or high volume prices on request.

Overview

The Trenz Electronic TE0723 is a Arduino compatible FPGA module integrating a Xilinx Zvng-7010, 512 MBvte DDR3L. and 16 MByte SPI Flash Memory for configuration and operation.

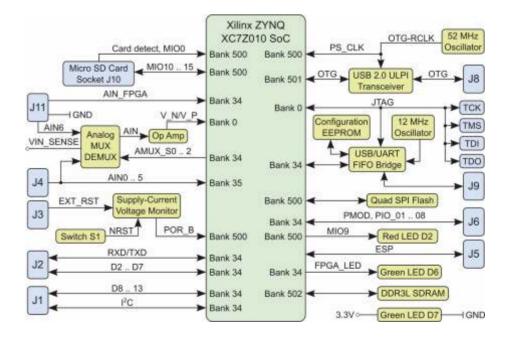
The "ArduZyng" is the lowest cost, Linux ready solution to use the latest FPGA: the Xilinx 7 series. Use it as a FPGA development platform, or run Linux on the Cortex A9 cores.













TE0726 Series

Xilinx Zyng, 512 MByte DDR3L SDRAM, 16 MByte Flash, 100 MBit Ethernet RJ45





Overview

The Trenz Electronic TE0726 is a Rasberry Pi compatible FPGA module integrating a Xilinx Zyng-7010, 512 MByte DDR3L SDRAM, 4 USB ports, an Ethernet port and 16 MByte Flash memory for configuration und operation.

All modules produced by Trenz Electronic are developed and manufactured in Germany.





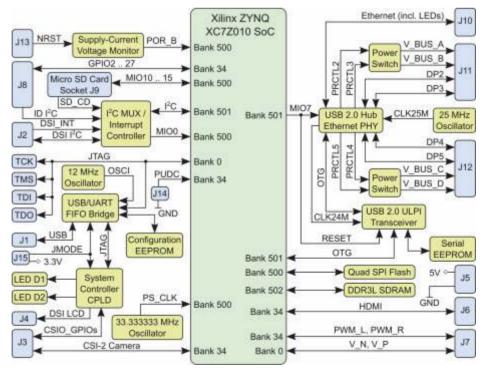




Key Features

- Xilinx Zynq XC7Z010-1CLG225C
 - 512 MByte DDR3L SDRAM
 - 16 MByte Flash
- LAN9514 USB Hub with Ethernet
 - 4 x USB with power switches
 - 100 MBit Ethernet RJ45
- · Micro SD card slot
- HDMI Typ A
- DSI Connector (Display)
- CSI-2 Connector (Camera)
- · Micro USB
 - power input
 - USB UART
 - JTAG ARM- und FPGA-Debug
- 3.5 mm audio plug (PWM Audio output only)
- · Raspberry Pi Model 2 form faktor
- HAT header with 26 I/O's

Other assembly options for cost or performance optimization plus high volume prices available on request.



TE0722 Series

Xilinx Zyng, Dual Core ARM A9+, 16 MByte SPI Flash, DIP40 Form Factor





Key Features

- Xilinx Zynq-7: XC7Z010-CLG225
 - 16 MByte SPI Flash (primary boot)
 - 33.333 MHz Clock (MEMS Oscillator)
- Dual Core ARM A9+
- · DIP40 form factor
 - 2 x 20 holes for socket pins or pin-header
 - Size: 18 x 51 mm
- Total user accessible PL I/O: 46 (+3 Input only)
 - DIP40 header pins: 34 I/O
 - XMOD J1: 6 I/O
 - XMOD J2: JTAG + 2 I/O (or 3 input + 2 I/O)
 - XMOD J3: 4 I/O
- 3.3V single supply
- RGB LED (PL I/O connected)
- "Done" LED (inverted polarity)
- User LED (ARM CPU MIO GPIO)
- MicroSD Card socket (MIO, ZYNQ secondary boot media)
- · Sil1143 Proximity and ambient light sensor

Other assembly options for cost or performance optimization available or high volume prices on request.

Overview

The DIPFORTy1 "Soft Propeller" is based on the Xilinx Zyng-7000, a System on Chip which contains a FPGA and a Dual Core ARM A9+ processor with enough logic gates to become a Propeller. The board also has 16 MByte of Flash used for configuration and everything fits on a Propeller-compatible DIP

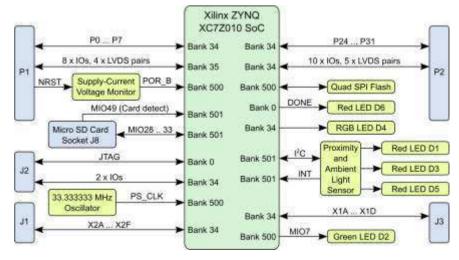
DIPFORTy1 "Soft-Propeller" is the lowest cost Zyng based module ever made and the first Zyng module that can use existing bases and project boards (Parallax Propeller chip compatibility). All this in a compact 1.8 x 5.1 cm form factor, at the most competitive price.













TE0841 Series Xilinx Kintex UltraScale, DDR4, Flash, 8 x Transceiver





Overview

The Trenz Electronic TE0841 is an industrial-grade FPGA module integrating a Xilinx Kintex UltraScale KU35 or KU40, max. 4 GByte 16-Bit width DDR4, max. 256 MByte QSPI Flash for configuration and operation, and powerful switch-mode power supplies for all on-board voltages. A large number of configurable I/O's is provided via rugged high-speed stacking strips.

All this on a tiny footprint, smaller than a credit card, at the most competitive price.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

Key Features

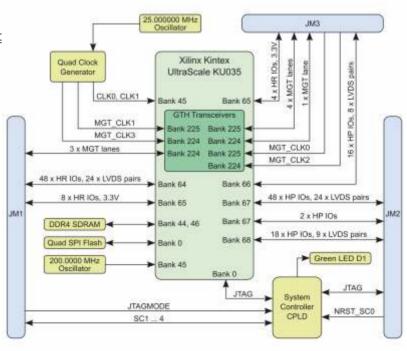
- · Xilinx Kintex UltraScale KU35 or KU40 FPGA
- · Rugged for industrial application
- · max. 4 GByte 16-bit wide DDR4
- max. 256 MBit (32 MByte) SPI Boot Flash
- Size: 40 x 50 mm
- · 3 mm mounting holes for skyline heat spreader
- B2B Connectors: 3 x Razor Beam, total 260 terminals
 - User I/O: HR 12, HP 132
 - Serial transceiver: GTH 8 lanes (all)
 - GT clock inputs: 2
- Clocking
- Si5338 4 output PLL, GT and PL clocks
- 200 MHz LVDS oscillator
- All power supplies on board, single supply operation supported.
- · Evenly spread supply pins for good signal intec

Other assembly options for cost or performance optimization available plus high volume prices on request.









TE0741 Series

Xilinx Kintex-7 T, Flash, 8 × Transceiver





Overview

Trenz Electronic TE0741 are industrial-grade FPGA modules integrating a Xilinx Kintex-7 T FPGA, 32 MByte Flash memory for configuration and operation, 8 transceivers, and powerful switch-mode power supplies for all on-board voltages. A large number of configurable I/O's is provided via rugged high-speed stacking strips. All modules in 4 x 5 cm form factor are mechanically compatible.

All this on a tiny footprint, smaller than a credit card, at the most competitive price.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

Key Features

- Industrial-grade Xilinx Kintex-7 (70T, 160T, 325T and 410T) SoM
- Rugged for shock and high vibration
- 32 MByte QSPI Flash memory (with XiP support)
- Programmable clock generator
 - 2 x Transceiver clock (default 125 MHz)
 - Fabric clock (default 200 MHz)
- Plug-on module with 2×100 -pin and 1×60 -pin high-speed hermaphroditic strips
- 144 FPGA I/O's (65 LVDS pairs possible) available on board-to-board connectors
- 8 GTX (high-performance transceiver) lanes - GTX (high-performance transceiver) clock input
- On-board high-efficiency DC-DC converters
 - 20.0 A x 1.0 V power rail
 - 1.5 A x 1.8 V power rail
- System management and power sequencing
- eFUSE bit-stream encryption
- · AES bit-stream encryption
- User LEDs
- Evenly spread supply pins for good signal integrity

Recommended Software:

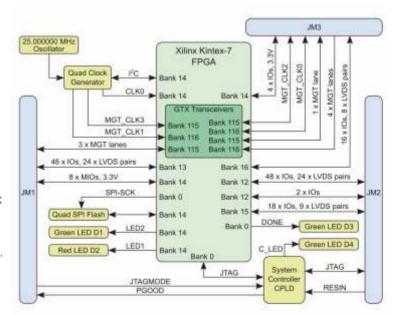
- Kintex-7 XC7K70T-2CF, Kintex-7 XC7K160T-2CF: Xilinx Vivado Webpack (free license)
- Kintex-7 XC7K325T-2CF, Kintex-7 XC7K410T-2CF: Xilinx Vivado Design Suite

Other assembly options for cost or performance optimization plus high volume prices available on request.









TE0710 Series

Xilinx Artix-7 T, DDR3, Flash, 2×100 MBit Ethernet





Overview

Trenz Electronic TE0710 are industrial-grade FPGA modules integrating a Xilinx Artix-7 T FPGA, two MBit Ethernet transceivers (physical layer), 512 Mbyte DDR3 SDRAM with 8-Bit width, 32 MByte Flash memory for configuration and operation, and powerful switch-mode power supplies for all on-board voltages. A large number of configurable I/O's is provided via rugged high-speed stackingstrips.

All modules in 4 \times 5 cm form factor are mechanically compatible.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

Key Features

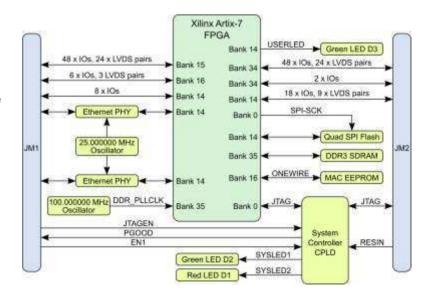
- Industrial-grade Xilinx Artix-7 (15T to 100T) SoM, supported by the free Xilinx Vivado WebPACK tool
- · Rugged for shock and high vibration
- 512 MByte DDR3 SDRAM
- Dual 100 MBit Ethernet PHY
- MAC Address EEPROM
- 32 MByte QSPI Flash memory (with XiP support)
- 100 MHz programmable MEMS oscillator
- Plug-on module with 2 × 100-pin high-speed hermaphroditic strips
- 112 FPGA I/O's (51 differential pairs) available on board-to-board connectors
- On-board high-efficiency DC-DC converters
 - 4.0 A x 1.0 V power rail
 - 1.0 A x 1.8 V power rail
 - 1.0 A x 1.5 V power rail
- · System management and power sequencing
- eFUSE bit-stream encryption
- AES bit-stream encryption
- User LED
- Evenly spread supply pins for good signal integrity

Other assembly options for cost or performance optimization plus high volume prices available on request.









TE0711 Series

Xilinx Artix-7 T, Flash, USB, high pin count, low cost





Key Features

- Industrial-grade Xilinx Artix-7 (15T to 100T) SoM, supported by the free Xilinx Vivado WebPACK tool
- · Rugged for shock and high vibration
- 32 MByte QSPI Flash memory (with XiP support)
- 100 MHz programmable MEMS oscillator
- Plug-on module with 2 × 100-pin and 1 × 60-pin high-speed hermaphroditic strips
- 178 FPGA I/O's (84 differential pairs) available on board-to-board connectors
- On-board high-efficiency DC-DC converters
 - 4.0 A x 1.0 V power rail
 - 1.0 A x 1.8 V power rail
- System management and power sequencing
- eFUSE bit-stream encryption
- AES bit-stream encryption
- 3 user LEDs
- FTDI USB to UART/FIFO bridge
- Evenly-spread supply pins for good signal integrity

Other assembly options for cost or performance optimization plus high volume prices available on request.



Trenz Electronic TE0711 are industrial-grade FPGA modules integrating a Xilinx Artix-7 T FPGA, 32 MByte Flash memory for configuration and operation, and powerful switch-mode power supplies for all on-

number of board voltages. A large configurable I/O's is provided via rugged high-speed stacking strips. All modules in 4×5 cm form factor are mechanically compatible.

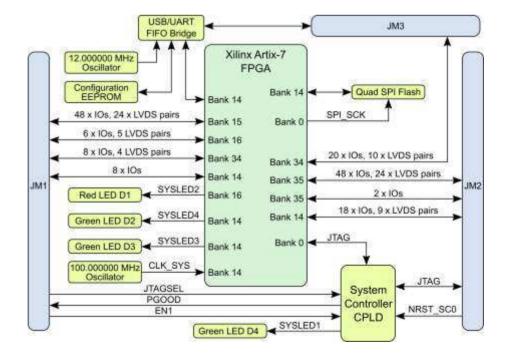
All this on a tiny footprint, smaller than a credit card, at the most competitive price.











TE0712 Series

Xilinx Artix-7 T, DDR3, Flash, 100 MBit Ethernet, 4 x Transceiver, High pin count





Key Features

- Industrial-grade Xilinx Artix-7 (15T to 200T)
 SoM, supported by the free Xilinx Vivado WebPACK tool
- · Rugged for shock and high vibration
- 1 GByte DDR3 SDRAM
- 100 MBit Ethernet PHY
 - MAC Address EEPROM
- 32 MByte QSPI Flash memory (with XiP support)
- · Programmable clock generator
 - Transceiver clock (default 125 MHz)
 - Fabric clock (default 200 MHz)
- Plug-on module with 2 × 100-pin and 1 × 60-pin high-speed hermaphroditic strips
- 158 FPGA I/O's (78 differential pairs) available on board-to-board connectors
- 4 GTP (high-performance transceiver) lanes
 - GTP (high-performance transceiver) clock input
- On-board high-efficiency DC-DC converters
 - 12 A x 1.0 V power rail
 - 1.5 A x 1.8 V power rail
 - 1.5 A x 1.5 V power rail
- System management and power sequencing
- eFUSE bit-stream encryption
- · AES bit-stream encryption
- User LED
- Evenly spread supply pins for good signal integrity

Other assembly options for cost or performance optimization plus high volume prices available on request.

Overview

Trenz Electronic TE0712 are industrial-grade FPGA modules integrating a Xilinx Artix-7 T FPGA, a Megabit Ethernet transceiver (physical layer), 1 Gigabyte DDR3 SDRAM with 32-Bit width, 32 Megabyte Flash memory for configuration and operation, and powerful switch-mode power supplies for all on-board voltages. A large number of configurable I/O's is provided via rugged high-speed stacking strips. All modules in 4x5 cm form factor are mechanically compatible.

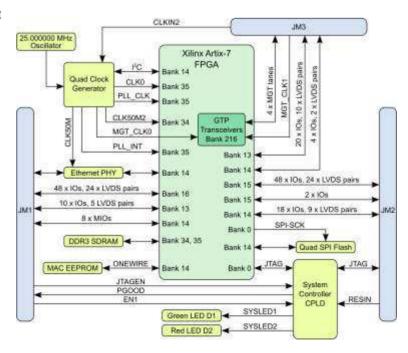
All this on a tiny footprint, smaller than a credit card, at the most competitive price.











TE0713 Series

Xilinx Artix-7 T, DDR3L, USB 3.0, 4 GTP lanes





Overview

The Trenz Electronic TE0713 is an industrial-grade FPGA module integrating a Xilinx Artix-7 FPGA, USB 3.0 to FIFO bridge, 1 GByte of DDR3L SDRAM, 32 MByte Flash memory for configuration and operation, and powerful switching-mode power supplies for all on-board voltages. Numerous configurable I/O's are provided via rugged high-speed strips. Modules in 4 x 5 cm form factor are fully mechanically and largely electrically compatible among them. All this on a tiny footprint, smaller than a credit card, at the most competitive price.

All modules produced by Trenz Electronic are developed and manufactured in Germany.

Extended device life cycle



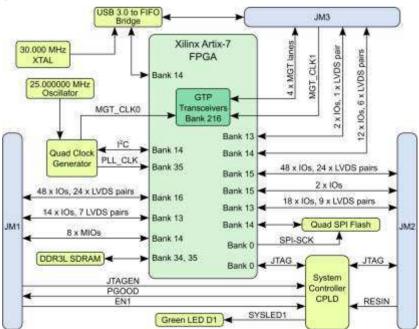




Key Features

- Xilinx Artix-7 (15T to 200T) SoM
- Both industrial and commercial temperature ranges available
- Rugged for high shock and high vibration resistance
- 1 GByte DDR3L 32-bit SDRAM
- 32 MByte QSPI Flash memory (with XiP support)
- USB 3.0 to FIFO interface bridge
- Programmable clock quad generator
 - GTP transceiver clock (default 125 MHz)
 - Fabric clock (default 200 MHz)
- Plug-on module with 2 x 100-pin and 1 x 60-pin highspeed hermaphroditic strips
- 152 FPGA I/O's (75 differential pairs) available via B2B connectors
- 4 GTP (multi Gigabit transceiver) lanes
- External clock input for GTP transceivers via B2B connector
- On-board high-efficiency DC-DC converters
- System management and power sequencing
- eFUSE bit-stream encryption
- AES bit-stream encryption
- User configurable LED
- Evenly spread supply pins for good signal integrity.

Other assembly options for cost or performance optimization plus high volume prices available on request.







Overview

The Trenz Electronic TE0714 is an industrial-grade FPGA module integrating a Xilinx Artix-7 (A15T, A35T, A50T), 16 MByte Flash memory for configuration and operation and powerful switch-mode power supplies for all on-board voltages. A large number of configurable I/O's is provided via rugged highspeed stacking strips.

All this on a tiny footprint, smaller than a credit card, at the most competitive price.

All modules produced by Trenz Electronic are developed and manufactured in Germany.











Key Features

- Xilinx Artix-7 (A15T, A35T, A50T)
- · Rugged for shock and high vibration
- 16 MByte QSPI Flash memory
- Dimensions: 4 x 3 cm
- · Differential MEMS Oscillator for GT Clocking
- MEMS Oscillator for PL Clocks (option)
- Plug-on module with 2 × 100-pin high-speed hermaphroditic strips
 - 144 FPGA I/O's (max 68 differential)
 - XADC Analog Input
 - 4 GTP (high-performance transceiver) lanes
 - GT Reference Clock input
 - Optimized I/O and power pins for good signal integrity
- · On-board high-efficiency DC-DC converters
- eFUSE bit-stream encryption (AES)
- · One user LED

Other assembly options for cost or performance optimization plus high volume prices available on request.

