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AUTOMOTIVE

ROHS

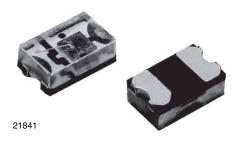
GREEN

(5-2008)**



Vishay Semiconductors

Silicon PIN Photodiode



DESCRIPTION

TEMD7000X01 is a high speed and high sensitive PIN photodiode. It is a miniature surface mount device (SMD) including the chip with a 0.23 mm² sensitive area detecting visible and near infrared radiation.

FEATURES

Package type: surface mount

• Package form: 0805

• Dimensions (L x W x H in mm): 2 x 1.25 x 0.85

• Radiant sensitive area (in mm²): 0.23

· High photo sensitivity

· High radiant sensitivity

• Suitable for visible and near infrared radiation

Fast response times

• Angle of half sensitivity: $\varphi = \pm 60^{\circ}$

• Floor life: 168 h, MSL 3, acc. J-STD-020

· Lead (Pb)-free reflow soldering

• AEC-Q101 qualified

 Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

Note

** Please see document "Vishay Material Category Policy": www.vishav.com/doc?99902

APPLICATIONS

• High speed photo detector

PRODUCT SUMMARY			
COMPONENT	I _{ra} (μΑ)	I _{ra} (μA) φ (deg)	
TEMD7000X01	3	± 60	430 to 1100

Note

• Test conditions see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE PACKAGING		REMARKS	PACKAGE FORM	
TEMD7000X01	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel	0805	

Note

· MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V _R	60	V	
Power dissipation	T _{amb} ≤ 25 °C	P _V	215	mW	
Junction temperature		Tj	100	°C	
Operating temperature range		T _{amb}	- 40 to + 100	°C	
Storage temperature range		T _{stg}	- 40 to + 100	°C	
Soldering temperature	Acc. reflow solder profile fig. 8	T _{sd}	260	°C	
Thermal resistance junction/ambient	Acc. J-STD-051	R _{thJA}	270	K/W	

PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I _F = 50 mA	V _F		1		V
Breakdown voltage	I _R = 100 μA, E = 0	V _(BR)	60			V
Reverse dark current	V _R = 10 V, E = 0	I _{ro}		1	3	nA
Diode capacitance	V _R = 0 V, f = 1 MHz, E = 0	C_{D}		4		pF
	V _R = 5 V, f = 1 MHz, E = 0	C_{D}		1.3		pF
Open circuit voltage	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}$	Vo		350		mV
Temperature coefficient of Vo	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}$	TK _{Vo}		- 2.6		mV/K
Short circuit current	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}$	l _k		3		μΑ
Temperature coefficient of I _k	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}$	TK _{lk}		0.1		%/K
Reverse light current	$E_e = 1 \text{ mW/cm}^2$, $\lambda = 950 \text{ nm}$, $V_R = 5 \text{ V}$	I _{ra}	2.4	3	3.6	μΑ
Angle of half sensitivity		φ		± 60		deg
Wavelength of peak sensitivity		λ_{p}		900		nm
Range of spectral bandwidth		λ _{0.1}		430 to 1100		nm
Rise time	$V_R = 10 \text{ V}, R_L = 1 \text{ k}\Omega, \lambda = 820 \text{ nm}$	t _r		100		ns
Fall time	$V_R = 10 \text{ V}, R_L = 1 \text{ k}\Omega, \lambda = 820 \text{ nm}$	t _f		100		ns

BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

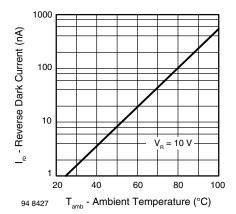


Fig. 1 - Reverse Dark Current vs. Ambient Temperature

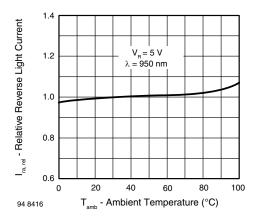


Fig. 2 - Relative Reverse Light Current vs. Ambient Temperature

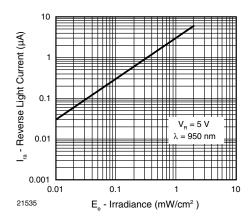


Fig. 3 - Reverse Light Current vs. Irradiance

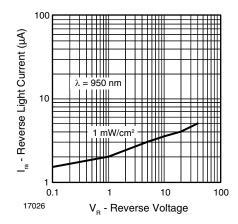


Fig. 4 - Reverse Light Current vs. Reverse Voltage

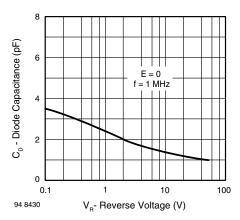


Fig. 5 - Diode Capacitance vs. Reverse Voltage

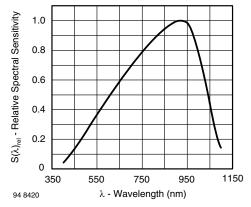


Fig. 6 - Relative Spectral Sensitivity vs. Wavelength

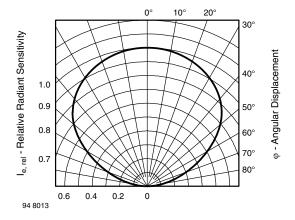


Fig. 7 - Relative Radiant Sensitivity vs. Angular Displacement



REFLOW SOLDER PROFILE

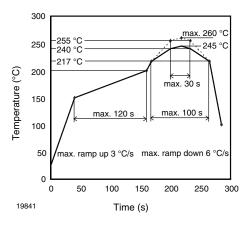


Fig. 8 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 168 h

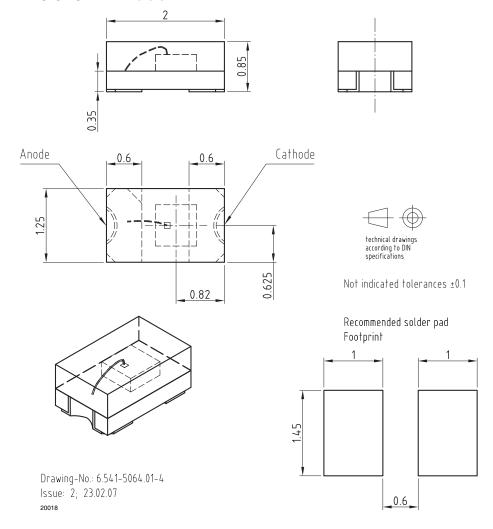
Conditions: T_{amb} < 30 °C, RH < 60 %

Moisture sensitivity level 3, acc. to J-STD-020.

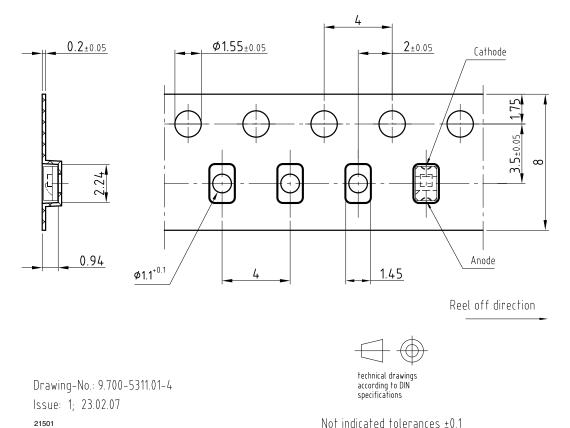
DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 $^{\circ}$ C (+ 5 $^{\circ}$ C), RH < 5 $^{\circ}$ M.

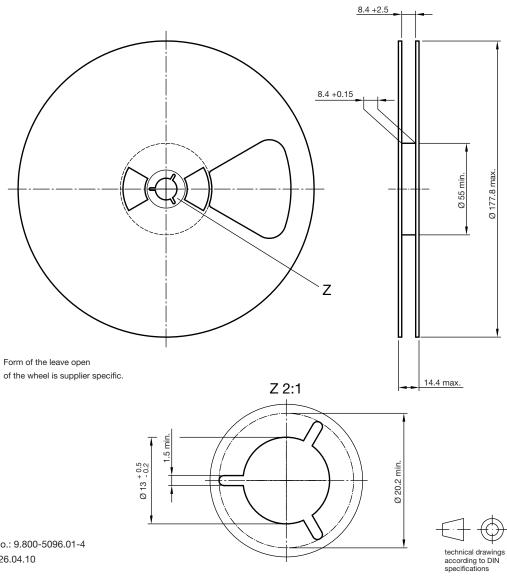
PACKAGE DIMENSIONS in millimeters



BLISTER TAPE DIMENSIONS in millimeters



REEL DIMENSIONS in millimeters



Drawing-No.: 9.800-5096.01-4

Issue: 2; 26.04.10

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Material Category Policy

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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