Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!

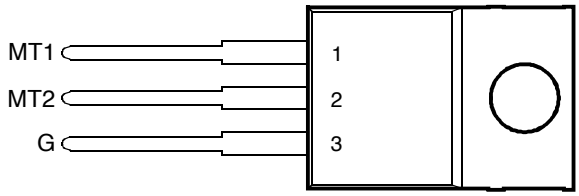
Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832
Email & Skype: info@chipsmall.com Web: www.chipsmall.com
Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China

BOURNS®

- **High Current Triacs**
- **12 A RMS**
- **Glass Passivated Wafer**
- **400 V to 800 V Off-State Voltage**
- **Max I_{GT} of 50 mA (Quadrants 1 - 3)**

TO-220 PACKAGE
(TOP VIEW)



Pin 2 is in electrical contact with the mounting base.

MDC2ACA

This series is currently available, but not recommended for new designs.

absolute maximum ratings over operating case temperature (unless otherwise noted)

RATING		SYMBOL	VALUE	UNIT
Repetitive peak off-state voltage (see Note 1)	TIC236D	V_{DRM}	400	V
	TIC236M		600	
	TIC236S		700	
	TIC236N		800	
Full-cycle RMS on-state current at (or below) 70°C case temperature (see Note 2)		$I_{T(RMS)}$	12	A
Peak on-state surge current full-sine-wave at (or below) 25°C case temperature (see Note 3)		I_{TSM}	100	A
Peak gate current		I_{GM}	±1	A
Operating case temperature range		T_C	-40 to +110	°C
Storage temperature range		T_{stg}	-40 to +125	°C
Lead temperature 1.6 mm from case for 10 seconds		T_L	230	°C

NOTES: 1. These values apply bidirectionally for any value of resistance between the gate and Main Terminal 1.
2. This value applies for 50-Hz full-sine-wave operation with resistive load. Above 70°C derate linearly to 110°C case temperature at the rate of 300 mA/°C.
3. This value applies for one 50-Hz full-sine-wave when the device is operating at (or below) the rated value of peak reverse volta ge and on-state current. Surge may be repeated after the device has returned to original thermal equilibrium.

electrical characteristics at 25°C case temperature (unless otherwise noted)

PARAMETER		TEST CONDITIONS			MIN	TYP	MAX	UNIT
I_{DRM}	Repetitive peak off-state current	V_D = Rated V_{DRM}	I_G = 0	T_C = 110°C			±2	mA
I_{GT}	Gate trigger current	V_{supply} = +12 V†	R_L = 10 Ω	$t_{p(g)}$ > 20 µs		12	50	mA
		V_{supply} = +12 V†	R_L = 10 Ω	$t_{p(g)}$ > 20 µs		-19	-50	
		V_{supply} = -12 V†	R_L = 10 Ω	$t_{p(g)}$ > 20 µs		-16	-50	
		V_{supply} = -12 V†	R_L = 10 Ω	$t_{p(g)}$ > 20 µs		34		
V_{GT}	Gate trigger voltage	V_{supply} = +12 V†	R_L = 10 Ω	$t_{p(g)}$ > 20 µs		0.8	2	V
		V_{supply} = +12 V†	R_L = 10 Ω	$t_{p(g)}$ > 20 µs		-0.8	-2	
		V_{supply} = -12 V†	R_L = 10 Ω	$t_{p(g)}$ > 20 µs		-0.8	-2	
		V_{supply} = -12 V†	R_L = 10 Ω	$t_{p(g)}$ > 20 µs		0.9	2	
V_T	On-state voltage	I_{TM} = ±17 A	I_G = 50 mA	(see Note 4)		±1.4	±2.1	V

† All voltages are with respect to Main Terminal 1.

NOTE 4: This parameter must be measured using pulse techniques, t_p = ≤ 1 ms, duty cycle ≤ 2 %. Voltage-sensing contacts separate from the current carrying contacts are located within 3.2 mm from the device body.

electrical characteristics at 25°C case temperature (unless otherwise noted) (continued)

PARAMETER		TEST CONDITIONS			MIN	TYP	MAX	UNIT
I_H	Holding current	V_{supply} = +12 V†	I_G = 0	Init' I_{TM} = 100 mA		22	40	mA
		V_{supply} = -12 V†	I_G = 0	Init' I_{TM} = -100 mA		-12	-40	
I_L	Latching current	V_{supply} = +12 V†	(see Note 5)				80	mA
		V_{supply} = -12 V†					-80	
dv/dt	Critical rate of rise of off-state voltage	V_D = Rated V_D	I_G = 0	T_C = 110°C		±400		V/µs
$dv/dt_{(c)}$	Critical rise of commutation voltage	V_D = Rated V_D		T_C = 80°C	±1.2	±9		V/µs
		di/dt = 0.5 $I_{T(RMS)}$/ms		I_T = 1.4 $I_{T(RMS)}$				
di/dt	Critical rate of rise of on -state current	V_D = Rated V_D	I_{GT} = 50 mA	T_C = 110°C		±100		A/µs
		di_G/dt = 50 mA/µs						

† All voltages are with respect to Main Terminal 1.

NOTE 5: The triacs are triggered by a 15-V (open-circuit amplitude) pulse supplied by a generator with the following characteristics: R_G = 100 Ω, $t_{p(g)}$ = 20 µs, t_r = ≤ 15 ns, f = 1 kHz.

thermal characteristics

PARAMETER		MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction to case thermal resistance			2	°C/W
$R_{\theta JA}$	Junction to free air thermal resistance			62.5	°C/W

TYPICAL CHARACTERISTICS

GATE TRIGGER CURRENT vs CASE TEMPERATURE

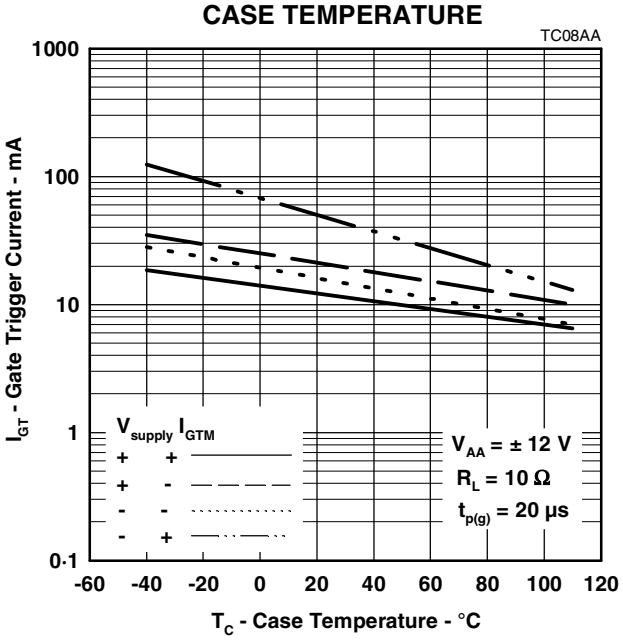


Figure 1.

GATE TRIGGER VOLTAGE vs CASE TEMPERATURE

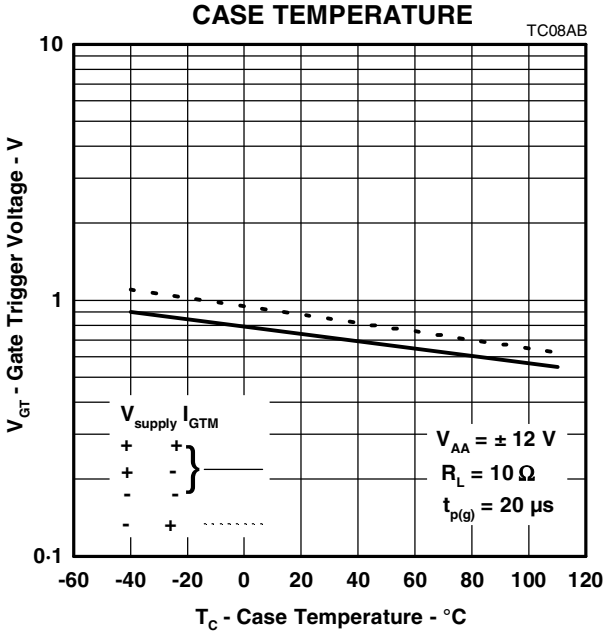


Figure 2.

TYPICAL CHARACTERISTICS

HOLDING CURRENT
vs
CASE TEMPERATURE

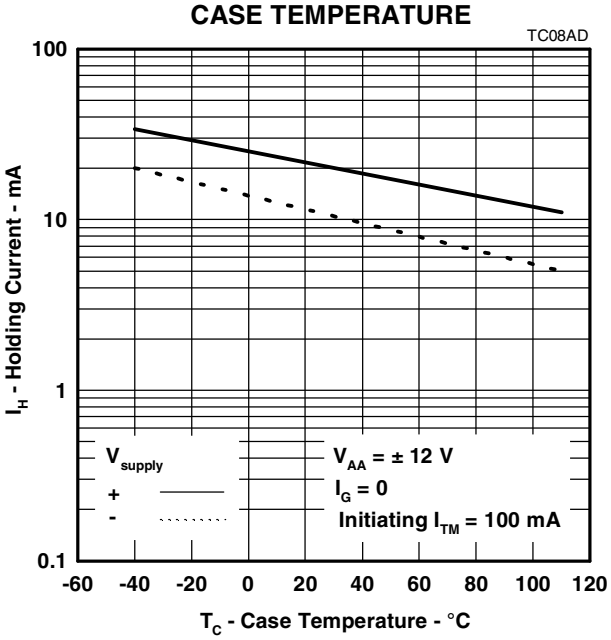


Figure 3.

LATCHING CURRENT
vs
CASE TEMPERATURE

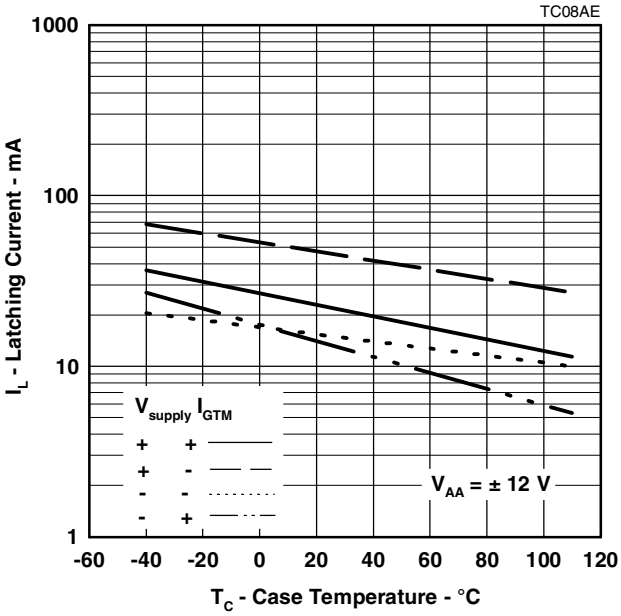


Figure 4.