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Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







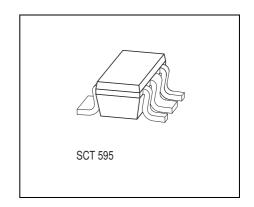


Low-Drop Voltage Tracker

TLE 4250 G

Features

- Output tracking tolerance ≤ ± 0.5%
- 50 mA output current
- Combined Tracking/Enable input
- Very low current consumption in off mode
- Low drop voltage
- Suitable for use in automotive electronics
- Wide operation range: up to 40 V
- Wide temperature range: 40 °C ≤ T_i ≤ 150 °C
- · Output protected against short circuit
- Overtemperature protection
- · Reverse polarity proof
- Very small SMD-Package SCT 595



Туре	Ordering Code	Package
TLE 4250 G	Q67006-A9351	SCT-595 (SMD)

Functional Description

The TLE 4250 G is a monolithic integrated low-drop voltage tracker in the very small SMD package SCT 595. It is designed to supply e.g. sensors under the severe conditions of automotive applications. Therefore the device is equipped with additional protection functions against overload, short circuit and reverse polarity.

Supply voltages up to 40 V are tracked to a reference voltage at the adjust input. Therefore the Adjust pin has to be connected to a reference voltage, e.g. to a 5 V supply on a micro-controller port.

The output is able to drive a load up to 50 mA while it follows the output of a main voltage regulator within an accuracy of 0.5%.

The TLE 4250 G can be switched in stand-by mode via the adjust input which causes the current consumption to drop to very low values. This feature makes the IC suitable for low power battery applications.



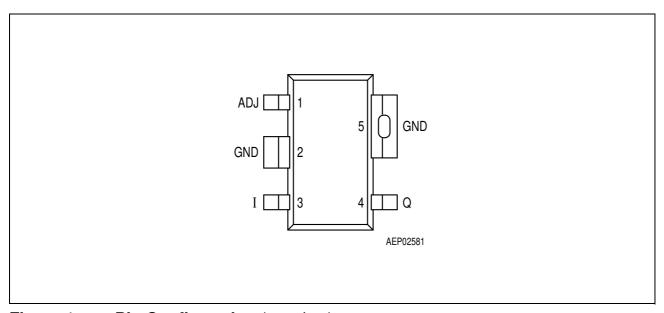


Figure 1 Pin Configuration (top view)

Pin Definitions and Functions

Pin No.	Symbol	Function
1	ADJ	Adjust/Enable input; connect to the reference voltage via ext. resistor or micro-controller port; high active input
2	GND	Ground; internally connected to pin 5
3	I	Input voltage
4	Q	Output voltage; must be blocked by a capacitor $C_{\rm Q} \ge$ 1 μ F, 2 $\Omega \le$ ESR \le 7 Ω
5	GND	Ground



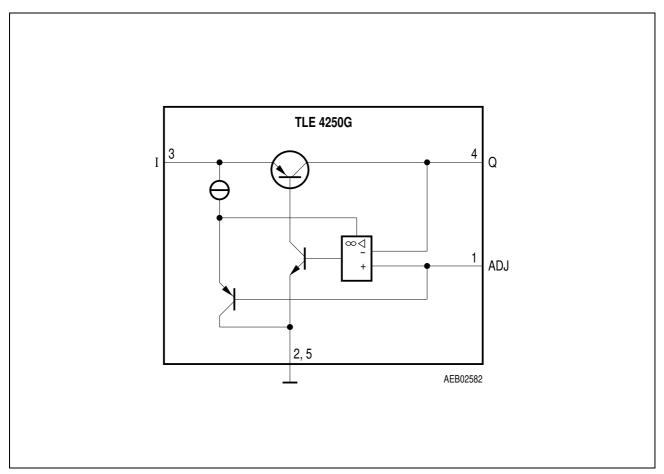


Figure 2 Block Diagram



Absolute Maximum Ratings

 $-40 \, ^{\circ}\text{C} < T_{\text{j}} < 150 \, ^{\circ}\text{C}$

Parameter	Symbol	Limit Values		Unit	Remarks
		min.	max.		

Input

Voltage	V_{l}	- 42	45	V	_
Current	I_{I}	_	_	mA	internally limited

Output

Voltage	V_{Q}	-1	40	V	_
Current	I_{Q}	_	_	mA	internally limited

Adjust

Voltage	V_{ADJ}	- 0.3	40	V	_
Current	I_{ADJ}	_	_	μΑ	internally limited

Temperatures

Junction temperature	$T_{\rm j}$	- 40	150	°C	_
Storage temperature	$T_{ m stg}$	- 50	150	°C	_

Thermal Resistances

Junction pin	$R_{ ext{thj-pin}}$	_	30	K/W	measured to pin 5
Junction ambient	$R_{\rm thja}$	_	99	K/W	1)

 $^{^{1)}}$ Worst case, regarding peak temperature; zero airflow; mounted an a PCB $80\times80\times1.5$ mm³, heat sink area $300~\text{mm}^2.$

Note: Maximum ratings are absolute ratings; exceeding any one of these values may cause irreversible damage to the integrated circuit.



Operating Range

Parameter	Symbol	Limit Values		Unit	Remarks
		min.	max.		
Input voltage	V_{l}	4	40	V	_
Adjust input voltage	V_{ADJ}	2.5	36	V	_
Junction temperature	T_{i}	- 40	150	°C	_



Electrical Characteristics

 $V_{\rm I}$ = 13.5 V; $V_{\rm ADJ}$ > 2.5 V; - 40 °C < $T_{\rm j}$ < 150 °C; unless otherwise specified

Parameter	Symbol	Limit Values			Unit	Test Condition
		min.	typ.	max.		

Output

•						
Output voltage tracking accuracy $\Delta V_{\rm Q} = V_{\rm ADJ} - V_{\rm Q}$	$\Delta V_{ m Q}$	– 25	_	25	mV	$6 \text{ V} < V_{\text{I}} < 28 \text{ V}$ 1 mA < $I_{\text{Q}} < 50 \text{ mA}$
Output voltage tracking accuracy	ΔV_{Q}	– 25	_	25	mV	$6 \text{ V} < V_{\text{I}} < 40 \text{ V}$ 1 mA < $I_{\text{Q}} < 10 \text{ mA}$
Output voltage tracking accuracy	ΔV_{Q}	- 5	_	5	mV	$6 \text{ V} < V_{\text{I}} < 16 \text{ V}$ 1 mA < $I_{\text{Q}} < 10 \text{ mA}$
Drop voltage	V_{dr}	_	100	300	mV	$I_{\rm Q}$ = 10 mA; $V_{\rm ADJ}$ > 4 V ¹⁾
Output current	I_{Q}	50	70	_	mA	1) T _j < 125 °C
Output capacitor	C_{Q}	1	_	_	μF	at 10 kHz; 2 $\Omega \le ESR \le 7 \Omega$
Current consumption $I_{q} = I_{l} - I_{Q}$	I_{q}	_	1.5	3.0	mA	$I_{\rm Q}$ < 30 mA
Current consumption $I_{q} = I_{l} - I_{Q}$	I_{q}	_	80	150	μΑ	$I_{\rm Q}$ < 1 mA
Quiescent current (stand-by) $I_{q} = I_{l} - I_{Q}$	I_{q}	_	10	20	μΑ	$V_{\rm ADJ} = 0 \text{ V}$ $T_{\rm j} < 85 ^{\circ}\text{C}$
Current consumption (drop area)	I_{q}	_	_	3	mA	$V_{\text{ADJ}} = V_{\text{I}} = 5 \text{ V}$ $I_{\text{Q}} = 0 \text{ mA}$
Load regulation	ΔV_{Q}	– 15	_	15	mV	1 mA < $I_{\rm Q}$ < 30 mA
Line regulation	ΔV_{Q}	– 10	_	10	mV	$6 \text{ V} < V_{\text{I}} < 40 \text{ V}$ $I_{\text{Q}} = 10 \text{ mA}$

 $[\]overline{\ ^{1)}}$ Measured when the output voltage $V_{\rm Q}$ has dropped 100 mV from the nominal value.



Electrical Characteristics (cont'd)

 $V_{\rm I}$ = 13.5 V; $V_{\rm ADJ}$ > 2.5 V; - 40 °C < $T_{\rm j}$ < 150 °C; unless otherwise specified

Parameter	Symbol	Limit Values			Unit	Test Condition
		min.	typ.	max.		
Power-Supply-Ripple- Rejection	PSRR	_	60	_	dB	$f_{\rm r}$ = 100 Hz $V_{\rm r}$ = 0.5 $V_{\rm PP}$

Adjust/Enable Input

Input biasing current	I_{ADJ}	_	0.1	0.5	μμΑ	$V_{ADJ} = 5 \; V$
Adjust low voltage to disable	V_{ADJ}	_	_	8.0	V	$T_{\rm j}$ < 125 °C $V_{\rm Q}$ off
Adjust range	V_{ADJ}	2.0	_	36	V	$V_{\rm Q} - V_{\rm ADJ}$ < 25 mV $T_{\rm j}$ < 125 °C

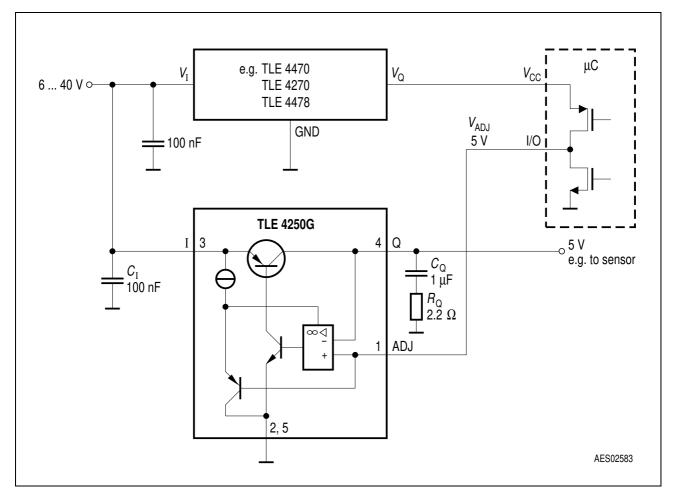
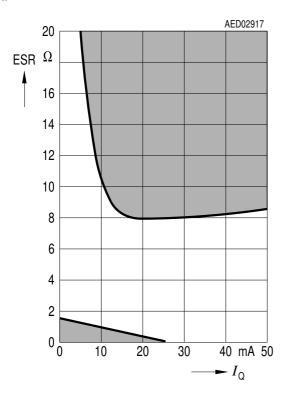


Figure 3 Application Circuit



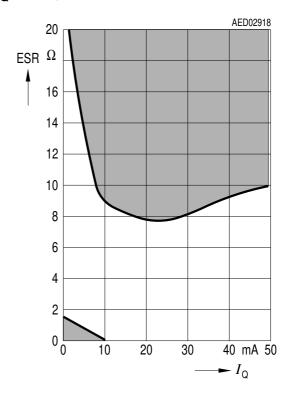
Region of Stability for

 $C_{\rm Q}$ = 1 μ F



Region of Stability for

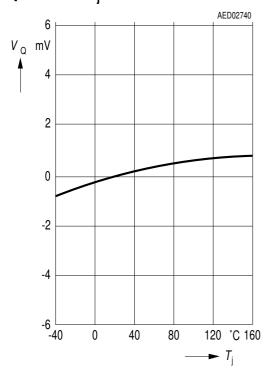
 $C_{\rm Q}$ = 2.2 $\mu {\sf F}$



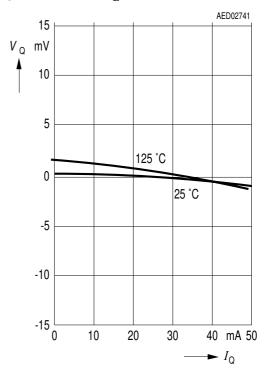


Typical Performance Characteristics

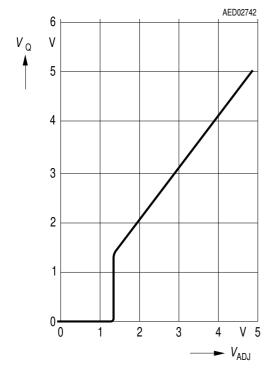
Tracking Accuracy $\Delta V_{\rm Q}$ versus Temperature $T_{\rm i}$



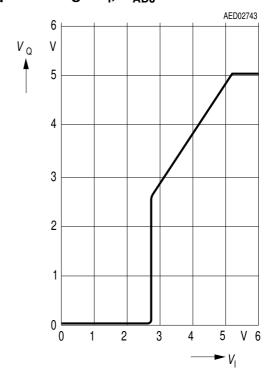
Tracking Accuracy $\Delta V_{\rm Q}$ versus Output Current $I_{\rm Q}$



Output Voltage $V_{\rm Q}$ versus Adjust Voltage $V_{\rm ADJ},\,V_{\rm I}$ > $V_{\rm ADJ}$

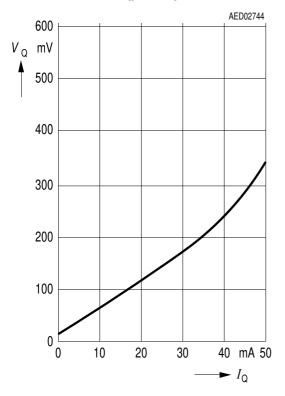


Output Voltage $V_{\rm Q}$ versus Input Voltage $V_{\rm I},\,V_{\rm ADJ}$ = 5 V

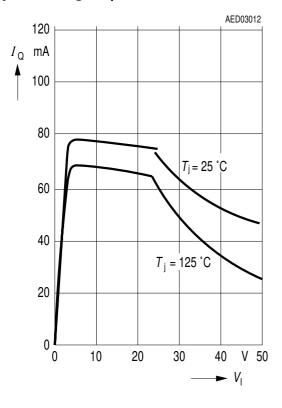




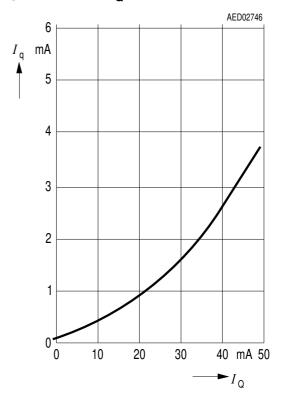
Drop Voltage V_{DR} versus Output Current $I_{\mathrm{Q}},\,V_{\mathrm{ADJ}}$ = 5 V



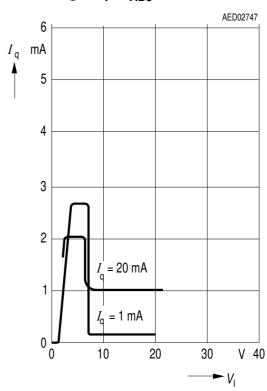
Output Current I_{Q} versus Input Voltage V_{I}



Current Consumption $I_{\rm q}$ versus Output Current $I_{\rm Q}$

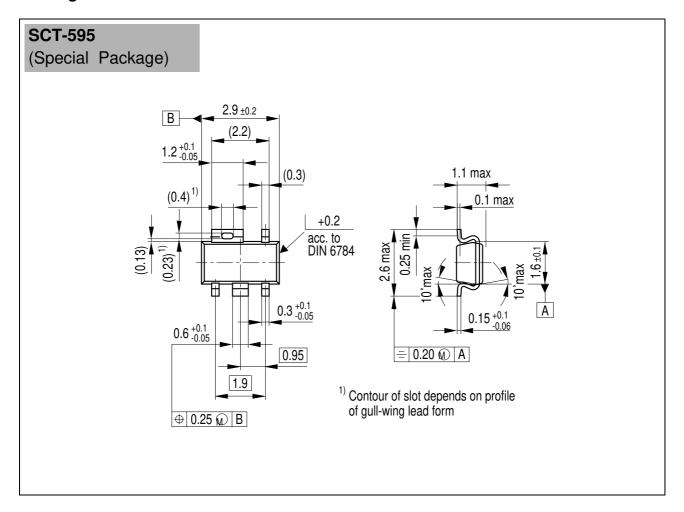


Current Consumption I_q versus Input Voltage V_l , V_{ADJ} = 5 V





Package Outlines



Sorts of Packing

Package outlines for tubes, trays etc. are contained in our Data Book "Package Information".

SMD = Surface Mounted Device

Dimensions in mm



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