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5 V/10 V Low Drop Voltage Regulator

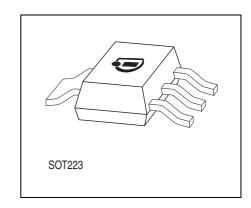
TLE 4266





Features

- Output voltage 5 V or 10 V
- Output voltage tolerance ≤ ±2%
- 120 mA current capability
- Very low current consumption
- Low-drop voltage
- Overtemperature protection
- Reverse polarity proof
- Wide temperature range
- Suitable for use in automotive electronics
- Inhibit
- Green Product (RoHS compliant)
- AEC Qualified



Functional Description

TLE 4266 is a low-drop voltage regulator for 5 V or 10 V supply in a PG-SOT223-4 SMD package. The IC regulates an input voltage $V_{\rm I}$ in the range of 5.5 V/10.5 V < $V_{\rm I}$ < 45 V to $V_{\rm Q,nom}$ = 5 V/10 V. The maximum output current is more than 120 mA. The IC can be switched off via the inhibit input, which causes the current consumption to drop below 10 μ A. The IC is shortcircuit-proof and incorporates a temperature protection which turns off the IC at overtemperature.

Choosing External Components

The input capacitor $C_{\rm I}$ is necessary for compensating line influences. Using a resistor of approx. 1 Ω in series with $C_{\rm I}$, the oscillating of input line inductivity and input capacitance can be clamped. The output capacitor $C_{\rm Q}$ is necessary for the stability of the regulating circuit. Stability is guaranteed at values $C_{\rm Q} \ge 10~\mu{\rm F}$ and an ESR $\le 10~\Omega$ within the whole operating temperature range.

Туре	Package
TLE 4266 G	PG-SOT223-4
TLE 4266 GSV10	PG-SOT223-4

Data Sheet 1 Rev. 2.5, 2008-03-10



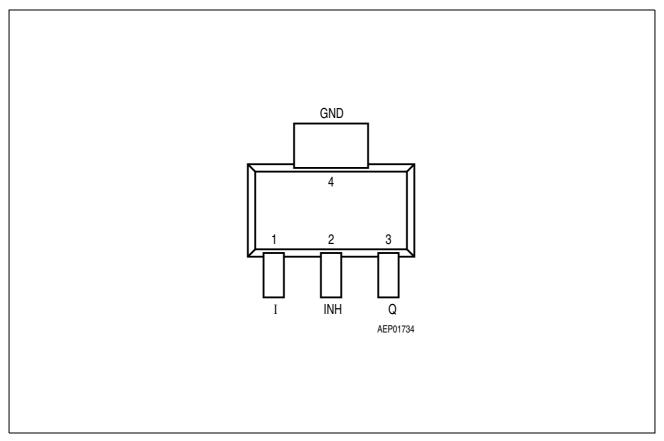


Figure 1 Pin Configuration (top view)

Table 1 Pin Definitions and Functions

Pin	Symbol	Function
1	I	Input voltage ; block to ground directly at the IC with a ceramic capacitor.
2	INH	Inhibit; low-active input.
3	Q	Output voltage; block to ground with a capacitor $C_Q \ge 10 \mu F$.
4	GND	Ground

Data Sheet 2 Rev. 2.5, 2008-03-10



Circuit Description

The device includes a precise reference voltage, which is very accurate due to resistor adjustment. A control amplifier compares the divided output voltage to this reference voltage and drives the base of the PNP series transistor through a buffer.

Saturation control as a function of the load current prevents any oversaturation of the power element. The IC also incorporates a number of protection circuitry for:

- Overload
- Overtemperature
- Reverse polarity

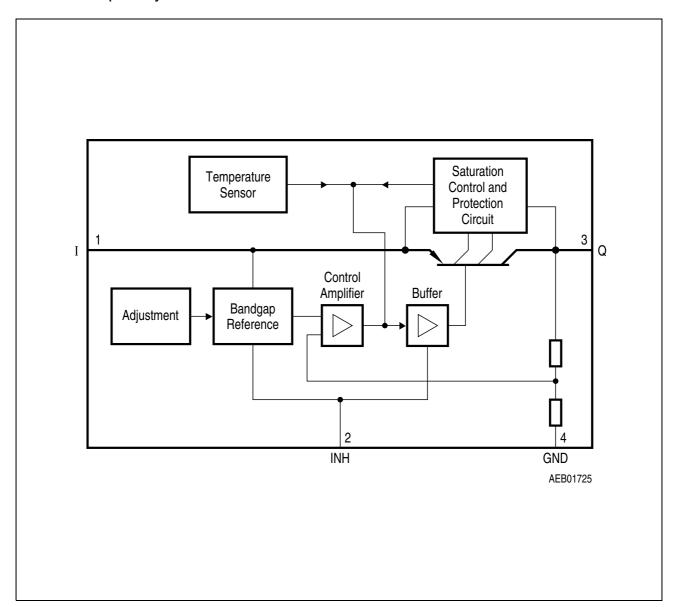


Figure 2 Block Diagram

Data Sheet 3 Rev. 2.5, 2008-03-10



Table 2 Absolute Maximum Ratings (TLE 4266 G, TLE 4266 GSV10)

 $T_{\rm i}$ = -40 to 150 °C

Parameter	Symbol	Lim	it Values	Unit	Notes	
		Min.	Min. Max.			
Input			<u> </u>			
Voltage	V_{l}	-42	45	V	_	
Current	I_{I}	_	_	_	internally limited	
Inhibit						
Voltage	V_{INH}	-42	45	V	_	
Output						
Voltage	V_{Q}	-1	32	V	_	
Current	I_{Q}	_	_	_	internally limited	
GND	•					
Current	I_{GND}	50	_	mA	_	
Temperature						
Junction temperature	T_{j}	_	150	°C	_	
Storage temperature	T_{S}	-50	150	°C	_	
Operating Range (TLE	4266 G)					
Input voltage	V_{I}	5.5	45	V	_	
Junction temperature	T_{j}	-40	150	°C	_	
Operating Range (TLE	4266 GSV10	0)	·			
Input voltage	V_{I}	10.5	45	V	_	
Junction temperature	T_{j}	-40	150	°C	_	
Thermal Resistance			•	•		
Junction ambient	$R_{\text{thj-a}}$	_	165	K/W	1)	
Junction case	$R_{ m thj\text{-}pin}$	_	17	K/W	measured to pin	

¹⁾ Package mounted on PCB $80 \times 80 \times 1.5 \text{ mm}^3$; 35μ Cu; 5μ Sn; Footprint only; zero airflow.

Data Sheet 4 Rev. 2.5, 2008-03-10



Table 3 Characteristics (TLE 4266 G)

 $V_{
m I}$ = 13.5 V; -40 °C $\leq T_{
m j} \leq$ 125 °C

Parameter	Symbol	Limit Values			Unit	Test Condition
		Min.	Тур.	Max.		
Output voltage	V_{Q}	4.9	5	5.1	V	5 mA $\leq I_{Q} \leq$ 100 mA 6 V $\leq V_{i} \leq$ 28 V
Output-current limitation	I_{Q}	120	150	_	mA	_
Current consumption $I_q = I_i - I_Q$	I_{q}	_	_	10	μΑ	$V_{INH} = 0 \; V;$ $T_{j} \leq 100 \; ^{\circ}C$
Current consumption $I_q = I_i - I_Q$	I_{q}	_	_	400	μΑ	$I_{\rm Q}$ = 1 mA Inhibit ON
Current consumption $I_q = I_i - I_Q$	I_{q}	_	10	15	mA	$I_{\rm Q}$ = 100 mA Inhibit ON
Drop voltage	V_{DR}	_	0.25	0.5	V	$I_{\rm Q}$ = 100 mA ¹⁾
Load regulation	$\Delta V_{Q,lo}$	_	_	40	mV	$I_{\rm Q}$ = 5 to 100 mA $V_{\rm i}$ = 6 V
Line regulation	$\Delta V_{Q,li}$	_	15	30	mV	$V_{\rm I}$ = 6 V to 28 V $I_{\rm Q}$ = 5 mA
Power supply ripple rejection	PSRR	_	54	_	dB	$f_{\rm r}$ = 100 Hz, $V_{\rm r}$ = 0.5 Vpp
Inhibit	<u> </u>					
Inhibit on voltage	$V_{INH, on}$	3.5	_	_	V	_
Inhibit off voltage	$V_{INH, off}$	_	_	0.8	V	_
Inhibit current	I_{INH}	5	15	25	μΑ	V_{INH} = 5 V

¹⁾ Drop voltage = V_i - V_Q (measured when the output voltage V_Q has dropped 100 mV from the nominal value obtained at V_i = 13.5 V).

Data Sheet 5 Rev. 2.5, 2008-03-10



Table 4 Characteristics (TLE 4266 GSV10)

 $V_{\rm I}$ = 13.5 V; -40 °C $\leq T_{\rm j} \leq$ 125 °C

Parameter	Symbol	Limit Values			Unit	Test Condition
		Min.	Тур.	Max.		
Output voltage	V_{Q}	9.8	10	10.2	V	$5 \text{ mA} \le I_{\text{Q}} \le 100 \text{ mA}$ $11 \text{ V} \le V_{\text{I}} \le 21 \text{ V}$
Output voltage	V_{Q}	9.8	10	10.2	V	1 mA $\leq I_{\rm Q} \leq$ 50 mA 11 V $\leq V_{\rm I} \leq$ 28 V
Output-current limitation	I_{Q}	120	150	200	mA	_
Current consumption $I_q = I_l - I_Q$	$I_{q,off}$	_	_	10	μΑ	$V_{\text{INH}} = 0 \text{ V};$ $T_{\text{j}} \le 100 \text{ °C}$
Current consumption $I_q = I_l - I_Q$	I_{q}	_	350	500	μΑ	$I_{\rm Q}$ < 1 mA Inhibit ON
Current consumption $I_q = I_l - I_Q$	I_{q}	_	7	15	mA	$I_{\rm Q}$ < 100 mA Inhibit ON
Drop voltage	V_{DR}	_	0.28	0.5	V	$I_{\rm Q}$ = 100 mA ¹⁾
Load regulation	$\Delta V_{Q,Lo}$	-80	_	80	mV	$I_{\rm Q}$ = 5 to 100 mA $V_{\rm I}$ = 11 V
Line regulation	$\Delta V_{Q,Li}$	-30	5	30	mV	$V_{\rm I}$ = 11 V to 28 V $I_{\rm Q}$ = 5 mA
Power supply ripple rejection	PSRR	_	54	_	dB	$f_{\rm r}$ = 100 Hz, $V_{\rm r}$ = 0.5 Vpp
Inhibit						
Inhibit on voltage	$V_{INH, on}$	3.5	_	_	V	_
Inhibit off voltage	$V_{INH, off}$	_	_	0.8	V	_
Inhibit current	I_{INH}	5	12	25	μΑ	V_{INH} = 5 V

¹⁾ Drop voltage = $V_{\rm I}$ - $V_{\rm Q}$ measured when the output voltage $V_{\rm Q}$ has dropped 100 mV from the nominal value.

Data Sheet 6 Rev. 2.5, 2008-03-10



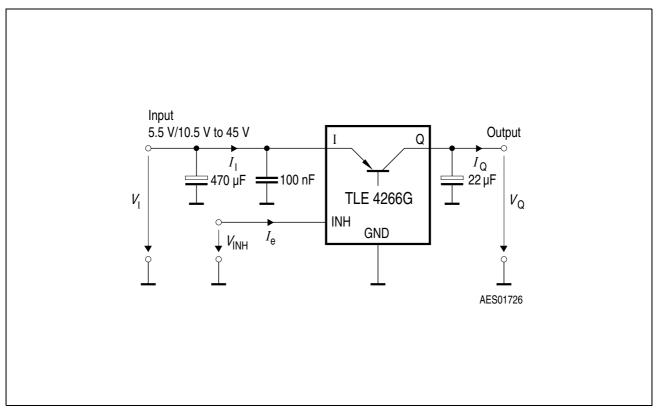


Figure 3 Measuring Circuit (TLE 4266 G, TLE 4266 GSV10)

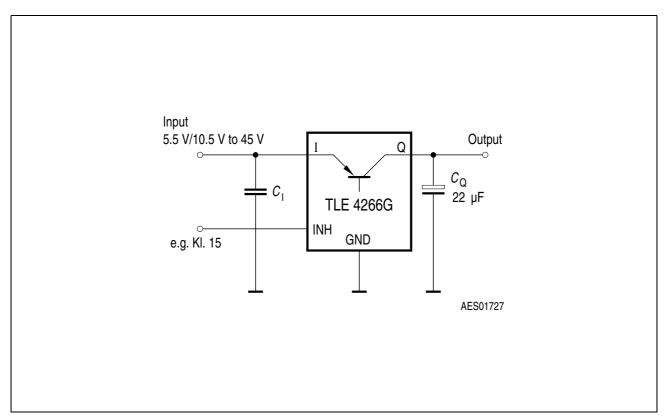
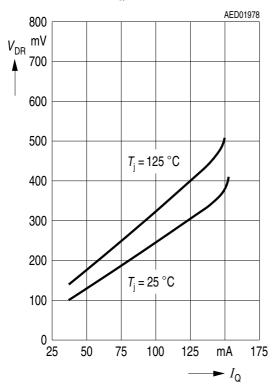


Figure 4 Application Circuit (TLE 4266 G, TLE 4266 GSV10)

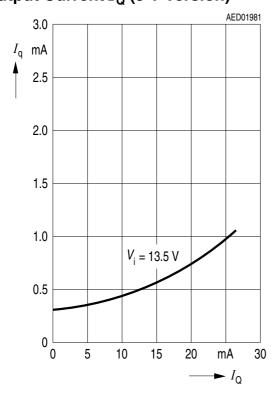
Data Sheet 7 Rev. 2.5, 2008-03-10



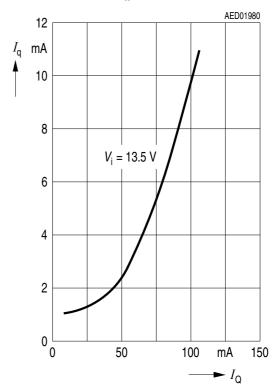
Drop Voltage V_{DR} versus Output Current I_{Q} (5 V, 10 V)



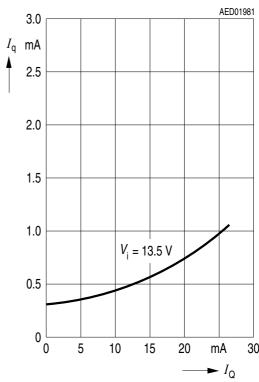
Current Consumption I_q versus Output Current I_Q (5 V version)



Current Consumption I_q versus Output Current I_Q (5 V)

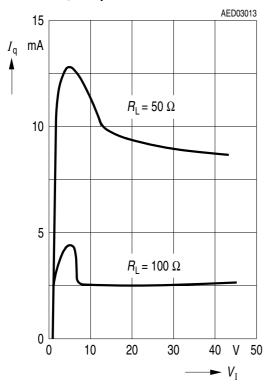


Current Consumption $I_{\rm q}$ versus Output Current $I_{\rm Q}$ (10 V version)

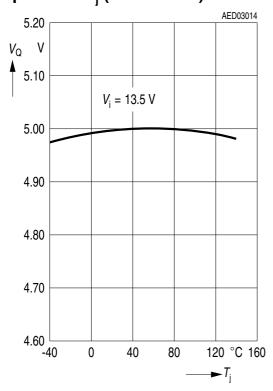




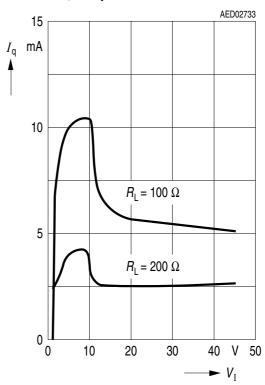
Current Consumption I_q versus Input Voltage V_1 (5 V version)



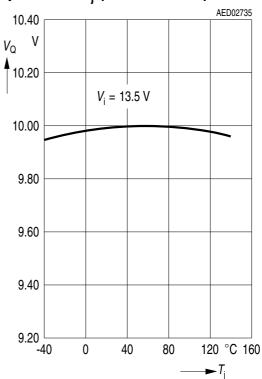
Output Voltage $V_{\rm Q}$ versus Temperature $T_{\rm i}$ (5 V version)



Current Consumption $I_{\rm q}$ versus Input Voltage $V_{\rm I}$ (10 V version)

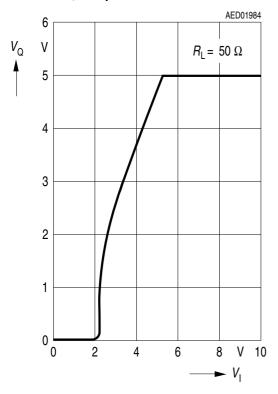


Output Voltage $V_{\rm Q}$ versus Temperature $T_{\rm i}$ (10 V version)

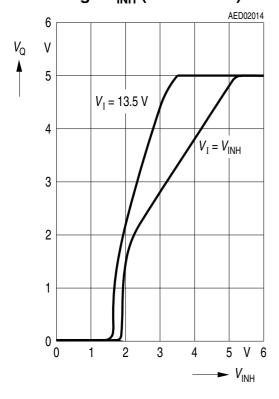




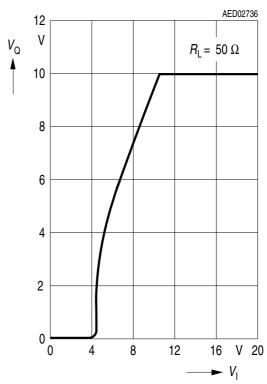
Output Voltage $V_{\rm Q}$ versus Input Voltage $V_{\rm I}$ (5 V version)



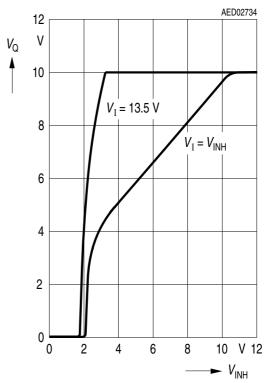
Output Voltage V_{Q} versus Inhibit Voltage V_{INH} (5 V version)



Output Voltage $V_{\rm Q}$ versus Input Voltage $V_{\rm I}$ (10 V version)

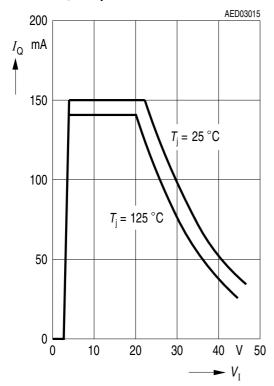


Output Voltage $V_{\rm Q}$ versus Inhibit Voltage $V_{\rm INH}$ (10 V version)

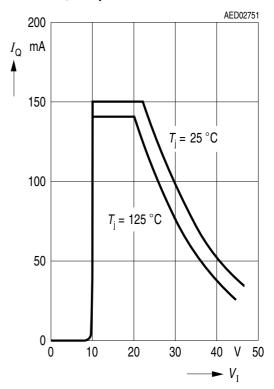




Output Current $I_{\rm Q}$ versus Input Voltage $V_{\rm I}$ (5 V-version)



Output Current $I_{\rm Q}$ versus Input Voltage $V_{\rm I}$ (10 V version)



Data Sheet 11 Rev. 2.5, 2008-03-10



Package Outlines

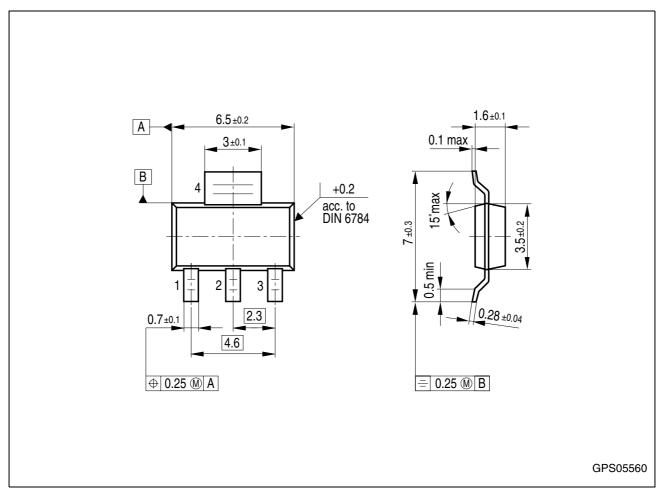


Figure 5 PG-SOT223-4 (Plastic Small Outline Transistor)

Green Product (RoHS compliant)

To meet the world-wide customer requirements for environmentally friendly products and to be compliant with government regulations the device is available as a green product. Green products are RoHS-Compliant (i.e Pb-free finish on leads and suitable for Pb-free soldering according to IPC/JEDEC J-STD-020).

You can find all of our packages, sorts of packing and others in our Infineon Internet Page "Products": http://www.infineon.com/products.

SMD = Surface Mounted Device

Dimensions in mm



Revision History

Version	Date	Changes
Rev. 2.5	2008-03-10	Simplified package name to PG-SOT223-4. No modification of released product.
Rev. 2.4	2007-03-20	Initial version of RoHS-compliant derivate of TLE 4266 Page 1: AEC certified statement added Page 1 and Page 12: RoHS compliance statement and Green product feature added Page 1 and Page 12: Package changed to RoHS compliant version Legal Disclaimer updated

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