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TOSHIBA Photocoupler GaAs IRED & Photo-Triac

TLP163J

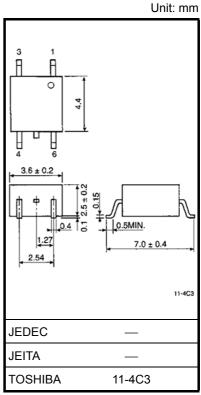
Triac Drive Programmable Controllers AC-Output Modules Solid State Relay

The TOSHIBA mini-flat coupler TLP163J is housed in a small outline package, suitable for surface-mount assembly.

The TLP163J consists of a gallium arsenide infrared emitting diode optically coupled to a photo-triac coupler.

The TLP163J features a greater capacity to withstand external noise than that of the TLP161J.

- Zero-voltage crossing turn-on •
- Peak off-state voltage: 600 V (min)
- Trigger LED current: 10 mA (max)
- On-state current: 70 mA (max)
- Isolation voltage: 2500 Vrms (min)
- UL recognized: UL1577, file No. E67349



Weight: 0.09 g (typ.)

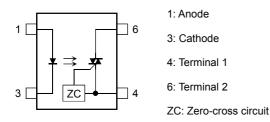
Trigger LED Current

	Trigger LED	Marking of Classification	
Classification (*)	$V_T = 3 V$,		
	Min	Max	
(IFT7)	—	7	Τ7
Standard	_	10	T7, Blank

*: e.g., (IFT7): TLP163J(IFT7)

Note: A part number for a certification test, use the standard part number, i.e. TLP163J(IFT7): TLP163J

Pin Configurations



Start of commercial production 2006/10

Absolute Maximum Ratings (Ta = 25°C)

Characteristics			Symbol	Rating	Unit		
	Forward current	lF	50	mA			
	Forward current de $(Ta \ge 53^{\circ}C)$	erating	∆I _F /°C	-0.7	mA/°C		
LED	D Peak forward current (100 μs pulse, 100 pps)				I _{FP}	1	А
	Reverse voltage		V _R	5	V		
	Junction temperatu	ire	Tj	125	°C		
	Off-state output ter	minal voltage	V _{DRM}	600	V		
	On-state RMS current	Ta = 25°C	I	70	mA		
		Ta = 70°C	I _{T(RMS)}	40	mA		
Detector	On-state current de (Ta ≥ 25°C)	erating	∆I _T /°C	-0.67	mA/°C		
	Peak on-state curre (100 μs pulse, 120		I _{TP}	2	А		
	Peak non-repetitive (P _W = 10 ms)	e surge current	I _{TSM}	1.2	А		
	Junction temperatu	ire	Tj	115	°C		
Storage temperature range			T _{stg} –55 to 125		°C		
Operating temperature range		T _{opr}	-40 to 100	°C			
Lead soldering temperature (10 s)		T _{sol}	260	°C			
Isolation voltage (AC, 1 minute, R.H. ≤ 60%) (Note 1)		BVS	2500	Vrms			

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Recommended Operating Conditions

Characteristics	Symbol	Min	Тур.	Max	Unit
Supply voltage	V _{AC}			240	Vac
Forward current	١ _F	15	20	25	mA
Peak on-state current	I _{TP}	_	_	1	А
Operating temperature	T _{opr}	-25	_	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Note 1: Device considered a two terminal device: Pins 1 and 3 shorted together and pins 4 and 6 shorted together.

Electrical Characteristics (Ta = 25°C)

	Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	VF	I _F = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	I _R	$V_R = 5 V$	_	_	10	μΑ
	Capacitance	CT	V = 0 V, f = 1 MHz	_	30	_	pF
	Peak off-state current	I _{DRM}	V _{DRM} = 600 V	_	10	1000	nA
	Peak on-state voltage	V _{TM}	I _{TM} = 70 mA	_	1.7	2.8	V
Detector	Holding current	Ι _Η		_	0.6	_	mA
Detector	Critical rate of rise of off-state voltage	dv/dt	V _{in} = 240 Vrms, Ta = 85°C (Figure 1)	200	500		V/µs
	Critical rate of rise of commutating voltage	dv/dt(c)	$V_{in} = 60$ Vrms, $I_T = 15$ mA (Figure 1)		0.2	_	V/µs

Coupled Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	I _{FT}	$V_T = 3 V$	_	_	10	mA
Inhibit voltage	VIH	I _F = Rated I _{FT}	_	_	20	V
Leakage in inhibited state	IIН	$I_F = Rated I_{FT}, V_T = Rated V_{DRM}$	_	200	600	μΑ
Turn-on time	ton	V_D = 3 → 1.5 V, R _L = 20 Ω, I _F = Rated I _{FT} x 1.5		30	100	μs
Impulse noise durability	V _N	t_N = 1 µs, snubber condition 120 Ω + 0.1 µF (Note 3)	_	2000	_	V

Isolation Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance (input to output)	CS	$V_{S} = 0 V, f = 1 MHz$		0.8	_	pF
Isolation resistance	R _S	$V_{S} = 500 \text{ V}, \text{ R.H.} \le 60\%$	1×10^{12}	10 ¹⁴	_	Ω
Isolation voltage		AC, 1 minute	2500	_	_	Vrms
		AC, 1 second, in oil	—	5000	_	VIIIS
		DC, 1 minute, in oil	—	5000	_	Vdc

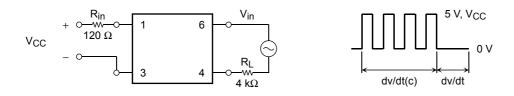
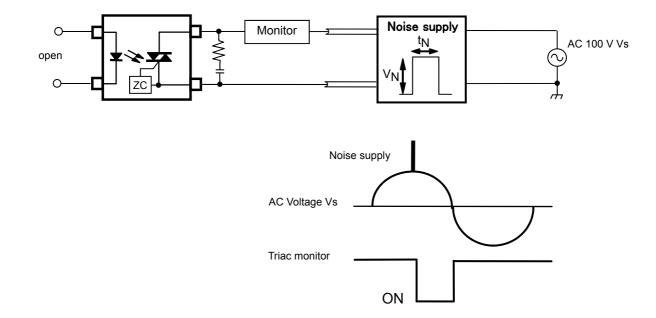


Figure 1 dv/dt Test Circuit

TOSHIBA



Note 3: impulse noise durability test circuit

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