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TOSHIBA Photocoupler GaAs Ired + Photo-Triac

TLP166J

Triac Drivers Programmable Controllers AC-Output Modules Solid State Relays

The TOSHIBA mini-flat coupler TLP166J is a small-outline coupler, suitable for surface-mount assembly.

The TLP166J consists of a gallium arsenide infrared emitting diode optically coupled to a triac-output photocoupler.

- Peak off-state voltage: 600 V (min)
- Trigger LED current: 10 mA (max)
- On-state current: 70 mA (max)
- Isolation voltage: 2500 Vrms (min)
- UL-approved: UL1577, file no. E67349
- Option(V4) type

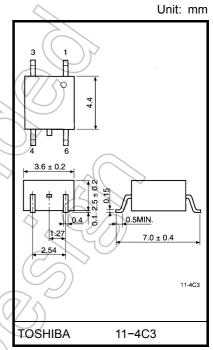
VDE approved: EN 60747-5-2 satisfied

Maximum operating insulation voltage:

Maximum permissible overvoltage:

565 Vpk

4000 Vpk



Weight: 0.09 g (typ.)

(Note): Please designate "Option (V4)", when an EN60747-5-2 approved type is needed. Please note that this product doesn't have 0.4mm insulator thickness when you apply for the safety standard.

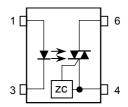
Trigger LED Current

Type (Note 1)	Trigger LED Current (mA) $V_{T}=3 \text{ V, Ta}=25^{\circ}\text{C}$ Min Max		Marking of Classification		
(IFT7)	-	7	T7		
None	-) 10	T7, blank		

e.g., IFT7: TLP166J(IFT7)

(Note 1): When applying for safety standard certification, use the standard part number. For example, TLP166J(IFT7): TLP166J

Pin Configurations



- Anode 1.
- Cathode
- Terminal 1
- Terminal 2

Absolute Maximum Ratings (Ta = 25°C)

Characteristic			Symbol	Rating	Unit	
	Forward current		lF	50	mA	
	Forward current derating (Ta ≥	ΔI _F / °C	-0.7	mA / °C		
LED	Peak forward current (100µs pu	I _{FP}	1	A		
	Reverse voltage	V _R	5	V (
	Junction temperature	Tj	125	°C		
	Off-state output terminal voltag	V_{DRM}	600	(X)		
	On-state RMS Current	Ta=25°C	IT(D) (O)	70	mA	
_		Ta=70°C	l _{T(RMS)}	40	IIIA	
Detector	On–state current derating(Ta ≥	ΔI _T / °C	-0.67	mA / °C		
	Peak on-state current (100µs p	I _{TP}	2	A		
	Peak non-repetitive surge curre (P _W =10ms)	I _{TSM}	1.2	A		
	Junction temperature	Tj	(115)	°C,		
Storage temperature range			T _{stg}	-55 to 125	°C <	
Operating temperature range			Topr	-40 to 100	Ç	
Lead soldering temperature (10s)			T _{sol}	260	ů	
Isolation voltage (AC, 1minute, R.H.≤ 60%) (Note 2)			BVs	2500	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 2): Device considered a two-terminal device: Pins 1 and 3 shorted together and Pin 4 and 6 shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	VAC	_	_	240	Vac
Forward current	l _F	15	20	25	mA
Peak on-state current	I _{TP}	_	_	1	Α
Operating temperature	T _{opr}	-25	_	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

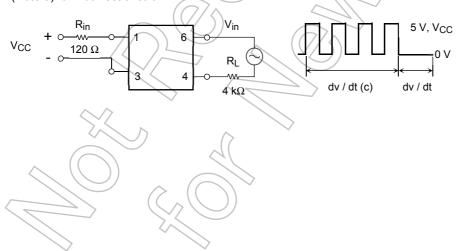
Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	V _F	I _F = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	I _R	V _R = 5 V	_	_	10	μA
	Capacitance	C _T	V = 0, f = 1 MHz	7	30	_	pF
Detector	Peak off-state current	I _{DRM}	V _{DRM} = 600 V		30	1000	nA
	Peak on-state voltage	V_{TM}	I _{TM} = 70 mA	7))1.7	2.8	V
	Holding current	lΗ	() 	0.6	_	mA
	Critical rate of rise of off–state voltage	dv / dt	V _{in} = 240 Vrms, Ta = 85°C (Note 3)	200	500	_	V / µs
	Critical rate of rise of commutating voltage	dv / dt(c)	I _T = 15 mA, V _{in} = 60 Vrms (Note 3)	· _	0.2	_	V / µs

Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур	Max	Unit
Trigger LED current	l _{FT}	V _T =3 V			10	mA
Inhibit voltage	V _{IH}	I _F = rated I _F T	(-)	<u> </u>	50	V
Leakage in inhibited state	I _{IH}	IF = rated IFT VT = rated V _{DRM}		-	600	μA
Capacitance input to output	Cs	V _S = 0, f = 1 MHz	2) –	0.8	1	pF
Isolation resistance	R _S	V _S = 500 V, R,H,≤ 60%	1×10 ¹²	10 ¹⁴	_	Ω
		AC, 1 minute	2500	_	_	Vrms
Isolation voltage	BVS	AC, 1 second, in oil	_	5000	_	VIIIIS
		DC, 1 minute, in oil	_	5000	_	Vdc

(Note 3): dv / dt Test circuit



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