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TOSHIBA Photocoupler GaAs IRed & Photo-Thyristor

## TLP748J

Office Machine Household Use Equipment Solid State Relay Switching Power Supply

The TOSHIBA TLP748J consists of a photo-thyristor optically coupled to a gallium arsenide infrared emitting diode in a six lead plastic DIP package.

- Peak OFF-state voltage: 600 V (min)
- Trigger LED current: 10 mA (max)
- ON-state current: 150 mA (max)
- UL recognized: UL1577, file no. E67349
- BSI approved: BS EN60065:2002,

BS EN60950-1:2006

• SEMKO approved: EN60065:2002,

EN60950-1:2006

- Isolation voltage: 4000 Vrms (min)
- Option (D4) type

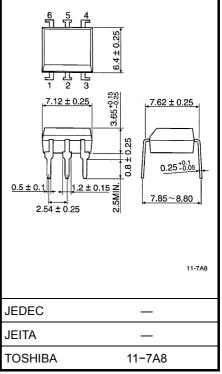
VDE approved: DIN EN 60747-5-2

Maximum operating insulation voltage: 890  $V_{pk}$  Highest permissible over voltage: 8000  $V_{pk}$ 

# (Note) When a EN 60747-5-2 approved type is needed, please designate the "option (D4)"

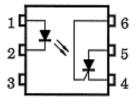
		7.62 mm pitch	10.16 mm pitch			
		standard type	TLPxxxxF type			
•	Creepage distance:	7.0 mm (min)	8.0 mm (min)			
	Clearance:	7.0 mm (min)	8.0 mm (min)			
	Insulation thickness:	0.4 mm (min)	0.4 mm (min)			

Unit: mm



Weight: 0.42 g (typ.)

#### Pin Configuration (top view)



1 : ANODE

2 : CATHODE

3: N.C.

4 : CATHODE

5: ANODE

6: GATE

#### **Absolute Maximum Ratings (Ta = 25°C)**

	Characteristic	Symbol	Rating	Unit	
	Forward current	lF	50	mA	
LED	Forward current derating (Ta ≥ 53 °C)	ΔI <sub>F</sub> / °C	-0.7	mA / °C	
쁘	Peak forward current (100 μs pulse, 100 pps)	I <sub>FP</sub>	1	Α	
	Reverse voltage	V <sub>R</sub>	5	V	
	Peak forward voltage ( $R_{GK}$ = 27 $k\Omega$ )	$V_{DRM}$	600	V	
	Peak reverse voltage (R <sub>GK</sub> = 27 kΩ)	$V_{RRM}$	600	V	
ō	ON-state current	I <sub>T(RMS)</sub>	150	mA	
Detector	ON-state current derating (Ta ≥ 25°C)	ΔI <sub>T</sub> / °C	-2.0	mA / °C	
ă	Peak ON-state current (100 μs pulse, 120 pps)	I <sub>TP</sub>	3	Α	
	Peak one cycle surge current	I <sub>TSM</sub>	2	Α	
	Peak reverse gate voltage	$V_{GM}$	5	V	
Storag	e temperature range	T <sub>stg</sub> –55 to 125		°C	
Operat	ing temperature range	T <sub>opr</sub>	-40 to 100	°C	
Lead s	oldering temperature (10 s)	T <sub>sol</sub>	260	°C	
Isolatio	on voltage (AC, 1 minute, R.H.≤ 60%) (Note 1)	BVS	4000	V <sub>rms</sub>	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device Considered a two terminal device: pins 1, 2 and 3 shorted together and pins 4, 5 and 6 shorted together.

#### **Recommended Operating Conditions**

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	V <sub>AC</sub>	_	_	240	V <sub>ac</sub>
Forward current	lF	15	_	25	mA
Operating temperature	T <sub>opr</sub>	-25	_	85	°C
Gate to cathode resistance	R <sub>GK</sub>	_	10	27	kΩ
Gate to cathode capacity	C <sub>GK</sub>	_	0.01	0.1	μF

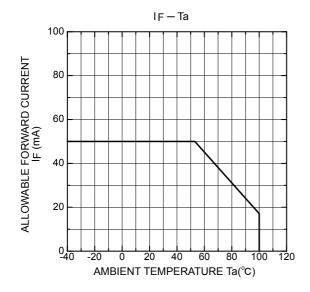
Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

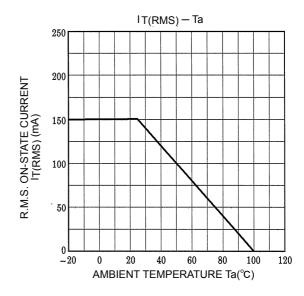
## Individual Electrical Characteristics (Ta = 25°C)

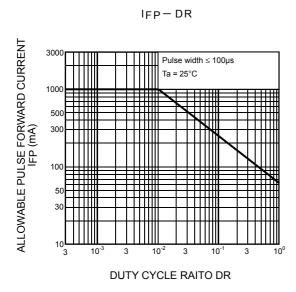
Characteristic		Symbol	Test Condition		Min	Тур.	Max	Unit
	Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 10 mA		1.0	1.15	1.3	V
LED	Reverse current	I <sub>R</sub>	V <sub>R</sub> = 5 V		_	_	10	μΑ
	Capacitance	C <sub>T</sub>	V = 0 V, f = 1 MHz		_	30	_	pF
	OFF-state current	I <sub>DRM</sub>	V <sub>AK</sub> = 600 V, R <sub>GK</sub> = 27 kΩ		_	_	5	μΑ
	Reverse current	I <sub>RRM</sub>	$V_{KA} = 600 \text{ V}, R_{GK} = 27 \text{ k}\Omega$		I	_	5	μΑ
ō	ON-state voltage	V <sub>TM</sub>	$I_{TM}$ = 100 mA $R_{GK}$ = 27 k $\Omega$		_	_	1.45	V
Detector	Holding current	lΗ			_	_	1	mA
۵	OFF-state dv / dt	dv / dt	V <sub>AK</sub> = 420 V, R <sub>GK</sub> = 27 kΩ		5	_	_	V/μs
	Oit	0.	V = 0 V,	Anode to gate		5	_	~F
	Capacitance	pacitance $C_j$ $f = 1 \text{ MHz}$	f = 1 MHz	Gate to cathode		500		pF

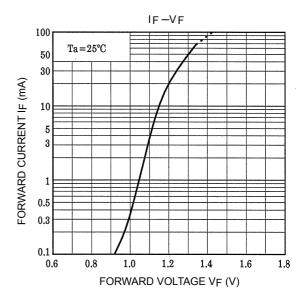
### Coupled Characteristics (Ta = 25°C)

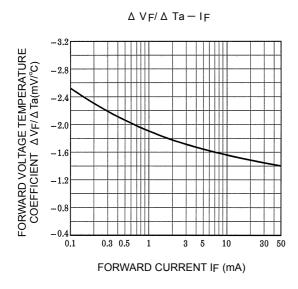
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit	
Trigger LED current	I <sub>FT</sub>	$V_{AK}$ = 6 V, $R_{GK}$ = 27 k $\Omega$	_	_	10	mA	
Turn-on time	t <sub>ON</sub>	$I_F$ = 30 mA, $V_{AA}$ = 50 V R <sub>GK</sub> = 27 k $\Omega$	_	15	1	μs	
Capacitance (input to output)	Cs	V <sub>S</sub> = 0 V, f = 1 MHz	_	0.8	1	pF	
Isolation resistance	R <sub>S</sub>	V <sub>S</sub> = 500 V, R.H.≤ 60%	1×10 <sup>12</sup>	10 <sup>14</sup>	_	Ω	
	BVS	AC, 1 minute	4000	_	_	V <sub>rms</sub>	
Isolation voltage		AC, 1 second, in oil	_	10000	_		
		DC, 1 minute, in oil	_	10000	1	V <sub>dc</sub>	

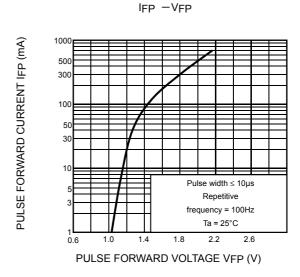




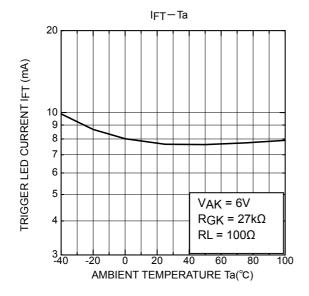


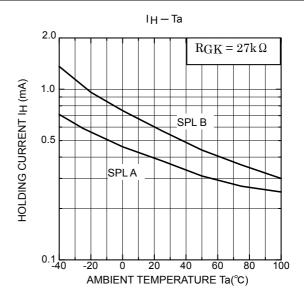


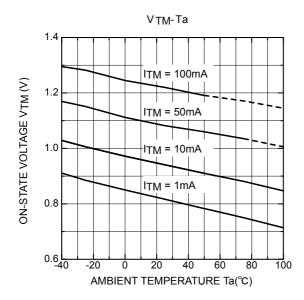




<sup>\*:</sup> The above graphs show typical characteristics.







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