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Multi-rate Gigabit & Fast Ethernet Transceivers Bi-Directional SFP 1000BASE 10KM LC Receptacle



			Spec Out	tline		
		C	PLINK	Customer	Notes	
Spec. Num	ber		S0489	NA	-	
Spec. Revis	sion		01	NA	-	
		TRBUG	1CFBI000E2G	NA		
		TRBUG1CBBI000E2G		NA		
		TRBUG1CUBI000E2G		NA		
Part Numb	ber	TRBUG1CDBI000E2G		NA	-	
		TRBUG1CFBC000E2G		NA		
		TRBUG1CBBC000E2G		NA		
		TRBUG1CUBC000E2G		NA		
		TRBUG1CDBC000E2G		NA		
Initiate Da	te	Feb 27 th , 2015		NA	-	
			Document I	History		
Date		Status	Author	Remarks		
2015/2/27	V	ersion 01	Bin Xiao	Preliminary Specifications		

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Features

- ☑ Lead Free Design & RoHS Compliant
- ☑ Compatible with SFP MSA
- ☑ Compliant with IEEE 802.3ah Gigabit Ethernet Standard
- ☑ 1310nm, 1490nm and 1550nm Wavelengths
- $\ensuremath{\boxtimes}$ Distances up to 10km over Single Mode Fiber
- ☑ Digital Diagnostics through Serial Interface
- ☑ Eye Safe (Class 1 Laser Safety)
- ☑ Simplex LC Optical Interface
- \square -40°C to +85°C Operating Temperature option
- ☑ Hot-pluggable
- ☑ TX Fault & Loss of Signal Outputs
- ☑ TX Disable Input
- ☑ Single +3.3V Power Supply
- Option of Digital Diagnostics function



Description

The TRBUG1CxBx000E2G modules are single fiber, bi-directional SFP transceivers that provide a quick and reliable interface for 1000BASE-BX-D/U Gigabit Ethernet applications.

Three types of modules are available: the 1310nm FP laser-based transceiver (BX-U), the 1490nm DFB laser-based transceiver (BX-D) and the 1550nm DFB laser-based transceiver (BX-D). The transceivers possess option to be integrated with digital diagnostics monitoring, which provides features to detect a problem before system performance is impacted. The diagnostic functions, alarms and warning features are provided via an I2C serial interface as described per the Multi-Source Agreement (MSA) document, SFF-8472 (Rev. 9.4).

All modules meet Class 1 Laser Safety requirements in accordance with the U.S. and international standards as described in the FDA/CDRH and IEC-60825 documents, respectively. The TRBUG1CxBx000E2G transceivers connect to standard 20-pad SFP connectors for hot plug capability. This allows the system designer to make configuration or maintenance changes by simply plugging in different types of transceivers without removing the power supply from the host system.

The transceivers have color-coded latches that identify the TX wavelength. The MSA compliant latch offers an easy and convenient way to release the module.

The transmitter and receiver DATA interfaces are ACcoupled internally. LV-TTL Transmitter Disable control input and Loss of Signal output interfaces are also provided.

The transceivers operate from a single +3.3V power supply over an operating case temperature range of -40°C to +85°C. The package is made of metal for EMI immunity.

Parameter Symbol Min. Max. Units °C Storage Temperature T_{stq} -40 +85 Commercial -5 +70 °C Operating Case Temperature ¹ TOP Industrial -40 +85 Supply Voltage +4.5٧ Vcc 0 Input Voltage 0 V_{CC} V VIN Measured on top side of SFP module at the front center vent hole of the cage,

Absolute Maximum Ratings

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Transmitter Performance Characteristics (Over Operating Case Temperature, Vcc= 3.13 to 3.47V)

Parameter			Minimum	Typical	Maximum	Units	
Operating Data Rate			125	-	1250	Mb/s	
Optical Output Power ¹			-9	-	-3	dBm	
BX10-U	1310nm FP		1260	1310	1360		
BX10-D	1490nm DFB	λ_c	1480	1490	1500	nm	
BX10-D	1550nm DFB		1530	1550	1570		
BX10–U	1281 - 1360nm	Δλ <i>RMSc</i>	-	-	2.58	3	
BX10-D	1480 - 1500nm	Δλ20	-	-	1	nm	
	1530- 1570nm		-	-	1		
		P _{hi} /P _{lo}	6	-	-	dB	
ower		-	-	-	- 45	dBm	
		RIN ₁₂ OMA	-	-	- 113	dB/Hz	
ance		ORLT	-	-	12	dB	
Transmitter Reflectance			-	-	-12	dB	
Total Jitter			-	-	227	ps	
Transmitter Output Eye			Compliant with Eye Mask Defined in IEEE 802.3-2005 Standard				
	BX10-D BX10-D BX10-U BX10-D bwer	BX10-D 1490nm DFB BX10-D 1550nm DFB BX10-U 1281 - 1360nm BX10-D 1480 - 1500nm BX10-D 1530- 1570nm	$ \begin{array}{ c c c c c } \hline BX10-D & 1490nm DFB & $$$$$$$$$$$$$$$$$$$$$$$$$$$$$BX10-D & 1550nm DFB & $$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$$	$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	

Receiver Performance Characteristics (Over Operating Case Temperature, Vcc = 3.13 to 3.47V)

All parameters guaranteed only at typical data rate

Parameter			Symbol	Minimum	Typical	Maximum	Units
Operating Data Rate			В	125	-	1250	Mb/s
Minimum Input Optical Power (10 ⁻¹² BER) ¹			P _{min}	- 20.0	-	-	dBm
Maximum Input Optical Power (10 ⁻¹² BER) ¹			P _{max}	-3.0	-	-	dBm
LOS Thresholds	Increasing Light Input		Pl _{os+}	-	-	- 20.0	dBm
LOS TITESTOIDS	Decreasing Light Input		Plos-	- 35.0	-	-	
LOS Hysteresis ¹			-	0.5	-	-	dB
Total Jitter			T_J	-	-	266	ps
	BX10-U	1490nm		1480	-	1500	
Wavelength of Operation		1550nm	λ	1530	-	1570	nm
	BX10-D 1310nm			1260	-	1360	
Receiver Reflectance			-	-	-	- 12	dB
¹ Measured at 1250Mb/s with 2	- & 1550nm wavel	- enaths.	-	- 12	u		

Laser Safety:

All transceivers are Class 1 Laser products per FDA/CDRH and IEC-60825 standards. They must be operated under specified operating conditions.



Oplink Communications, LLC.

This product complies with 21 CFR 1040.10 and 1040.11 Meets Class 1 Laser Safety Requirements

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Transmitter Electrical Interface (over Operating Case Temperature, VCC = 3.13 to 3.47V)

Parameter	Symbol	Minimum	Typical	Maximum	Units
Input Voltage Swing (TD+ & TD-)1	V _{PP-DIF}	0.5	-	2.4	V
Input HIGH Voltage (TX Disable) ²	ViH	2.0	-	Vcc	V
Input LOW Voltage (TX Disable) ²	V _{IL}	0	-	0.8	V
Output HIGH Voltage (TX Fault) ³	Vон	2.0	-	<i>Vcc</i> + 0.3	V
Output LOW Voltage (TX Fault) ³	V _{OL}	0	-	0.8	V
¹ Differential peak-to-peak voltage.					
² There is an internal 4.7 to $10k\Omega$ pull-up resistor to <i>Vcc.</i>					
³ Open collector compatible, 4.7 to $10k\Omega$ pull-up resistor to <i>Vcc</i> .					

Receiver Electrical Interface (over Operating Case Temperature, VCC = 3.13 to 3.47V)

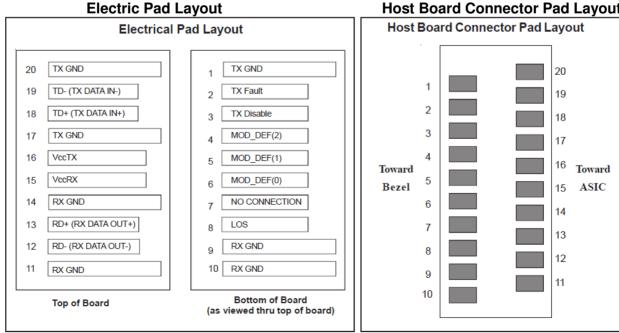
Parameter	Symbol	Minimum	Typical	Maximum	Units		
Output Voltage Swing (RD+ & RD) ¹	V _{PP-DIF}	0.6	-	2	V		
Output HIGH Voltage (LOS) ²	V _{OH}	2.0	-	Vcc + 0.3	V		
Output LOW Voltage (LOS) ²	Vol	0	-	0.5	V		
¹ Differential peak-to-peak voltage across external 100Ω load. ² Open collector compatible, 4.7 to 10kΩ pull-up resistor to <i>Vcc</i>							

Electrical Power Supply Characteristics (over Operating Case Temperature, VCC = 3.13 to 3.47V)

Parameter	Symbol	Minimum	Typical	Maximum	Units
Supply Voltage	Vcc	3.13	3.3	3.47	V
Supply Current	lcc	-	-	300	mA

Module Definition

MOD_DEF(0)	MOD_DEF(1)	MOD_DEF(2)	Interpretation by Host	
pin 6	pin 5	pin 4		
TTL LOW	SCL	SDA	Serial module definition protocol	



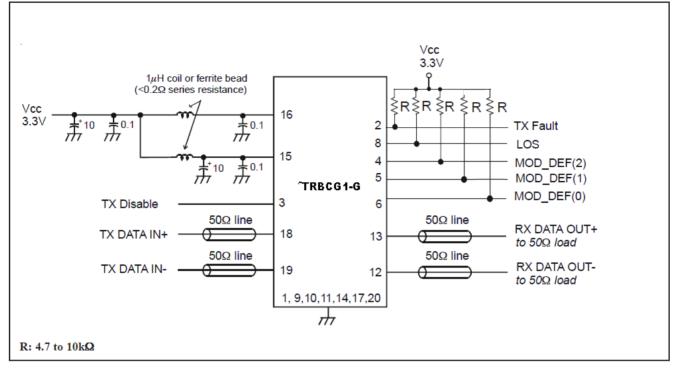
Host Board Connector Pad Layout

46335 Landing Pkwy • Fremont CA 94538-6407 • Tel: 510-933-7200 • Fax: 510-933-7300 • Email: info@oplink.com • Web: www.oplink.com Page 4 of 6

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Example of SFP host board schematic



Application Notes

Electrical interface: All signal interfaces are compliant with the SFP MSA specification. The high speed DATA interface is differential AC-coupled internally with 1µF and can be directly connected to a 3.3V SERDES IC. All low speed control and sense output signals are open collector TTL compatible and should be pulled up with a 4.7 - 10k Ω resistor on the host board.

Loss of Signal (LOS): The Loss of Signal circuit monitors the level of the incoming optical signal and generates logic HIGH when an insufficient photocurrent is produced.

TX Fault: The output indicates LOW when the transmitter is operating normally and HIGH with a laser fault including laser end-of-life. TX Fault is an open collector/drain output and should be pulled up with a 4.7 - $10k\Omega$ resistor on the host board. TX Fault is non-latching (automatically deasserts when fault goes away).

TX Disable: When the TX Disable pin is at logic HIGH, the transmitter optical output is disabled (less than -45dBm).

Serial Identification: The module definition of SFP is indicated by the three module definition pins, MOD_DEF(0), MOD_DEF(1) and MOD_DEF(2). Upon power up, MOD_DEF(1:2) appear as NC (no connection), and MOD_DEF(0) is TTL LOW. When the host system detects this condition, it activates the serial protocol (standard two-wire I2C serial interface) and generates the serial clock signal (SCL). The negative edge clocks data from the SFP EEPROM.

The serial data signal (SDA) is for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation.

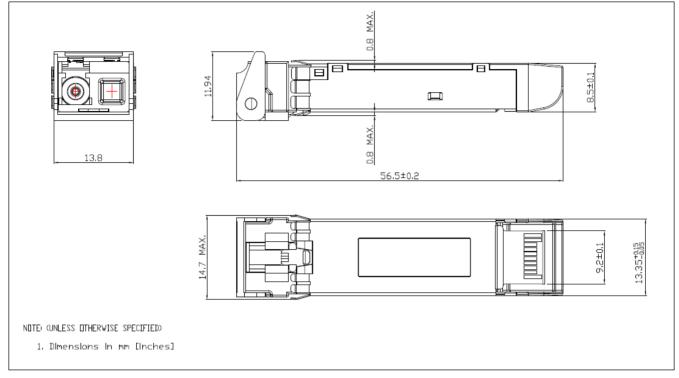
The data transfer protocol and the details of the mandatory and vendor specific data structures are defined in the SFP MSA. EEPROM ID is per SFF-8472, Rev. 9.4.

Power supply and grounding: The power supply line should be well-filtered. All $0.1\mu F$ power supply bypass capacitors should be as close to the transceiver module as possible.

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Package Outline



Ordering information

Part Number	DDM	Operating Temperature range	Latch	Nominal Wavelength		Optical Link Power	Distance ¹
Part Number			Color	Тx	Rx	Budget	Distance
TRBUG1CFBI000E2G	YES	-40~85 °C	Blue	1310nm	1490nm	11dB	10km
TRBUG1CBBI000E2G	YES	-40~85 °C	Violet	1490nm	1310nm	11dB	10km
TRBUG1CUBI000E2G	YES	-40~85 °C	Blue	1310nm	1550nm	11dB	10km
TRBUG1CDBI000E2G	YES	-40~85 °C	Yellow	1550nm	1310nm	11dB	10km
TRBUG1CFBC000E2G	YES	-5~70 °C	Blue	1310nm	1490nm	11dB	10km
TRBUG1CBBC000E2G	YES	-5~70 °C	Violet	1490nm	1310nm	11dB	10km
TRBUG1CUBC000E2G	YES	-5~70 °C	Blue	1310nm	1550nm	11dB	10km
TRBUG1CDBC000E2G	YES	-5~70 °C	Yellow	1550nm	1310nm	11dB	10km

¹ The indicated transmission distance is for guidelines only, not guaranteed. The exact distance is dependent on the fiber loss, connector and splice loss, and allocated system penalty. Longer distances can be supported if the optical link power budget is satisfied.

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