



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts,Customers Priority,Honest Operation,and Considerate Service",our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



## Contact us

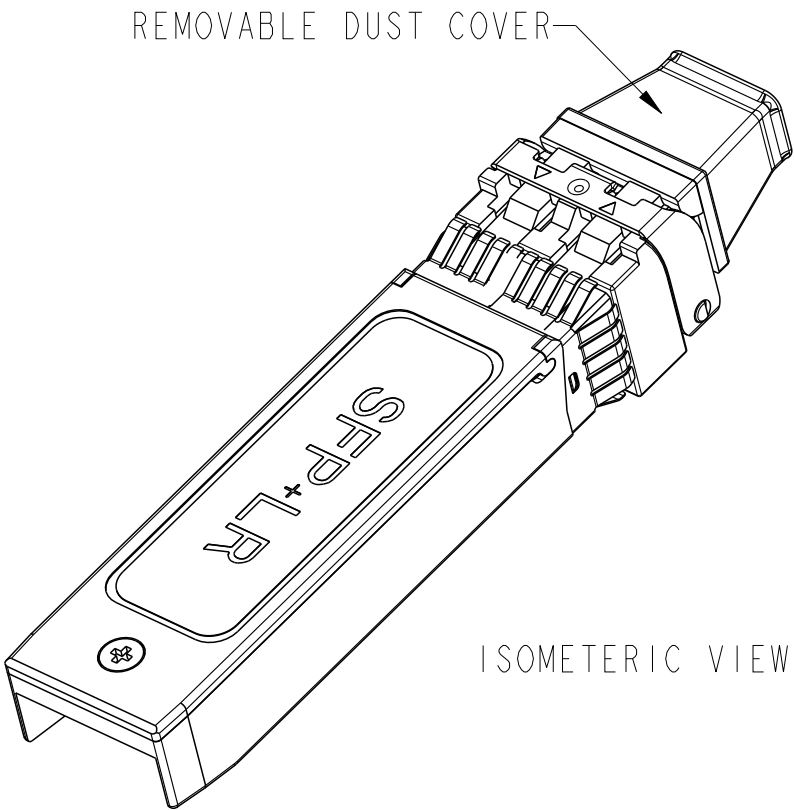
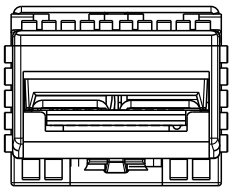
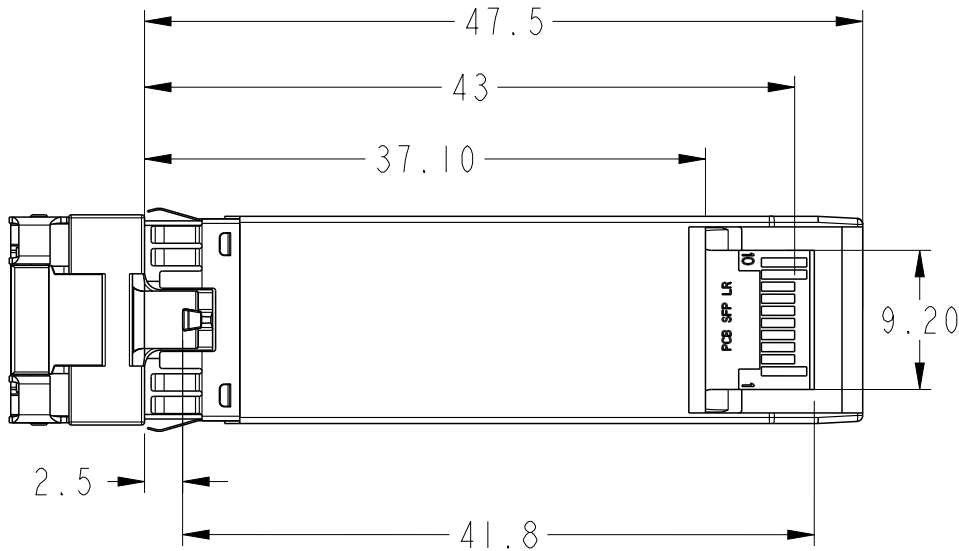
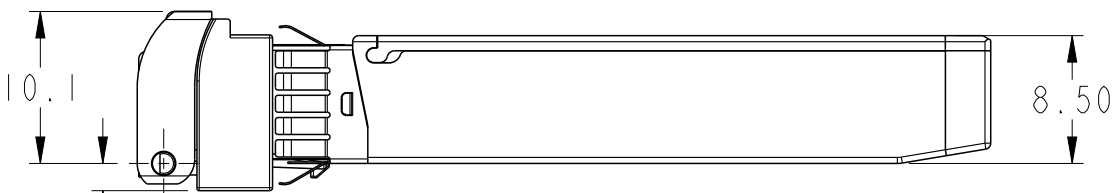
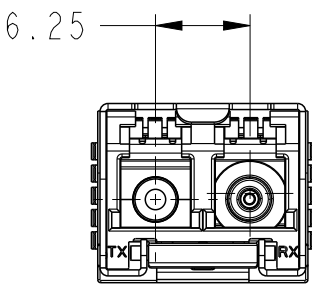
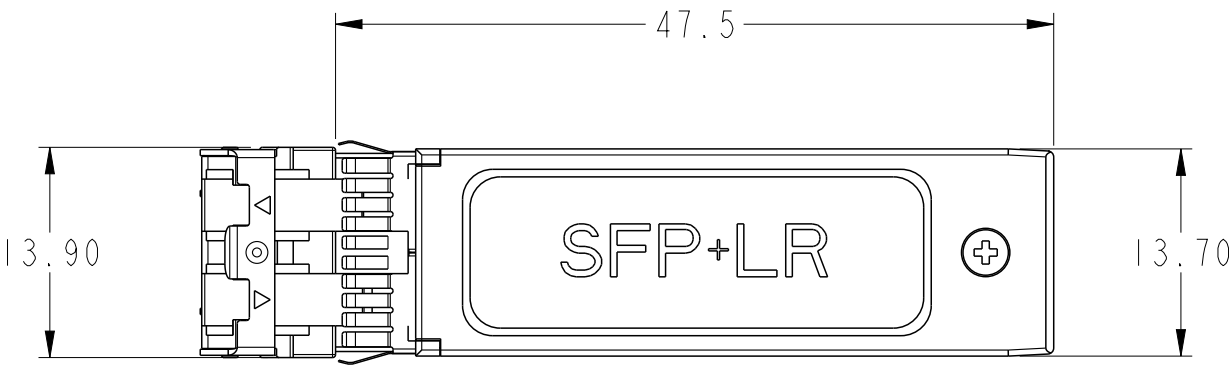
Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: [info@chipsmall.com](mailto:info@chipsmall.com) Web: [www.chipsmall.com](http://www.chipsmall.com)

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



PRODUCT NUMBER  
TRX10GDP0310CI



- NOTES:
1. REFERENCE PRODUCT SPECIFICATION GS-12-0974 FOR DETAILED TRANSCEIVER SPECIFICATIONS.
  2. TRANSCEIVER ASSEMBLY IS RoHS 6/6 COMPLIANT.
  3. TRANSCEIVER ASSEMBLY IS PACKAGED IN BLISTER PACKAGING.
  4. DIMENSIONS ARE FOR REFERENCE ONLY.

spec ref	-	dr	David Lord	2011/07/11	projection	MM	size	A3	scale	2:1
tolerance std	-	eng	Deborah Ingram	2011/12/02			ecn no	ELX-V-008044-1	rel level	Released
ASME Y14.5	TOLERANCES UNLESS OTHERWISE SPECIFIED	chr	-	-						
		appr	John Spickler	2011/12/20						
surface	linear	0.X	±-		SFP+ LR ASSEMBLY	cat. no.	Product - Customer Drw	sheet 1 of 1	rev	B
		0.XX	±-							
		0.XXX	±-							
ASME Y14.5	angular	0°	±-°	www.fci.com	10GBd 1310nm LIMITING TRANSCEIVER					