



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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High Voltage Fast-Switching NPN Power Transistor

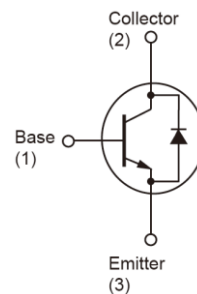
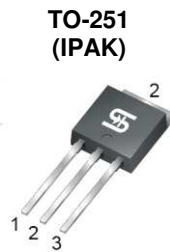
FEATURES

- High Voltage Capability
- Fast Switching Speed
- Pb-free plating
- RoHS compliant
- Halogen-free mold compound

APPLICATION

- Electronic Ballast
- Switch mode power supply

KEY PERFORMANCE PARAMETERS			
PARAMETER		VALUE	UNIT
BV _{CEO}		450	V
BV _{CBO}		1050	V
I _C		2.5	A
V _{CE(SAT)}	I _C =0.7A, I _B =0.14A	0.5	V



Notes: Moisture sensitivity level: level 3. Per J-STD-020

ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT
Collector-Base Voltage	V _{CBO}	1050	V
Collector-Emitter Voltage @ V _{BE} =0V	V _{CES}	450	V
Emitter-Base Voltage	V _{EBO}	15	V
Collector Current	I _C	2.5	A
Collector Peak Current (tp <5ms)	I _{CM}	4	A
Base Current	I _B	1.5	A
Base Peak Current (tp <5ms)	I _{BM}	3	A
Power Total Dissipation @ T _A =25°C	P _{DTOT}	30	W
Maximum Operating Junction Temperature	T _J	+150	°C
Storage Temperature Range	T _{STG}	-55 to +150	°C

THERMAL PERFORMANCE			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction to Case Thermal Resistance	R _{θJC}	4.17	°C/W
Junction to Ambient Thermal Resistance	R _{θJA}	100	°C/W

ELECTRICAL SPECIFICATIONS ($T_A = 25^\circ\text{C}$ unless otherwise noted)						
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
Collector-Base Voltage	$I_C = 0.5\text{mA}$	BV_{CBO}	1050	--	--	V
Collector-Emitter Breakdown Voltage	$I_C = 5\text{mA}$	BV_{CEO}	450	--	--	V
Emitter-Base Breakdown Voltage	$I_E = 1\text{mA}$	BV_{EBO}	15	--	--	V
Collector Cutoff Current	$V_{CE} = 400\text{V}, I_B = 0$	I_{CEO}	--	10	250	μA
Collector Cutoff Current	$V_{CB} = 950\text{V}, I_E = 0$	I_{CBO}	--	--	10	μA
Collector-Emitter Saturation Voltage	$I_C = 0.7\text{A}, I_B = 0.14\text{A}$	$V_{CE(SAT)1}$	---	--	0.5	V
Collector-Emitter Saturation Voltage	$I_C = 2\text{A}, I_B = 0.6\text{A}$	$V_{CE(SAT)2}$	---	1.5	3.0	V
Base-Emitter Saturation Voltage	$I_C = 2\text{A}, I_B = 0.6\text{A}$	$V_{BE(SAT)1}$	--	1.0	1.6	V
DC Current Gain	$V_{CE} = 5\text{V}, I_C = 0.1\text{A}$	h_{FE1}	50	70	100	
	$V_{CE} = 3\text{V}, I_C = 0.5\text{A}$	h_{FE2}	18	23	50	
Diode Forward Voltage	$I_C = 1\text{A}$	V_F	--	--	1.5	V
Rise Time ^(Note 2)	$V_{CC} = 5\text{V}, I_C = 0.5\text{A}$	t_r	--	--	1	μs
Storage Time ^(Note 2)		t_{STG}	2.5	3	3.5	μs
Fall Time ^(Note 2)		t_f	--	--	1.2	μs
Repetitive Avalanche Energy	$L = 2\text{mH}$	E_{AR}	5	--	--	mJ

Notes:

1. Pulse test: $\leq 380\mu\text{s}$, duty cycle $\leq 2\%$
2. For DESIGN AID ONLY, not subject to production testing.

ORDERING INFORMATION

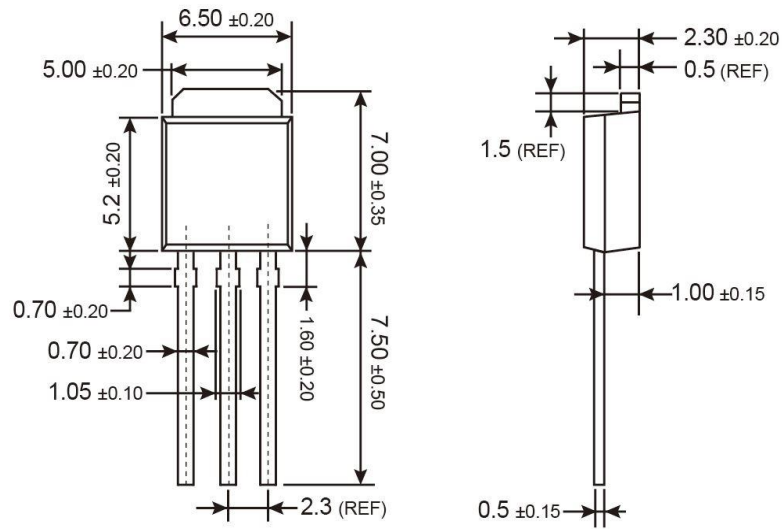
PART NO.	PACKAGE	PACKING
TSC5802DCH C5G	TO-251	75pcs / Tube
TSC5802DCP ROG	TO-252	2,500pcs / 13" Reel

Note:

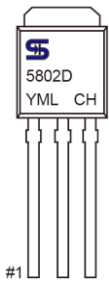
1. Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
2. Halogen-free according to IEC 61249-2-21 definition

PACKAGE OUTLINE DIMENSIONS (Unit: Millimeters)

TO-251 (IPAK)

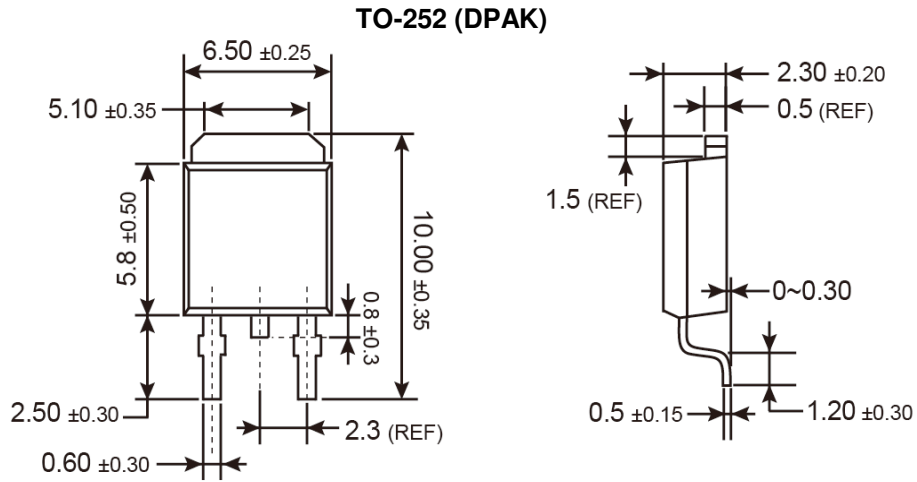


Marking Diagram

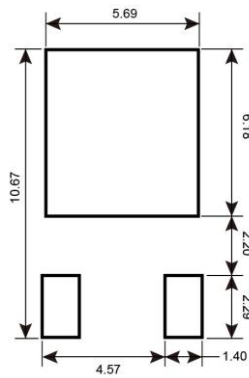


- Y** = Year Code
- M** = Month Code for Halogen Free Product
 - O** =Jan **P** =Feb **Q** =Mar **R** =Apr
 - S** =May **T** =Jun **U** =Jul **V** =Aug
 - W** =Sep **X** =Oct **Y** =Nov **Z** =Dec
- L** = Lot Code (1~9, A~Z)

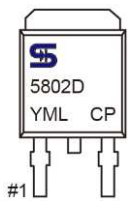
PACKAGE OUTLINE DIMENSIONS (Unit: Millimeters)



SUGGESTED PAD LAYOUT



MARKING DIAGRAM



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