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30V N-Channel Power MOSFET



SOP-8

Pin Definition:

Source
 Source
 Source
 Drain
 Gate
 Drain
 Drain

Key Parameter Performance

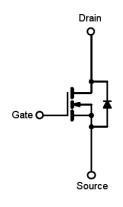
Parameter		Value	Unit	
$V_{ t DS}$		30	V	
R _{DS(on)} (max)	V _{GS} = 10V	4.2	mΩ	
	$V_{GS} = 4.5V$	6		
Q_g		24	nC	

Ordering Information

Part No.	Package	Packing		
TSM042N03CS RLG	SOP-8	2.5kps / 13" Reel		

Note: "G" denotes for Halogen- and Antimony-free as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds

Block Diagram



N-Channel MOSFET

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	30	V	
Gate-Source Voltage		V_{GS}	±20	V	
Continuous Drain Current	Tc=25ºC		30	Α	
	Tc=100°C	- I _D	19	Α	
Pulsed Drain Current (Note 1)		I _{DM}	120	Α	
Single Pulse Avalanche Energy (Note 2)		E _{AS}	125	mJ	
Single Pulse Avalanche Current (Note 2)		I _{AS}	50	Α	
Power Dissipation @ T _C = 25°C		P _D	7	W	
Operating Junction Temperature		TJ	175	∘C	
Storage Temperature Range		T _{STG}	-55 to +175	°C	

Thermal Performance

Parameter	Symbol	Limit	t Unit	
Thermal Resistance - Junction to Ambient	R _{eJA}	62	°C/W	



30V N-Channel Power MOSFET



Electrical Specifications (T_J=25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Тур	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	BV _{DSS}	30			V
Drain-Source On-State Resistance	$V_{GS} = 10V, I_D = 12A$	R _{DS(ON)}		3.8	4.2	mΩ
	$V_{GS} = 4.5V, I_D = 6A$			5.2	6	
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	$V_{GS(TH)}$	1.2	1.6	2.5	V
Zero Gate Voltage Drain Current	$V_{DS} = 30V, V_{GS} = 0V$				1	μА
	V _{DS} = 24V, T _J = 125 ^o C	I_{DSS}			10	
Gate Body Leakage	$V_{GS}=\pm 20V,V_{DS}=0V$	I _{GSS}			±100	nA
Forward Transconductance (Note 3)	$V_{DS} = 10V, I_{D} = 6A$	g fs		12		S
Dynamic						
Total Gate Charge (Note 3,4)		Q_g		24		
Gate-Source Charge (Note 3,4)	$V_{DS} = 15V, I_{D} = 12A,$	Q_{gs}		4.2		nC
Gate-Drain Charge (Note 3,4)	$V_{GS} = 4.5V$	Q_{gd}		13		
Input Capacitance		C _{iss}		2200		pF
Output Capacitance	$V_{DS} = 25V, V_{GS} = 0V,$ f = 1.0MHz	C _{oss}		280		
Reverse Transfer Capacitance		C_{rss}		177		
Switching					•	
Turn-On Delay Time (Note 3,4)		$t_{d(on)}$		12.6		
Turn-On Rise Time (Note 3,4)	$V_{DD} = 15V, I_D = 15A,$	t _r		19.5		
Turn-Off Delay Time (Note 3,4)	$V_{GS} = 10V, R_{GEN} = 3.3\Omega$	t _{d(off)}		42.8		ns
Turn-Off Fall Time (Note 3,4)		t _f		13.2		
Source-Drain Diode Ratings and Ch	aracteristic					
Maximum Continuous Drain-Source Diode Forward Current	Integral reverse diode in the MOSFET	Is			30	Α
Maximum Pulse Drain-Source Diode Forward Current		I _{SM}			120	Α
Diode Forward Voltage	$V_{GS} = 0V, I_{S} = 1A$	V _{SD}			1	V

Note:

- 1. Pulse width limited by safe operating area
- 2. L=0.1mH, I_{AS} =50A, V_{DD} = 25V, R_{G} = 25 Ω , Starting T_{J} = 25 $^{\circ}$ C
- 3. Pulse test: pulse width ≤300µs, duty cycle ≤2%
- 4. Switching time is essentially independent of operating temperature.

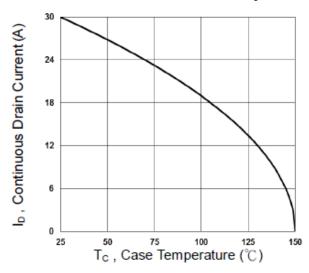


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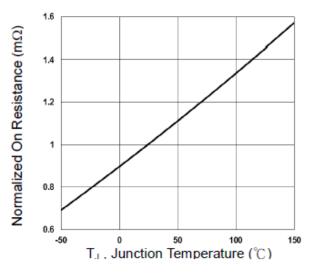


Electrical Characteristics Curve

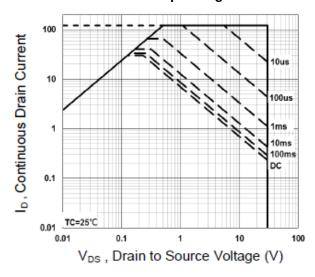
Continuous Drain Current vs. Tc



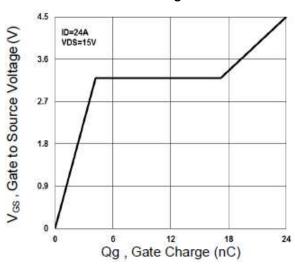
On-Resistance vs. Junction Temperature



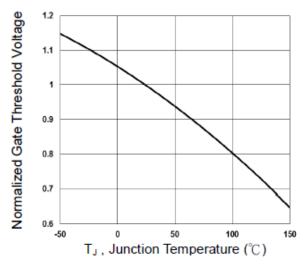
Maximum Safe Operating Area



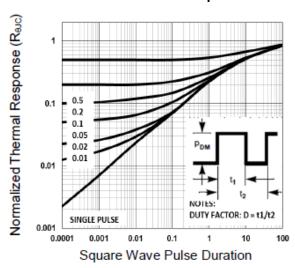
Gate Charge



Threshold Voltage vs. Junction Temperature



Normalized Thermal Transient Impedance Curve

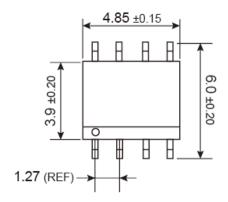


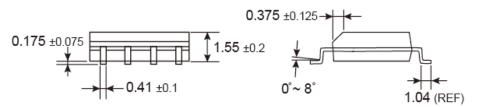


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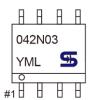
SOP-8 Mechanical Drawing





Unit: Millimeters

Marking Diagram



Y = Year Code

 $\label{eq:market} \begin{array}{ll} \textbf{M} &= \text{Month Code for Halogen Free Product} \\ & (\textbf{O}=\text{Jan},\,\textbf{P}=\text{Feb},\,\textbf{Q}=\text{Mar},\,\textbf{R}=\text{Apl},\,\textbf{S}=\text{May},\,\textbf{T}=\text{Jun},\,\textbf{U}=\text{Jul},\,\textbf{V}=\text{Aug},\,\textbf{W}=\text{Sep},\\ & \textbf{X}=\text{Oct},\,\textbf{Y}=\text{Nov},\,\textbf{Z}=\text{Dec}) \end{array}$

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L = Lot Code

Version: A14



Pb ROHS

TSM042N03CS 30V N-Channel Power MOSFET

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