

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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N-Channel Power MOSFET

30V, 108A, 4.5mΩ

FEATURES

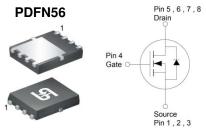
- Low R_{DS(ON)} to minimize conductive losses
- Low gate charge for fast power switching
- 100% UIS and R_q tested
- Compliant to RoHS directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21

KEY PERFORMANCE PARAMETERS				
PARAMETER		VALUE	UNIT	
,	V_{DS}		V	
R _{DS(on)}	$V_{GS} = 10V$	4.5	0	
(max)	$V_{GS} = 4.5V$	6.3	mΩ	
Q_g		9	nC	

APPLICATIONS

- DC-DC Converters
- Battery Power Management
- ORing FET/Load Switching





Note: MSL 1 (Moisture Sensitivity Level) per J-STD-020

ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V_{DS}	30	V	
Gate-Source Voltage		V_{GS}	±20	V	
Continuous Drain Current (Note 1)	$T_C = 25^{\circ}C$		108	^	
	$T_C = 25^{\circ}C$ $T_A = 25^{\circ}C$	l _D	18	Α	
Pulsed Drain Current		I _{DM}	432	Α	
Single Pulse Avalanche Current (Note 2)		I _{AS}	26	Α	
Single Pulse Avalanche Energy (Note 2)		E _{AS}	104	mJ	
Total Power Dissipation	$T_C = 25^{\circ}C$	P _D	89	\A/	
	$T_C = 125$ °C		17.8	W	
Total Power Dissipation	$T_A = 25^{\circ}C$	J	2.6	14/	
	$T_A = 125^{\circ}C$	P_{D}	0.5	W	
Operating Junction and Storage Temperature Range		T _J , T _{STG}	- 55 to +150	°C	

THERMAL PERFORMANCE				
PARAMETER	SYMBOL	LIMIT	UNIT	
Junction to Case Thermal Resistance	R _{eJC}	1.4	°C/W	
Junction to Ambient Thermal Resistance	R _{OJA}	48	°C/W	

Thermal Performance Note: $R_{\Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistances. The case-thermal reference is defined at the solder mounting surface of the drain pins. $R_{\Theta JA}$ is guaranteed by design while $R_{\Theta CA}$ is determined by the user's board design.



PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
Static						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	BV _{DSS}	30			V
Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250 \mu A$	$V_{GS(TH)}$	1.2	2	2.5	V
Gate-Source Leakage Current	$V_{GS} = \pm 20V, V_{DS} = 0V$	I _{GSS}			±100	nA
	$V_{GS} = 0V$, $V_{DS} = 30V$	I _{DSS}			1	μА
Drain-Source Leakage Current	$V_{GS} = 0V, V_{DS} = 30V$ $T_{J} = 125^{\circ}C$				100	
Drain-Source On-State Resistance	$V_{GS} = 10V, I_D = 18A$	Б		3.6	4.5	mΩ
(Note 3)	$V_{GS} = 4.5V, I_D = 18A$	R _{DS(on)}		5.5	6.3	
Forward Transconductance (Note 3)	$V_{DS} = 5V, I_{D} = 18A$	g _{fs}		47		S
Dynamic (Note 4)						
Total Gate Charge	$V_{GS} = 10V, V_{DS} = 15V,$ $I_D = 18A$	Q_g		19		
Total Gate Charge	$V_{GS} = 4.5V, V_{DS} = 15V,$	Q_g		9		nC
Gate-Source Charge		Q_{gs}		4		-
Gate-Drain Charge	I _D = 18A	Q _{gd}		3.7		-
Input Capacitance		C _{iss}		1194		
Output Capacitance	$V_{GS} = 0V, V_{DS} = 15V$	C _{oss}		421		pF
Reverse Transfer Capacitance	f = 1.0MHz	C _{rss}		97		
Gate Resistance	f = 1.0MHz	R_g	0.4	1.2	2.4	Ω
Switching (Note 4)						
Turn-On Delay Time		t _{d(on)}		5.4		
Turn-On Rise Time	$V_{GS} = 10V, V_{DS} = 15V,$ $I_{D} = 18A, R_{G} = 2\Omega,$	t _r		2.2		
Turn-Off Delay Time		t _{d(off)}		12.6		ns
Turn-Off Fall Time		t _f		2		-
Source-Drain Diode						
Forward Voltage (Note 3)	$V_{GS} = 0V, I_{S} = 18A$	V _{SD}			1.2	٧
Reverse Recovery Time	I _S = 18A ,	t _{rr}		23		ns
Reverse Recovery Charge	$dI/dt = 100A/\mu s$	Q _{rr}		17		nC

Notes:

- 1. Silicon limited current only.
- 2. $L=0.3mH,~V_{GS}=10V,~V_{DD}=50V,~R_{G}$ = $25\Omega,~I_{AS}=26A,~Starting~T_{J}=25^{\circ}C$
- 3. Pulse test: Pulse Width \leq 300 μ s, duty cycle \leq 2%.
- 4. Switching time is essentially independent of operating temperature.

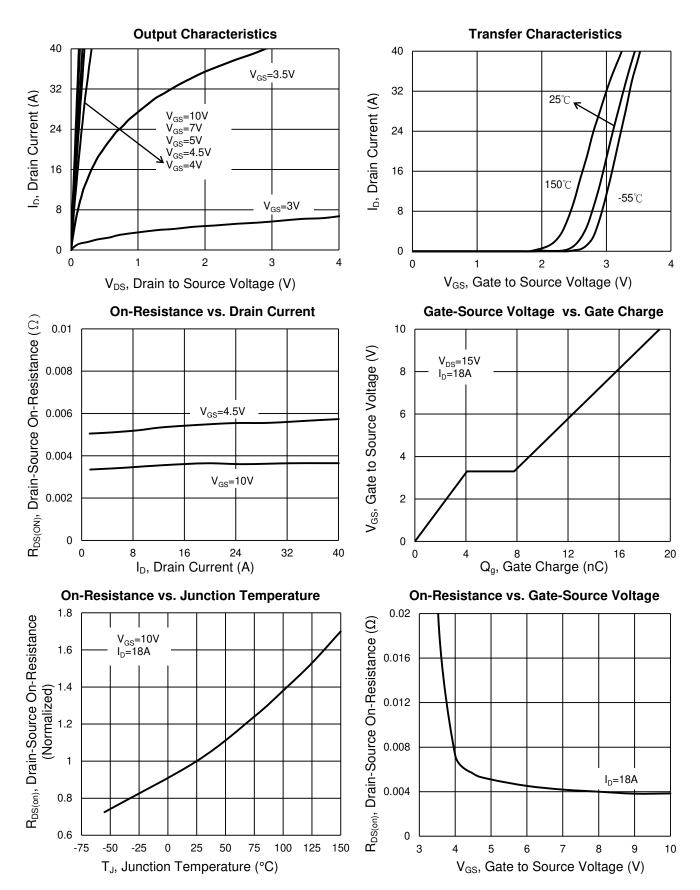
ORDERING INFORMATION

PART NO.	PACKAGE	PACKING
TSM045NA03CR RLG	PDFN56	2,500pcs / 13" Reel



CHARACTERISTICS CURVES

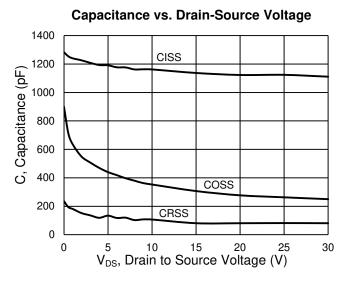
 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

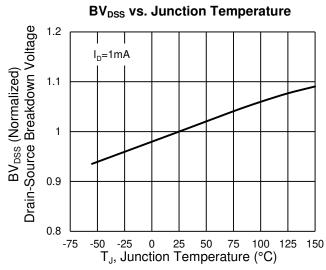




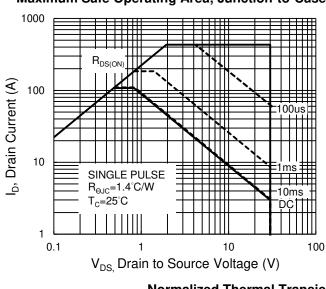
CHARACTERISTICS CURVES

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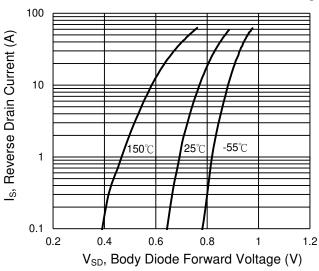




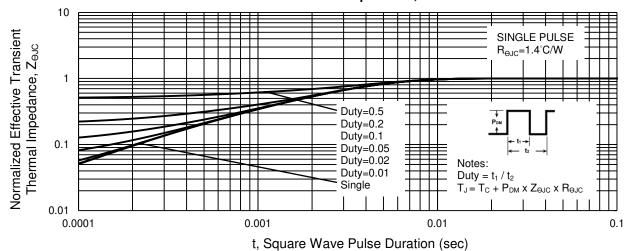
Maximum Safe Operating Area, Junction-to-Case



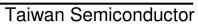
Source-Drain Diode Forward Current vs. Voltage



Normalized Thermal Transient Impedance, Junction-to-Case



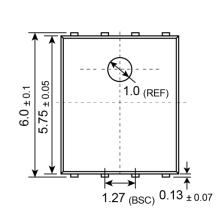
4

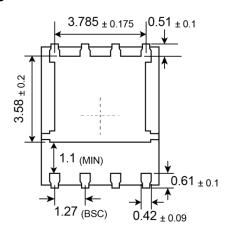




PACKAGE OUTLINE DIMENSIONS (Unit: Millimeters)

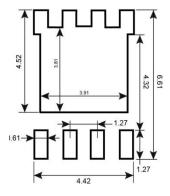
PDFN56







SUGGESTED PAD LAYOUT (Unit: Millimeters)



5

MARKING DIAGRAM



G = Halogen Free

Y = Year Code

WW = Week Code (01~52)

F = Factory Code



Taiwan Semiconductor

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