# imall

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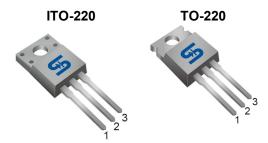
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COMPLIANT

### **TSM13N50A 500V N-Channel MOSFET**



### **Key Parameter Performance**

Parameter	Value	Unit
V <sub>DS</sub>	500	V
R <sub>DS(on)</sub> (max)	0.48	
Qg	31	nC

#### **Features**

✓ Improved dv/dt capability

∠ 100% EAS Guaranteed

#### **Ordering Information**

Ordering Information	<u>on</u>	
Part No.	Package	Packing
TSM13N50ACI C0G	ITO-220	50pcs / Tube
TSM13N50ACZ C0G	TO-220	50pcs / Tube
Note: 'G_denotes for Haloge <900ppm bromine, <9 and <1000ppm antime	00ppm chlorine (<1	

### Absolute Maximum Ratings (Tc=25°C unless therwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	500	V	
Gate-Source Voltage	V <sub>GS</sub>	±30	V	
$T_c = 25^{\circ}C$		13	•	
Continuous Drain Current (Note 1) $T_c = 20 \text{ C}$ $T_c = 100^{\circ}\text{C}$	ID	8	A	
Pulsed Drain Current (Note 1,2,3)	I <sub>DM</sub>	52	А	
Total Power Dissipation @ T <sub>c</sub> =25°C	P <sub>DTOT</sub>	52	W	
Single Pulsed Avalanche Energy (Note 4)	E <sub>AS</sub>	542	mJ	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	- 55 to +150	°C	

Pin Definition: 1. Gate 2. Drain 3. Source

#### **Thermal Performance**

Devemeter	Symphol	Limit	11	
Parameter	Symbol	ITO220	TO-220	Unit
Junction to Case Thermal Resistance	R <sub>初JC</sub>	2.4	0.6	°C/W
Junction to Ambient Thermal Resistance	Raya	65	62.5	°C/W

### **Block Diagram**

Drain

Ο Source

N-Channel MOSFET





### **TSM13N50A** 500V N-Channel MOSFET

#### Electrical Specifications (T<sub>C</sub>=25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Тур	Max	Unit
Static (Note 5)						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_{D} = 250 \mu A$	$BV_{DSS}$	500			V
Gate Threshold Voltage	$V_{\text{DS}} = V_{\text{GS}}, \ I_{\text{D}} = 250 \mu A$	$V_{GS(TH)}$	2		4	V
Gate Body Leakage	$V_{GS} = \pm 30 V, V_{DS} = 0 V$	I <sub>GSS</sub>			±100	nA
Zero Gate Voltage Drain Current	$V_{\text{DS}} = 500V,  V_{\text{GS}} = 0V$	I <sub>DSS</sub>			1	μA
Drain-Source On-State Resistance	$V_{GS} = 10V, I_{D} = 6.5A$	R <sub>DS(ON)</sub>		0.38	0.48	
Dynamic (Note 6)						
Total Gate Charge	1001/1000	Qg	-	31	40	nC
Gate-Source Charge	$V_{DS} = 400V, I_D = 13A,$ $V_{GS} = 10V$	Q <sub>gs</sub>	0	11		
Gate-Drain Charge		Q <sub>gd</sub>		7		
Input Capacitance		Cins		1965		
Output Capacitance	$V_{DS} = 25V, V_{GS} = 0V,$ f = 1.0MHz	<b>G</b> ors		185		pF
Reverse Transfer Capacitance		C <sub>rss</sub>		11		
Switching (Note 6)						
Turn-On Delay Time		t <sub>d(on)</sub>		32		
Turn-On Rise Time	$V_{DD} = 200V$ $R_{GEN} = 25 $ $I_D = 13A V_{OS} = 10V$	t <sub>r</sub>		18		
Turn-Off Delay Time		t <sub>d(off)</sub>		79		ns
Turn-Off Fall Time		t <sub>f</sub>		16		
Source-Drain Diode						
Forward On Voltage	1,_₌13A, V <sub>GS</sub> =0V	V <sub>SD</sub>			1.4	V

Current limited by package
Pulse width limited by the maximum junction temperature

3. Pulse width limited by safe operating area

4. L=15mH,  $I_{AS}$ =8.5A,  $V_{DD}$ =50V,  $R_{G}$ =25|, Starting  $T_{J}$ =25°C

5. Pulse test: pulse width  $H300\mu$ S, duty cycle H2%

6. Guaranteed by design, not subject to production testing.



### **TSM13N50A 500V N-Channel MOSFET**

#### **Electrical Characteristics Curves**

-50

-100

0

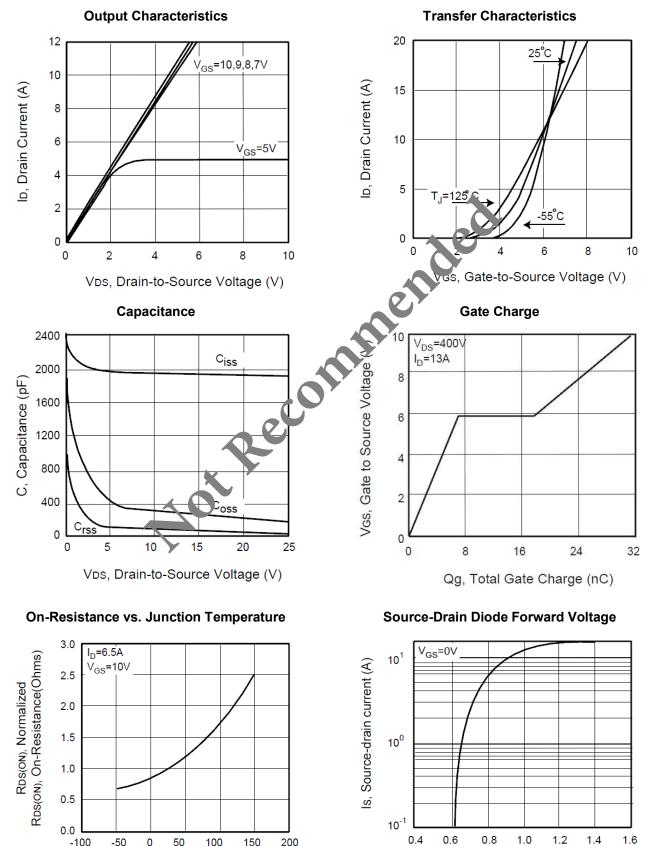
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TJ, Junction Temperature(°C)

100

150

200

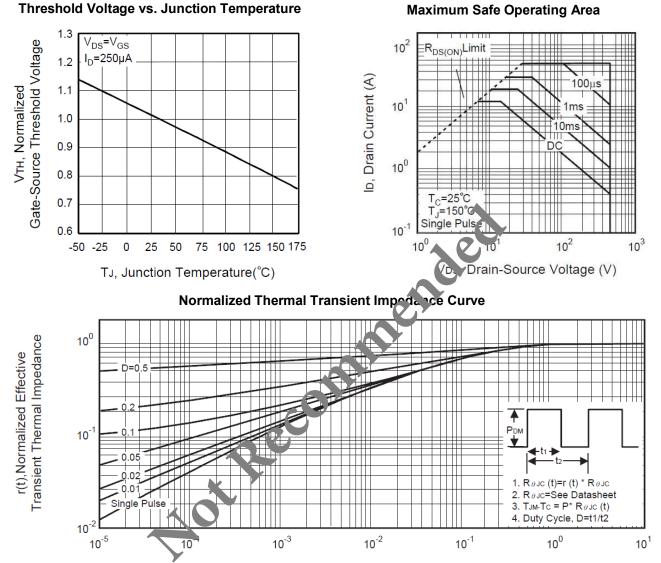


VSD, Body Diode Forward Voltage (V)



### **TSM13N50A** 500V N-Channel MOSFET

#### **Electrical Characteristics Curves**

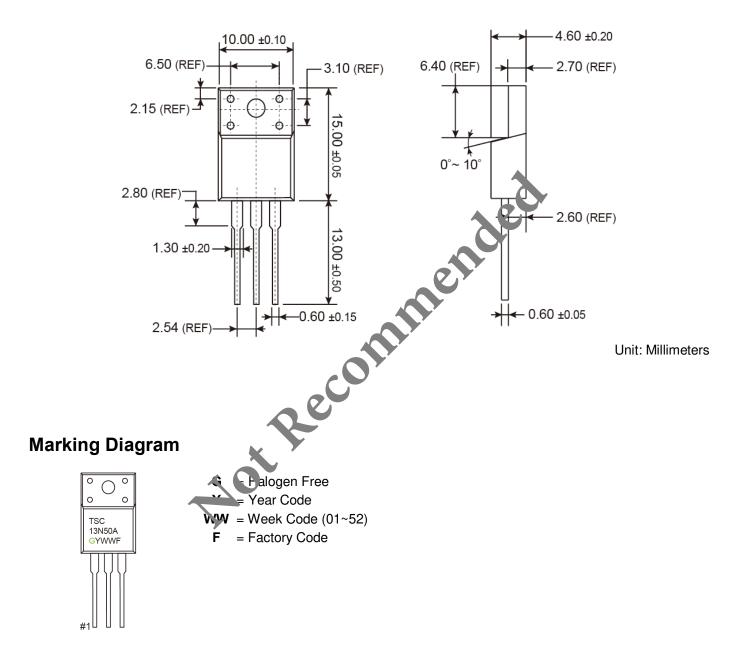


Square Wave Pulse Duration (msec)



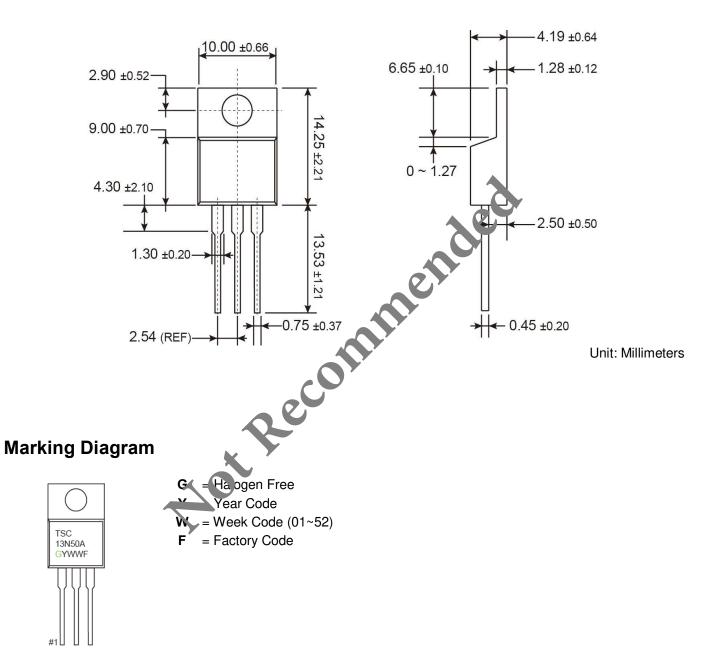


### **ITO-220 Mechanical Drawing**









### **TO-220 Mechanical Drawing**







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