



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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### Pin Definition:

1. Gate
2. Source
3. Drain

### Key Parameter Performance

Parameter		Value	Unit
$V_{DS}$		20	V
$R_{DS(on)}$ (max)	$V_{GS} = 4.5V$	33	mΩ
	$V_{GS} = 2.5V$	40	
	$V_{GS} = 1.8V$	51	
$Q_g$		11	nC

### Features

- Advance Trench Process Technology
- High Density Cell Design for Ultra Low On-resistance

### Application

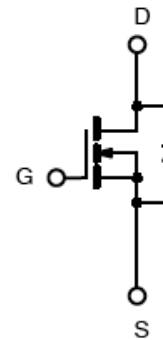
- Load Switch
- PA Switch

### Ordering Information

Part No.	Package	Packing
TSM2312CX RFG	SOT-23	3,000pcs / 7" Reel

**Note:** "G" denotes for Halogen- and Antimony-free as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds

### Block Diagram



N-Channel MOSFET

### Absolute Maximum Ratings ( $T_C = 25^\circ C$ , unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V <sub>DS</sub>	20	V
Gate-Source Voltage		V <sub>GS</sub>	±8	V
Continuous Drain Current		I <sub>D</sub>	4.9	A
Pulsed Drain Current <sup>(Note 1)</sup>		I <sub>DM</sub>	15	A
Continuous Source Current (Diode Conduction) <sup>(Note 2)</sup>		I <sub>S</sub>	1.0	A
Maximum Power Dissipation	Ta = 25°C	P <sub>D</sub>	0.75	W
	Ta = 75°C		0.48	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

### Thermal Performance

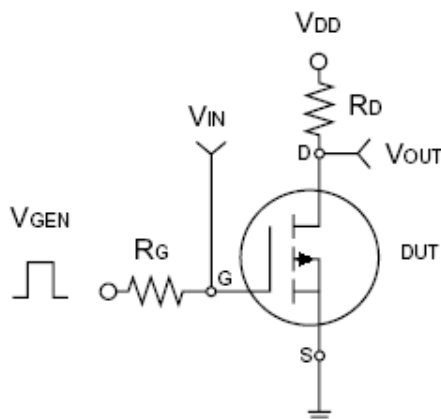
Parameter	Symbol	Limit	Unit
Thermal Resistance Junction to Lead	$R_{\theta JL}$	75	°C/W
Thermal Resistance Junction to Ambient	$R_{\theta JA}$	140	°C/W

### Electrical Specifications

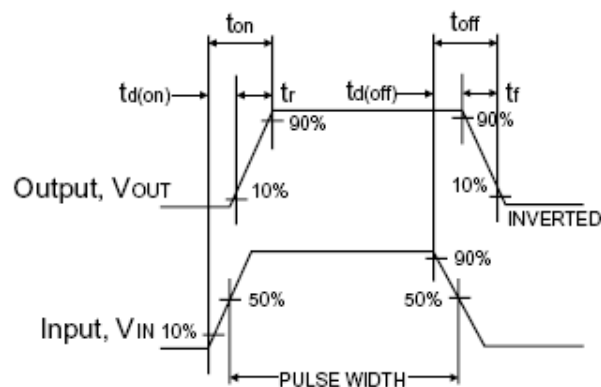
Parameter	Conditions	Symbol	Min	Typ	Max	Unit
Static <sup>(Note 3)</sup>						
Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250uA	BV <sub>DSS</sub>	20	--	--	V
Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250uA	V <sub>GS(TH)</sub>	0.45	0.65	1.0	V
Gate Body Leakage	V <sub>GS</sub> = ±8V, V <sub>DS</sub> = 0V	I <sub>GSS</sub>	--	--	±100	nA
Zero Gate Voltage Drain Current	V <sub>DS</sub> = 16V, V <sub>GS</sub> = 0V	I <sub>DSS</sub>	--	--	1.0	μA
On-State Drain Current	V <sub>DS</sub> =10V, V <sub>GS</sub> = 4.5V	I <sub>D(ON)</sub>	15	--	--	A
Drain-Source On-State Resistance	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 4.9A	R <sub>DS(ON)</sub>	--	27	33	mΩ
	V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 4.4A		--	33	40	
	V <sub>GS</sub> = 1.8V, I <sub>D</sub> = 3.9A		--	42	51	
Forward Transconductance	V <sub>DS</sub> = 15V, I <sub>D</sub> = 5.0A	g <sub>fs</sub>	--	40	--	S
Diode Forward Voltage	I <sub>S</sub> = 1.0A, V <sub>GS</sub> = 0V	V <sub>SD</sub>	--	0.8	1.2	V
Dynamic <sup>(Note 4)</sup>						
Total Gate Charge	V <sub>DS</sub> = 10V, I <sub>D</sub> = 5.0A, V <sub>GS</sub> = 4.5V	Q <sub>g</sub>	--	11	14	nC
Gate-Source Charge		Q <sub>gs</sub>	--	1.5	--	
Gate-Drain Charge		Q <sub>gd</sub>	--	2.1	--	
Input Capacitance	V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V, f = 1.0MHz	C <sub>iss</sub>	--	500	--	pF
Output Capacitance		C <sub>oss</sub>	--	300	--	
Reverse Transfer Capacitance		C <sub>rss</sub>	--	140	--	
Switching <sup>(Note 5)</sup>						
Turn-On Delay Time	V <sub>DD</sub> = 10V, R <sub>L</sub> = 10Ω, I <sub>D</sub> = 1A, V <sub>GEN</sub> = 4.5V, R <sub>G</sub> = 6Ω	t <sub>d(on)</sub>	--	15	25	ns
Turn-On Rise Time		t <sub>r</sub>	--	40	60	
Turn-Off Delay Time		t <sub>d(off)</sub>	--	48	70	
Turn-Off Fall Time		t <sub>f</sub>	--	31	45	

#### Notes:

1. Pulse width limited by the maximum junction temperature
2. Surface Mounted on FR4 Board  $t \leq 5$  sec.
3. Pulse test:  $PW \leq 300\mu s$ , duty cycle  $\leq 2\%$
4. For DESIGN AID ONLY, not subject to production testing.
5. Switching time is essentially independent of operating temperature.



Switching Test Circuit

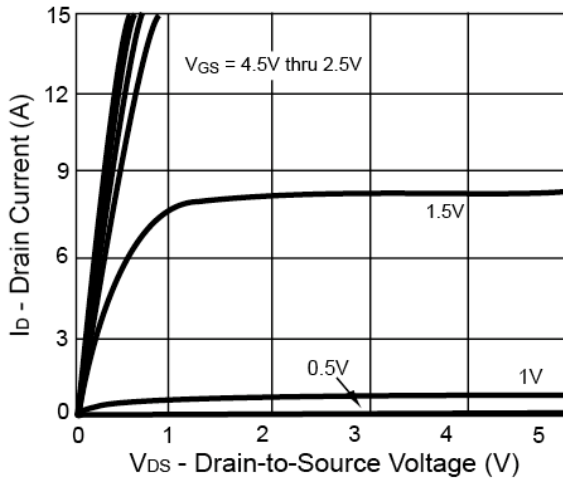


Switchin Waveforms

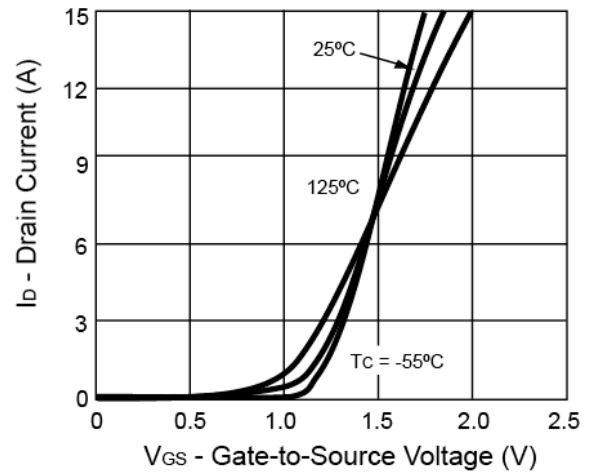


## Electrical Characteristics Curve

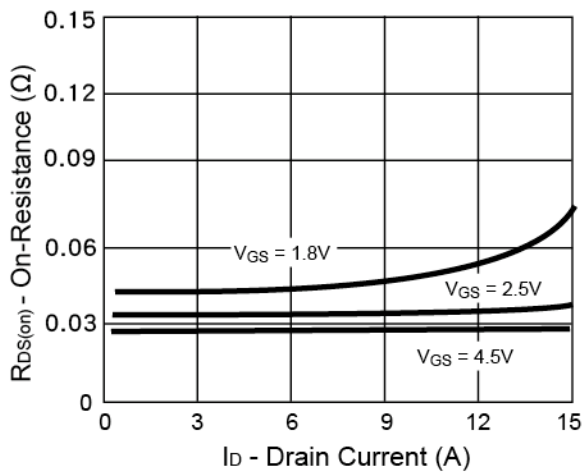
**Output Characteristics**



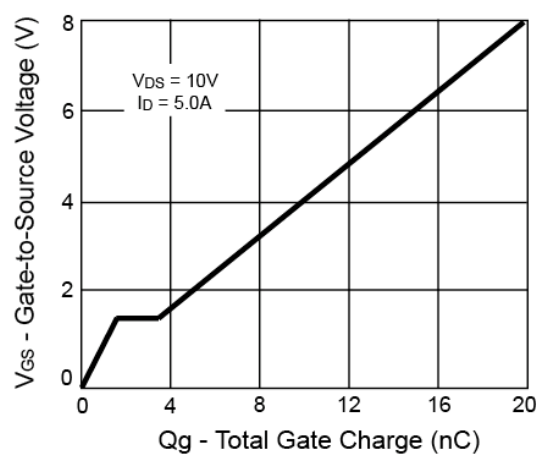
**Transfer Characteristics**



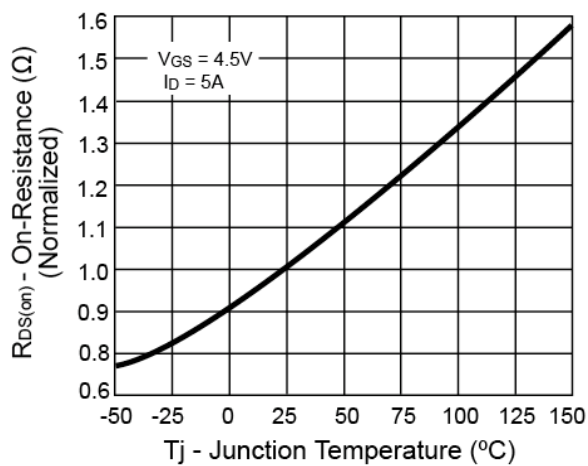
**On-Resistance vs. Drain Current**



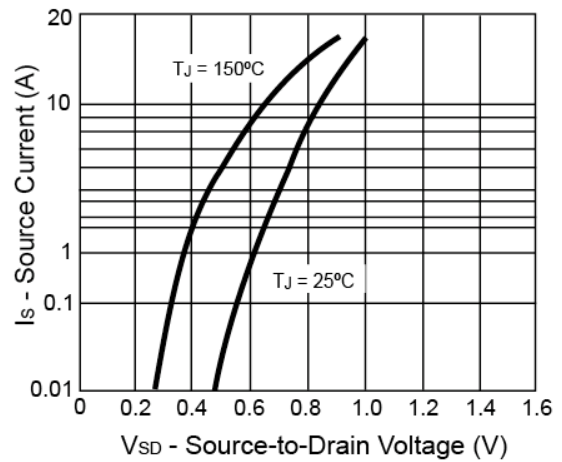
**Gate Charge**



**On-Resistance vs. Junction Temperature**

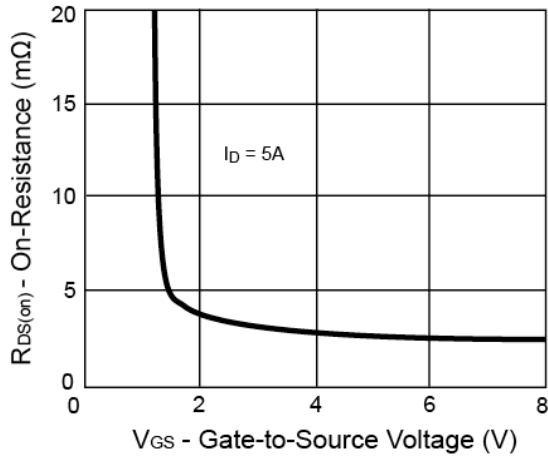


**Source-Drain Diode Forward Voltage**

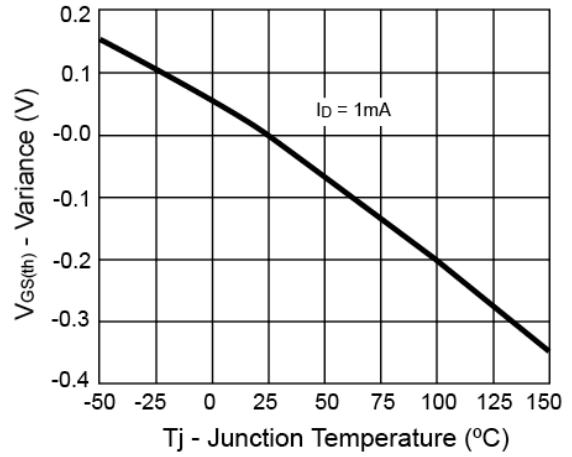


### Electrical Characteristics Curve

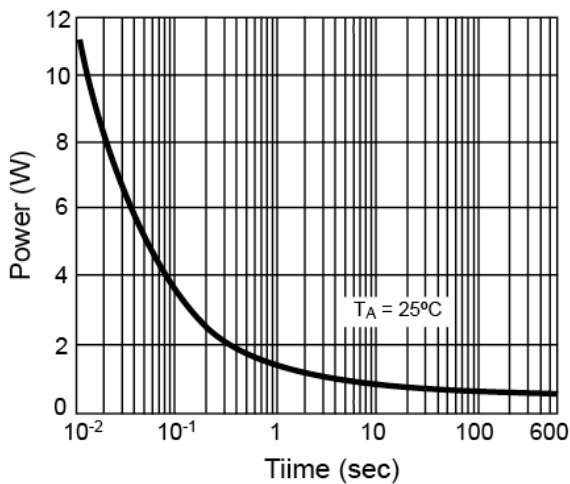
**On-Resistance vs. Gate-Source Voltage**



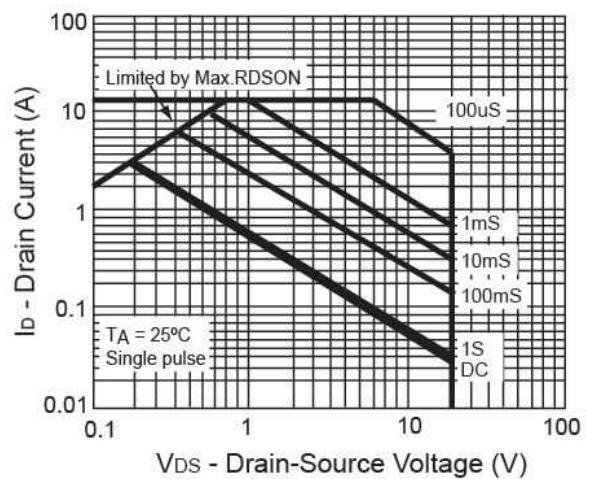
**Threshold Voltage**



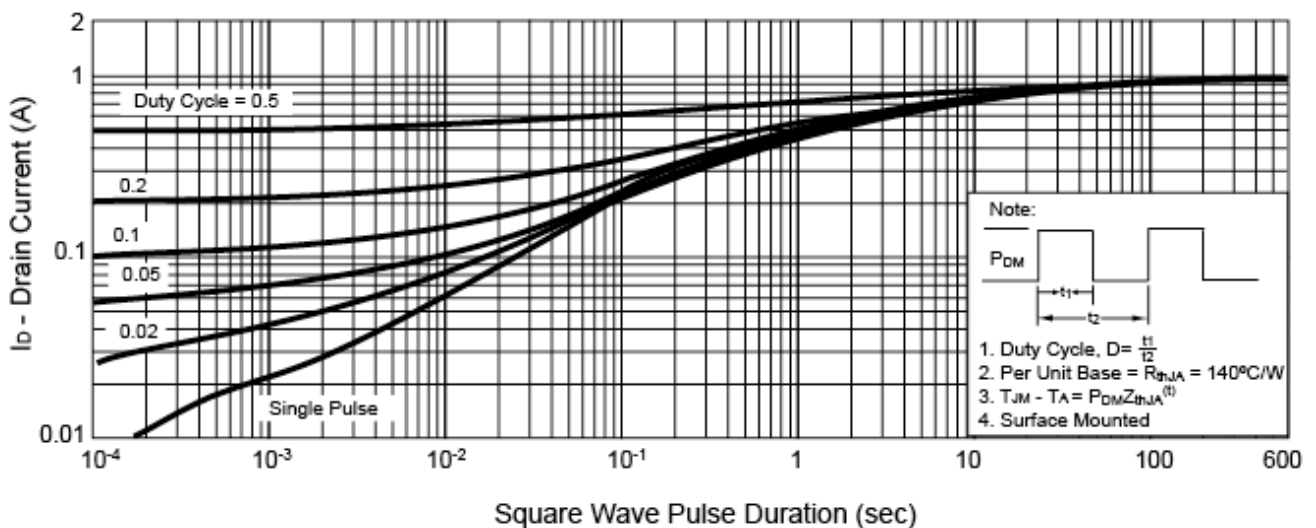
**Single Pulse Power**



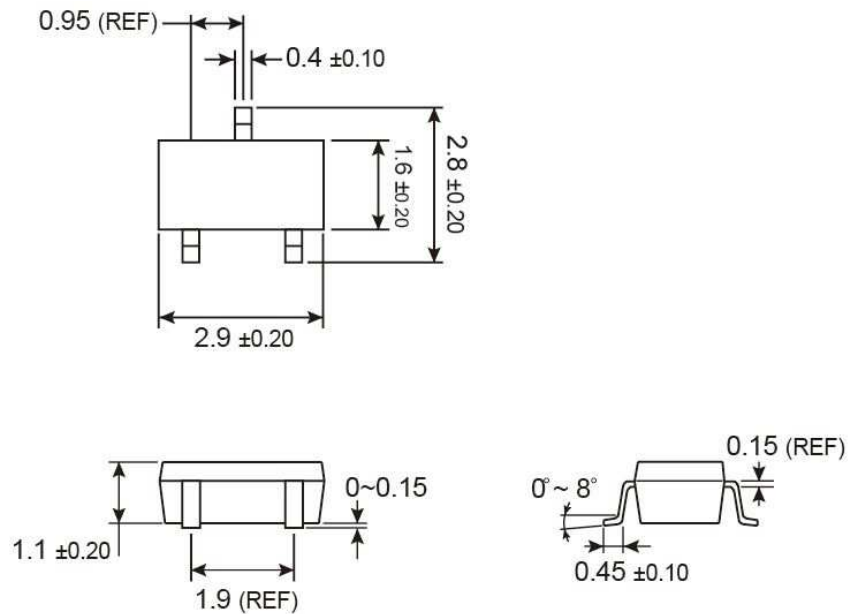
**Safety Operation Area**



**Normalized Thermal Transient Impedance, Junction-to-Ambient**

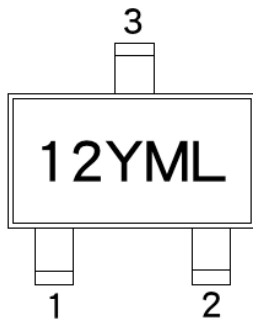


## SOT-23 Mechanical Drawing



Unit: Millimeters

## Marking Diagram



**12** = Device Code  
**Y** = Year Code  
**M** = Month Code for Halogen Free Product  
     **O** =Jan   **P** =Feb   **Q** =Mar   **R** =Apr  
     **S** =May   **T** =Jun   **U** =Jul   **V** =Aug  
     **W** =Sep   **X** =Oct   **Y** =Nov   **Z** =Dec  
**L** = Lot Code

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