imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

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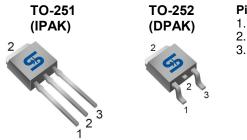
Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









Pin Definition: 1. Gate 2. Drain 3. Source

Key Parameter Performance

Parameter	Value	Unit
V _{DS}	600	V
R _{DS(on)} (max)	4	
Qg (typ)	9.5	nC

Features

- ✓ 100% Avalanche Tested
- ✓ G-S ESD Protection Diode Embedded

Ordering Information

Part No.	Package	Packing
TSM2N60ECH C5G	TO-251	75pcs / Tube
TSM2N60ECP ROG	TO-252	2.5kpcs / 13_Reel

Note: 'G_denotes for Halogen- and Antimony-free as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds

N-Channel MOSFET with ESD Protection

Absolute Maximum Ratings (T_c = 25°C unless otherwise noted)

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	600	V	
Gate-Source Voltage		V _{GS}	±30	V	
O II O I (Note 1)	Tc = 25°C	- I _D -	2	А	
Continuous Drain Current (Note 1) $Tc = 10$			Tc = 100°C	1.43	А
Pulsed Drain Current (Note 2)		I _{DM}	8	А	
Repetitive Avalanche Current ^(Note 1)		I _{AR}	2	А	
Repetitive Avalanche Energy (Note 1)		E _{AR}	5.2	А	
Single Pulse Avalanche Energy (Note 3)		E _{AS}	66	mJ	
Total Davies Dissignation	$T_{\rm C} = 25$	°C	P	52.1	W
Total Power Dissipation		W/°C			
Peak Diode Recovery dV/dt (Note 4)		dV/dt	4.5	V/ns	
Operating Junction Temperature		TJ	-55 to +150	°C	
Storage Temperature Range		T _{STG}	-55 to +150	°C	

Thermal Performance

Parameter	Symbol	Limit	Unit
Thermal Resistance - Junction to Case	R _{∄IJC}	2.4	°C/W
Thermal Resistance - Junction to Ambient	R _{ð:IJA}	110	°C/W

Block Diagram





Electrical Specifications (T_c = 25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Тур	Max	Unit
Static (Note 5)						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250 \mu A$	BV _{DSS}	600			V
Drain-Source On-State Resistance	$V_{GS} = 10V, I_D = 1A$	R _{DS(ON)}		3.2	4	
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu A$	V _{GS(TH)}	3		5	V
	$V_{DS} = 600V, V_{GS} = 0V$				1	μA
Zero Gate Voltage Drain Current	V _{DS} = 480V, T _J = 125°C	- I _{DSS}			10	
Gate Body Leakage	$V_{GS} = \pm 30V, V_{DS} = 0V$	I _{GSS}			±100	μA
Forward Transconductance	$V_{DS} = 30V, I_{D} = 1A$	g _{fs}		3		S
Dynamic ^(Note 6)						
Total Gate Charge	$V_{DS} = 480V, I_D = 2A,$ $V_{GS} = 10V$	Qg		9.5		nC
Gate-Source Charge		Q _{gs}		2.1		
Gate-Drain Charge		Q _{gd}		3.9		
Input Capacitance	$V_{\rm DS} = 25V, V_{\rm GS} = 0V,$	C _{iss}		362		pF
Output Capacitance		C _{oss}		40		
Reverse Transfer Capacitance	f = 1MHz	C _{rss}		7.2		
Switching (Note 7)						
Turn-On Delay Time		t _{d(on)}		21		
Turn-On Rise Time	$V_{DD} = 300V$, $V_{GS} = 10V$,	t _r		22		ns
Turn-Off Delay Time	$R_G = 25\Omega, I_D = 2A$	t _{d(off)}		41		
Turn-Off Fall Time		t _f		21		
Source-Drain Diode Ratings and C	haracteristic (Note 5)					
Maximum Continuous Drain-Source Diode Forward Current		I _S			2	Α
Maximum Pulse Drain-Source Diode Forward Current		I _{SM}			8	Α
Diode-Source Forward Voltage	$V_{GS} = 0V, I_S = 2A$	V _{SD}			1.5	V
Reverse Recovery Time	$V_{GS} = 0V, I_{S} = 2A$	t _{rr}		238		ns
Reverse Recovery Charge	dl _F /dt = 100A/µs	Q _{rr}		0.8		nC

Notes:

- 1. Current limited by package
- 2. Pulse width limited by the maximum junction temperature
- 3. V_{DD} = 50V, L= 30.5mH, I_{AS} = 2A, R_G = 25 Ω , Starting T_J = 25°C
- 4. I_{SD} #2A, di/dt #200A/µs, V_{DD} #BV_{DS}, Starting T_J = 25°C
- 5. Pulse test: PW $\#300\mu s,$ duty cycle #2%
- 6. For DESIGN AID ONLY, not subject to production testing.
- 7. Switching time is essentially independent of operating temperature.



0.5

0.0

-80

-40

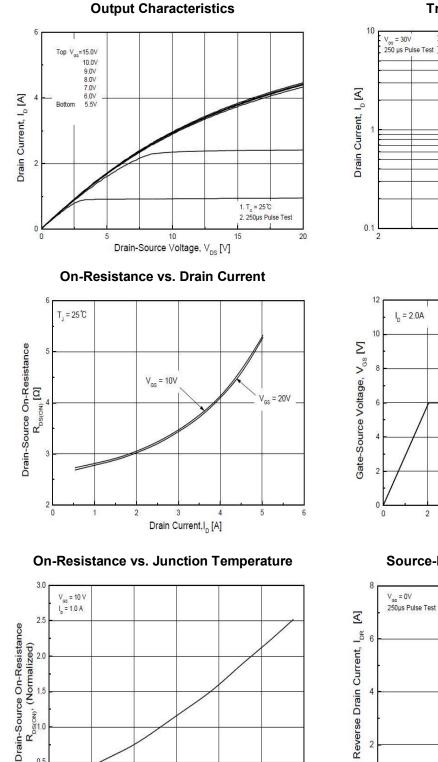
0

40

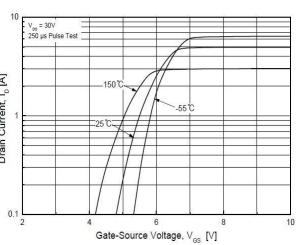
Junction Temperature, T, [°C]

80

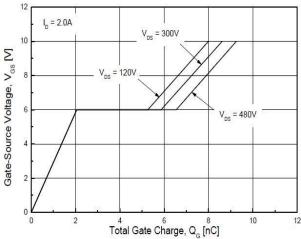
TSM2N60E 600V, 2A, 4| N-Channel Power MOSFET



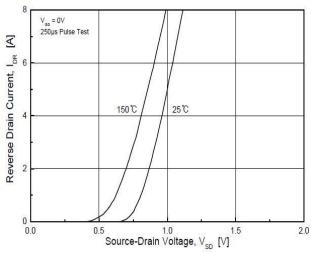
Electrical Characteristics Curves (T_c = 25°C, unless otherwise noted)







Source-Drain Diode Forward Voltage



160

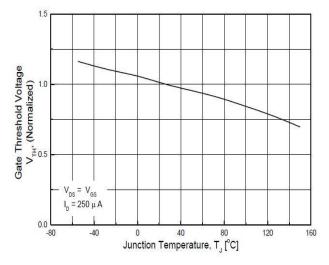
120



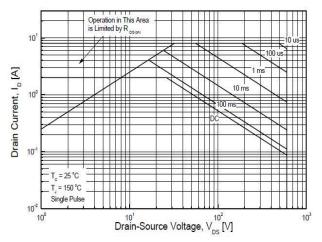
Electrical Characteristics Curve (T_c = 25°C, unless otherwise noted)

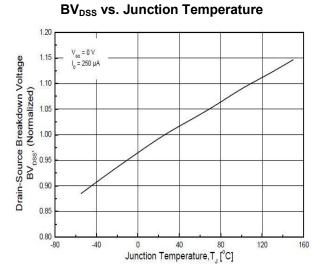
Drain Current vs. Case Temperature

Threshold Voltage vs. Junction Temperature

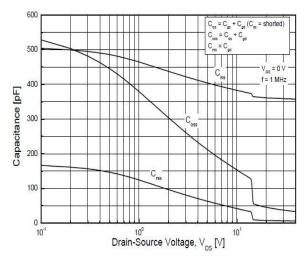


Maximum Safe Operating Area

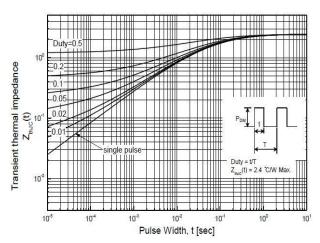




Capacitance vs. Drain-Source Voltage

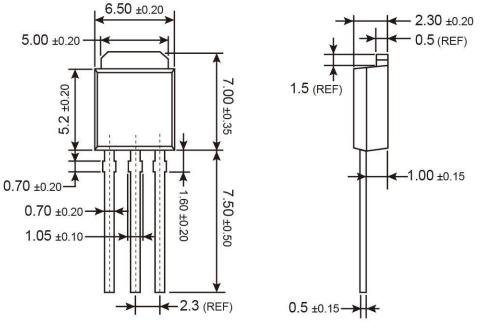


Normalized Transient Impedance



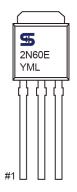


TO-251 Mechanical Drawing



Unit: Millimeters

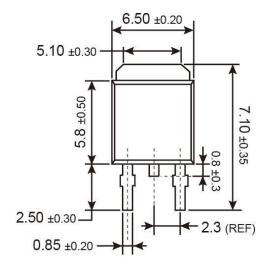
Marking Diagram

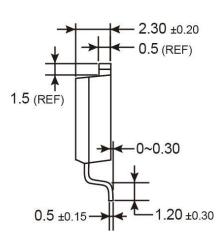


- Y = Year Code
- M = Month Code for Halogen Free Product
 (O=Jan, P=Feb, Q=Mar, R=Apl, S=May, T=Jun, U=Jul, V=Aug, W=Sep, X=Oct, Y=Nov, Z=Dec)
- L = Lot Code



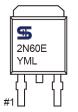
TO-252 Mechanical Drawing





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Marking Diagram



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