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600V, 4A, 2.5 | N-Channel Power MOSFET

TO-251 (IPAK)



TO-252 (DPAK)



Pin Definition:

- 1. Gate
- 2. Drain
- 3. Source

Key Parameter Performance

Parameter	Value	Unit
V_{DS}	600	V
$R_{DS(on)}(max)$	2.5	
Q _g (typ)	13	nC

Features

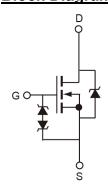
- ∠ 100% Avalanche Tested
- ∠ G-S ESD Protection Diode Embedded

Ordering Information

Part No.	Package	Packing
TSM4N60ECH C5G	TO-251	75pcs / Tube
TSM4N60ECP ROG	TO-252	2.5kpcs / 13_Reel

Note: 'G_denotes for Halogen- and Antimony-free as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds

Block Diagram



N-Channel MOSFET with ESD Protection

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V_{DS}	600	V
Gate-Source Voltage		V_{GS}	±30	V
Continuous Drain Current (Note 1) Tc=25°C		4	А
Continuous Drain Current	Tc=100°C	l _D	2.34	Α
Pulsed Drain Current (Note 2)		I _{DM}	16	Α
Repetitive Avalanche Current (Note 1)		I _{AR}	4	Α
Repetitive Avalanche Energy (Note 1)		E _{AR}	8.62	mJ
Single Pulse Avalanche Energy (Note 3)		E _{AS}	192	mJ
Total Dawer Dissipation	@ T _C = 25°C	Б	86.2	W
Total Power Dissipation	Derate above T _C = 25°C	P_D	4 2.34 16 4 8.62 192 86.2 0.68 V 4.5	W/°C
Peak Diode Recovery dV/dt (Note 4)		dV/dt	4.5	V/ns
Operating Junction Temperature		T_J	150	℃
Storage Temperature Range		T _{STG}	-55 to +150	°C

Thermal Performance

Parameter	Symbol	Limit	Unit
Thermal Resistance - Junction to Case	R _{∄IJC}	1.45	°C/W
Thermal Resistance - Junction to Ambient	R _{疗以A}	110	°C/W





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Electrical Specifications (T_C = 25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Тур	Max	Unit
Static (Note 5)		•				•
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	BV _{DSS}	600			V
Drain-Source On-State Resistance	$V_{GS} = 10V, I_{D} = 2A$	R _{DS(ON)}		2	2.5	-
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	$V_{GS(TH)}$	3		5	V
	$V_{DS} = 600V, V_{GS} = 0V$				1	μА
Zero Gate Voltage Drain Current	$V_{DS} = 480V, T_{J} = 125^{\circ}C$	- I _{DSS}			10	
Gate Body Leakage	$V_{GS} = \pm 30V, V_{DS} = 0V$	I _{GSS}			±100	μΑ
Forward Transconductance	$V_{DS} = 30V, I_{D} = 2A$	g _{fs}		6		S
Dynamic (Note 6)						
Total Gate Charge		Qg		12		
Gate-Source Charge	$V_{DS} = 480V, I_D = 4A,$ $V_{GS} = 10V$	Q_{gs}		3		nC
Gate-Drain Charge	V _{GS} = 10 V	Q_{gd}		6		
Input Capacitance		C _{iss}		545		
Output Capacitance	$V_{DS} = 25V, V_{GS} = 0V,$ f = 1MHz	C _{oss}		61		pF
Reverse Transfer Capacitance	7 I = TIVITIZ	C _{rss}		10		
Switching (Note 7)						
Turn-On Delay Time		t _{d(on)}		18		
Turn-On Rise Time	$V_{DD} = 300V, V_{GS} = 10V,$	t _r	-	27		
Turn-Off Delay Time	$R_G = 25\Omega$, $I_D = 4A$	$t_{d(off)}$		47		ns
Turn-Off Fall Time		t _f		21		
Source-Drain Diode Ratings and C	Characteristic (Note 5)					
Maximum Continuous Drain-Source Diode Forward Current		Is			4	Α
Maximum Pulse Drain-Source Diode Forward Current		I _{SM}	-		16	Α
Diode-Source Forward Voltage	$V_{GS} = 0V$, $I_S = 4A$	V _{SD}			1.5	V
Reverse Recovery Time	$V_{GS} = 0V$, $I_S = 4A$	t _{rr}		316		ns
Reverse Recovery Charge	$dI_F/dt = 100A/\mu s$	Q _{rr}	-	1.2		nC
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Notes:

- 1. Current limited by package
- 2. Pulse width limited by the maximum junction temperature
- 3. V_{DD} = 50V, L= 22mH, I_{AS} = 4A, R_G = 25 Ω , Starting T_J = 25 $^{\circ}$ C
- 4. $I_{SD} H 4A$, di/dt $H 200A/\mu s$, $V_{DD} H B V_{DS}$, Starting $T_J = 25^{\circ}C$
- 5. Pulse test: PW #300µs, duty cycle #2%
- 6. For DESIGN AID ONLY, not subject to production testing.
- 7. Switching time is essentially independent of operating temperature.

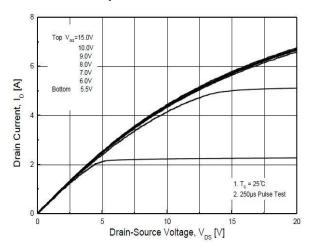


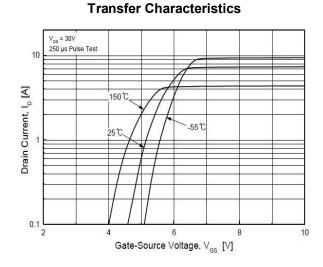




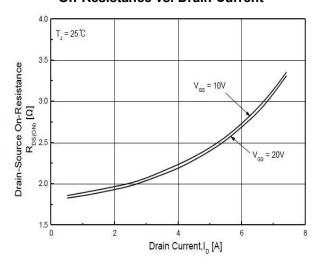
Electrical Characteristics Curves (T_C = 25°C, unless otherwise noted)

Output Characteristics

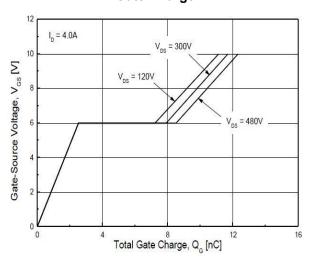




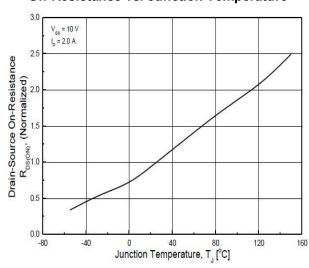
On-Resistance vs. Drain Current



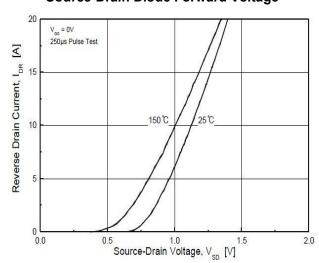
Gate Charge



On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage



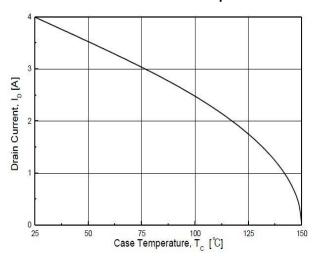


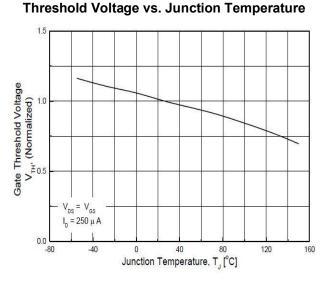


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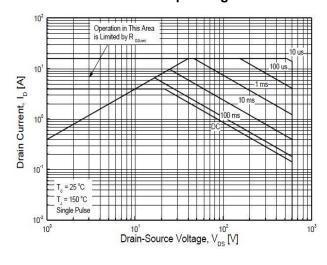
Electrical Characteristics Curve (T_C = 25°C, unless otherwise noted)

Drain Current vs. Case Temperature

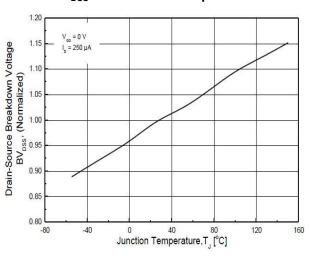




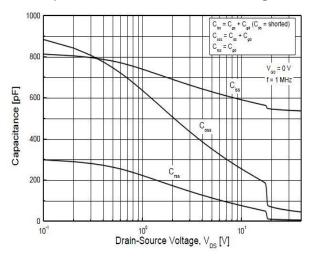
Maximum Safe Operating Area



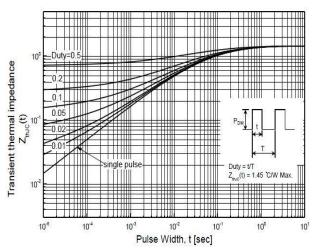
BV_{DSS} vs. Junction Temperature



Capacitance vs. Drain-Source Voltage



Normalized Transient Impedance

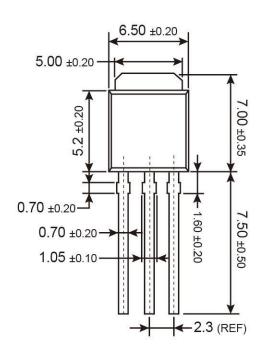


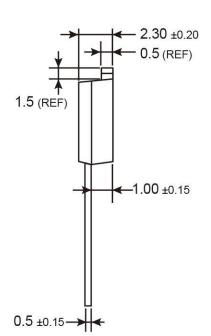


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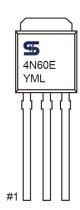
TO-251 Mechanical Drawing





Unit: Millimeters

Marking Diagram



Y = Year Code

M = Month Code for Halogen Free Product (O=Jan, P=Feb, Q=Mar, R=Apl, S=May, T=Jun, U=Jul, V=Aug, W=Sep, X=Oct, Y=Nov, Z=Dec)

L = Lot Code

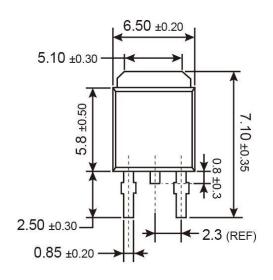


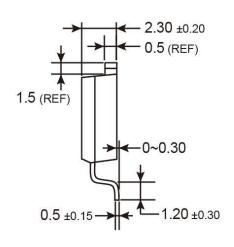
TSM4N60E 600V, 4A, 2.5|



N-Channel Power MOSFET

TO-252 Mechanical Drawing





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