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### 30V P-Channel Power MOSFET



**SOT-23** 



### Pin Definition:

- 1. Gate
- 2. Source
- 3. Drain

### **Key Parameter Performance**

Parameter		Value	Unit	
$V_{ t DS}$		-30	٧	
R <sub>DS(on)</sub> (max)	V <sub>GS</sub> =- 10V	65		
	$V_{GS} = -4.5V$	75	m	
	$V_{GS} = -2.5V$	100		
$Q_g$		8	nC	

### **Features**

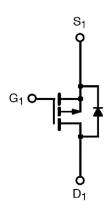
- ∠ Fast Switching
- ∠ Suited for -2.5V Gate Drive Applications
- ∠ Halogen-free

### **Ordering Information**

Part No.	Package	Packing		
TSM650P03CX RFG	SOT-23	3kcs / 7_Reel		

**Note:** 'G\_denotes for Halogen- and Antimony-free as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds

### **Block Diagram**



P-Channel MOSFET

### **Absolute Maximum Ratings** (T<sub>C</sub> = 25°C unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V <sub>DS</sub>	-30	V
Gate-Source Voltage		$V_{GS}$	±12	V
Continuous Drain Current	$T_C = 25^{\circ}C$		-4.1	Α
Continuous Drain Current	T <sub>C</sub> = 100°C	l <sub>D</sub>	-2.6	Α
Pulsed Drain Current (Note 1)		I <sub>DM</sub>	-16.4	Α
Power Dissipation @ T <sub>C</sub> = 25°C		P <sub>D</sub>	1.56	W
Operating Junction Temperature		T <sub>J</sub>	150	°C
Storage Temperature Range	·	T <sub>STG</sub>	-55 to +150	°C

### **Thermal Performance**

Parameter	Symbol	Limit	Unit
Thermal Resistance - Junction to Ambient	R∄IJA	80	°C/W



## 30V P-Channel Power MOSFET



**Electrical Specifications** (T<sub>C</sub> = 25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Тур	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = -250\mu A$	BV <sub>DSS</sub>	-30			V
Drain-Source On-State Resistance	$V_{GS} = -10V, I_{D} = -4A$	R <sub>DS(on)</sub>		55	65	m
	$V_{GS} = -4.5V, I_{D} = -3A$			65	75	
	$V_{GS} = -2.5V, I_{D} = -2A$			85	100	
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	$V_{GS(TH)}$	-0.4	-0.7	-0.9	V
Zava Cata Valtana Duain Commant	$V_{DS} = -30V, V_{GS} = 0V$				-1	μΑ
Zero Gate Voltage Drain Current	$V_{DS} = -24V, T_{J} = 125^{\circ}C$	$I_{DSS}$			-10	
Gate Body Leakage	$V_{GS} = \pm 12V, V_{DS} = 0V$	$I_{GSS}$	-		±100	nA
Forward Transconductance (Note 2)	$V_{DS} = -10V, I_{D} = -3A$	g <sub>fs</sub>		5.4		S
Dynamic						
Total Gate Charge (Note 2,3)	15)/ 1 44	$Q_g$		8		nC
Gate-Source Charge (Note 2,3)	$V_{DS} = -15V, I_{D} = -4A,$	$Q_{gs}$		1.9		
Gate-Drain Charge (Note 2,3)	$V_{GS} = -4.5V$	$Q_{gd}$		1.4		
Input Capacitance	\\ 45\\\\\ 0\\	$C_{iss}$		810		
Output Capacitance	$V_{DS} = -15V, V_{GS} = 0V,$	C <sub>oss</sub>		85		рF
Reverse Transfer Capacitance	f = 1.0MHz	$C_{rss}$		50		
Switching						
Turn-On Delay Time (Note 2,3)		$t_{d(on)}$		5.4		
Turn-On Rise Time (Note 2,3)	$V_{DD} = -15V, I_{D} = -1A,$	t <sub>r</sub>		19.4		
Turn-Off Delay Time (Note 2,3)	$V_{GS} = -10V, R_{GEN} = 6$	$t_{d(off)}$		45.9		ns
Turn-Off Fall Time (Note 2,3)	·	t <sub>f</sub>		12.4		
Source-Drain Diode Ratings and Ch	aracteristic					
Maximum Continuous Drain-Source		1			-4.1	^
Diode Forward Current	Integral reverse diode in the MOSFET	I <sub>S</sub>			-4.1	Α
Maximum Pulse Drain-Source Diode		1			-16.4	^
Forward Current		I <sub>SM</sub>			-10.4	Α
Diode-Source Forward Voltage	$V_{GS} = 0V, I_{S} = -1A$	$V_{SD}$			-1	V

### Note:

- 1. Pulse width limited by safe operating area
- 2. Pulse test: pulse width #300µs, duty cycle #2%
- 3. Switching time is essentially independent of operating temperature.



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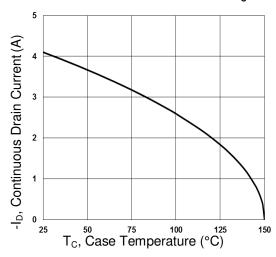
Normalized R<sub>DS</sub>(on) vs. T<sub>J</sub>

1.6

## Pb ROHS COMPLIANT

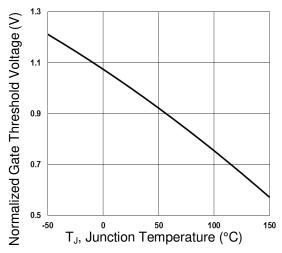
### **Electrical Characteristics Curve**

### Continuous Drain Current vs. Tc

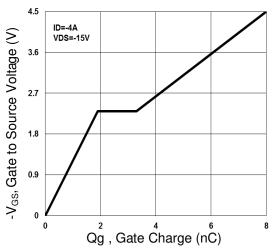


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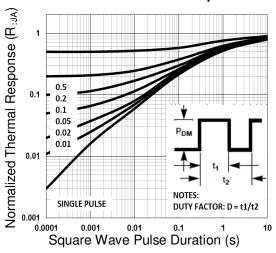
### Threshold Voltage vs. Junction Temperature



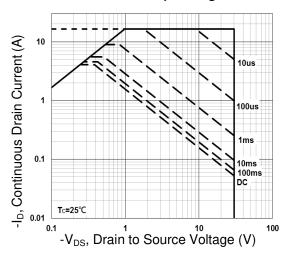




### **Normalized Thermal Transient Impedance Curve**



### **Maximum Safe Operating Area**

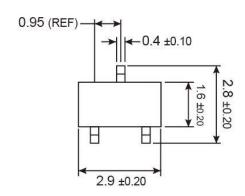


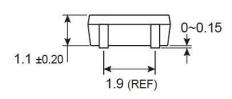


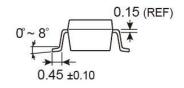
## 30V P-Channel Power MOSFET



## **SOT-23 Mechanical Drawing**







Unit: Millimeters

### **Marking Diagram**



**63** = Device Code

Y = Year Code

M = Month Code for Halogen Free Product (O=Jan, P=Feb, Q=Mar, R=Apl, S=May, T=Jun, U=Jul, V=Aug, W=Sep, X=Oct, Y=Nov, Z=Dec)

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L = Lot Code

Version: A14



## TSM650P03CX 30V P-Channel Power MOSFET



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