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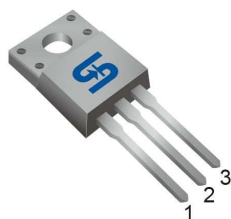
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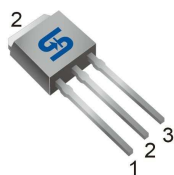




ITO-220



TO-251 (IPAK)



Pin Definition:

1. Gate
2. Drain
3. Source

Key Parameter Performance

Parameter	Value	Unit
V_{DS}	500	V
$R_{DS(on)}$ (max)	1.4	Ω
Q_g	25	nC

TO-252 (DPAK)



Features

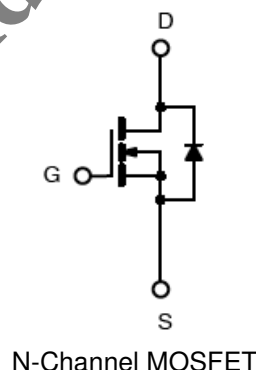
- Low $R_{DS(ON)}$ 1.4 Ω (Max.)
- Low gate charge typical @ 25nC (Typ.)
- Low C_{rss} typical @ 15pF (Typ.)
- Fast Switching

Ordering Information

Part No.	Package	Packing
TSM6N50CI C0G	ITO-220	50pcs / Tube
TSM6N50CP ROG	TO-252	2.5kpcs / 13" Reel
TSM6N50CH C5G	TO-251	75pcs / Tube

Note: "G" denotes for halogen- and Antimony-free or those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds

Block Diagram



N-Channel MOSFET

Absolute Maximum Ratings ($T_A=25^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V_{DS}	500	V	
Gate-Source Voltage	V_{GS}	± 30	V	
Continuous Drain Current	I_D	$T_A = 25^{\circ}C$	5.6	A
		$T_A = 100^{\circ}C$	3	A
Pulsed Drain Current ^(Note 1)	I_{DM}	15	A	
Single Pulse Avalanche Energy ^(Note 2)	E_{AS}	180	mJ	
Avalanche Current (Repetitive) ^(Note 3)	I_{AR}	5	A	
Total Power Dissipation @ $T_C = 25^{\circ}C$	P_{TOT}	ITO-220	25	W
		TO-252, TO-251	90	
Operating Junction Temperature	T_J	150	$^{\circ}C$	
Storage Temperature Range	T_{STG}	-55 to +150	$^{\circ}C$	



Thermal Performance

Parameter ^(Note 4)	Symbol	Limit	Unit	
Thermal Resistance - Junction to Case	R _{θJC}	ITO-220	5	°C/W
		TO-252, TO-251	2.78	
Thermal Resistance - Junction to Ambient	R _{θJA}	62.5	°C/W	

Electrical Specifications (T_J=25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	BV _{DSS}	500	--	--	V
Drain-Source On-State Resistance	V _{GS} = 10V, I _D = 2.8A	R _{DS(ON)}	--	1.15	1.4	Ω
Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	V _{GS(TH)}	2.0	--	4.0	V
Zero Gate Voltage Drain Current	V _{DS} = 500V, V _{GS} = 0V	I _{DSS}	--	--	1	μA
Gate Body Leakage	V _{GS} = ±20V, V _{DS} = 0V	I _{GSS}	--	--	±10	μA
Forward Transfer Conductance	V _{DS} = 8V, I _D = 1A	g _{fs}	--	2.6	--	S

Dynamic ^(Note 5,6)						
Total Gate Charge	V _{GS} = 10V, V _{DS} = 400V, I _D = 5A, V _{GS} = 10V	Q _g	--	25	33	nC
Gate-Source Charge		Q _{gs}	--	5	--	
Gate-Drain Charge		Q _{gd}	--	10	--	
Input Capacitance	V _{DS} = 25V, V _{GS} = 0V, f = 1.0 MHz	C _{iss}	--	680	900	pF
Output Capacitance		C _{oss}	--	85	110	
Reverse Transfer Capacitance		C _{rss}	--	15	20	

Switching ^(Note 5,6)						
Turn-On Delay Time	V _{GS} = 10V, I _D = 5A, V _{DD} = 250V, R _G = 25Ω	t _{d(on)}	--	20	50	ns
Turn-On Rise Time		t _r	--	40	90	
Turn-Off Delay Time		t _{d(off)}	--	90	190	
Turn-Off Fall Time		t _f	--	45	100	

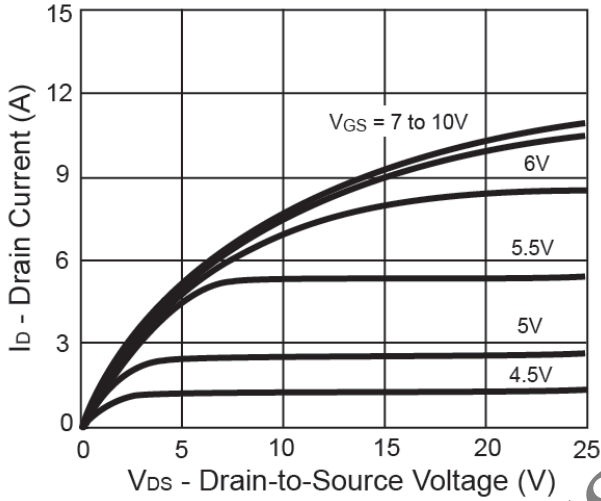
Source-Drain Diode Ratings and Characteristic						
Source Current	Integral reverse diode in the MOSFET	I _S	--	--	5	A
Source Current (Pulse)		I _{SM}	--	--	15	A
Diode Forward Voltage	I _S = 5A, V _{GS} = 0V	V _{SD}	--	--	1.6	V
Reverse Recovery Time	V _{GS} = 0V, I _S = 5A,	t _{fr}	--	430	--	ns
Reverse Recovery Charge	di _F /dt = 100A/μs	Q _{fr}	--	2	--	μC

Note:

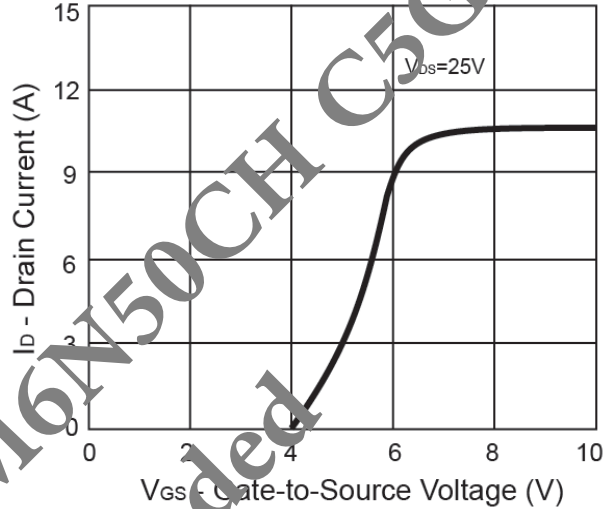
- Limited by maximum junction temperature
- V_{DD} = 50V, I_{AS} = 5A, L = 10mH, Starting T_J = 25°C
- Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
- Surface mounted on FR4 board t ≤ 10sec
- Pulse test: pulse width ≤ 300μs, duty cycle ≤ 2%
- Essentially Independent of Operating Temperature

Electrical Characteristics Curves

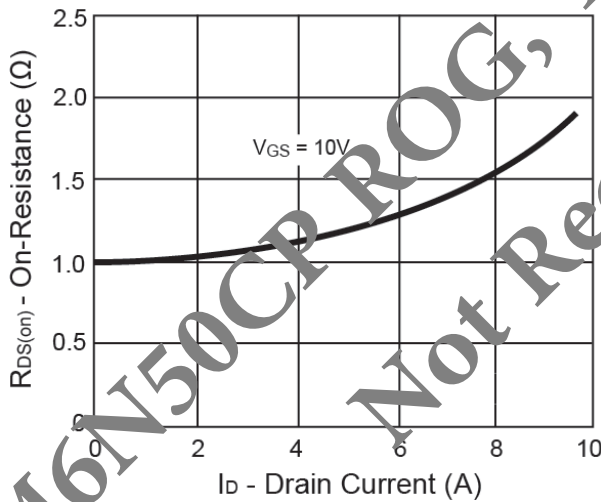
Output Characteristics



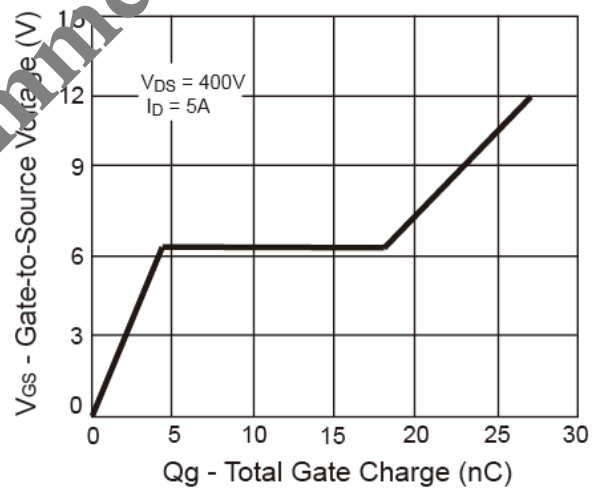
Transfer Characteristics



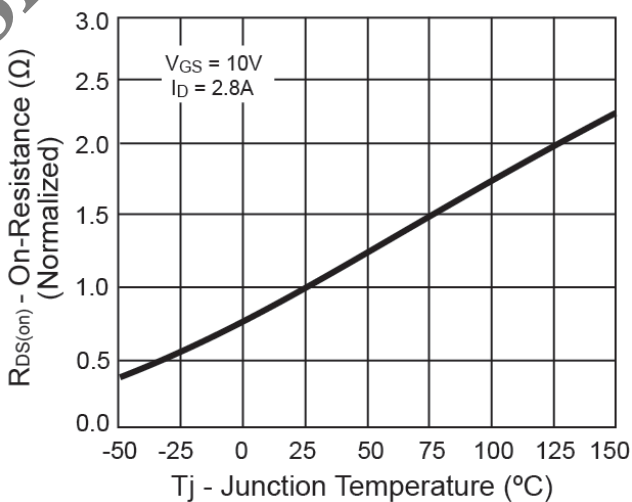
On-Resistance vs. Drain Current



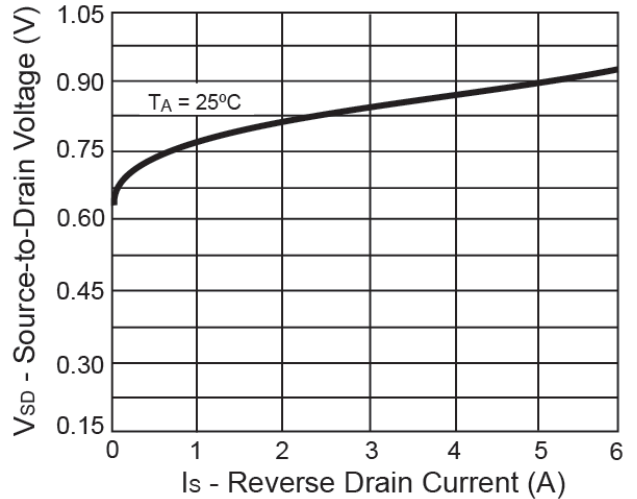
Gate Charge



On-Resistance vs. Junction Temperature

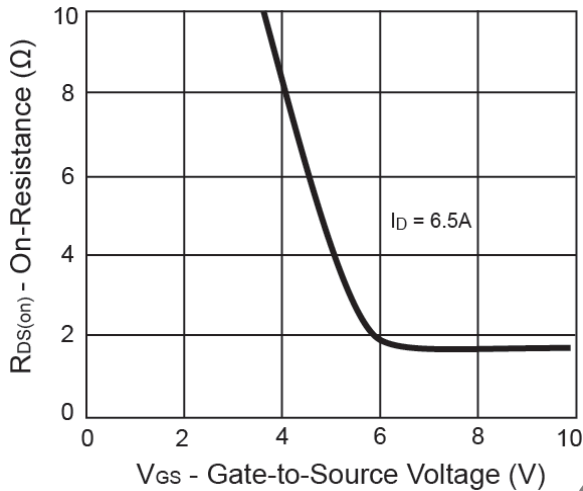


Source-Drain Diode Forward Voltage

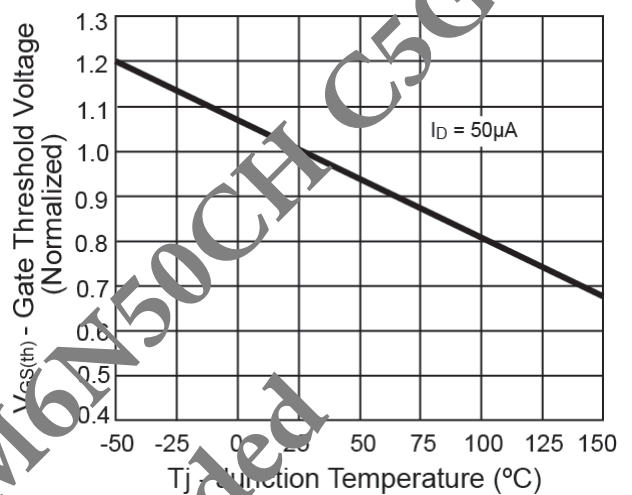


Electrical Characteristics Curves

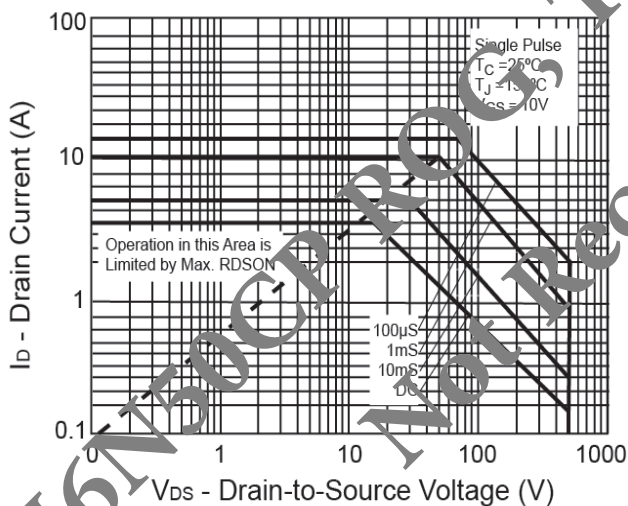
On-Resistance vs. Gate-Source Voltage



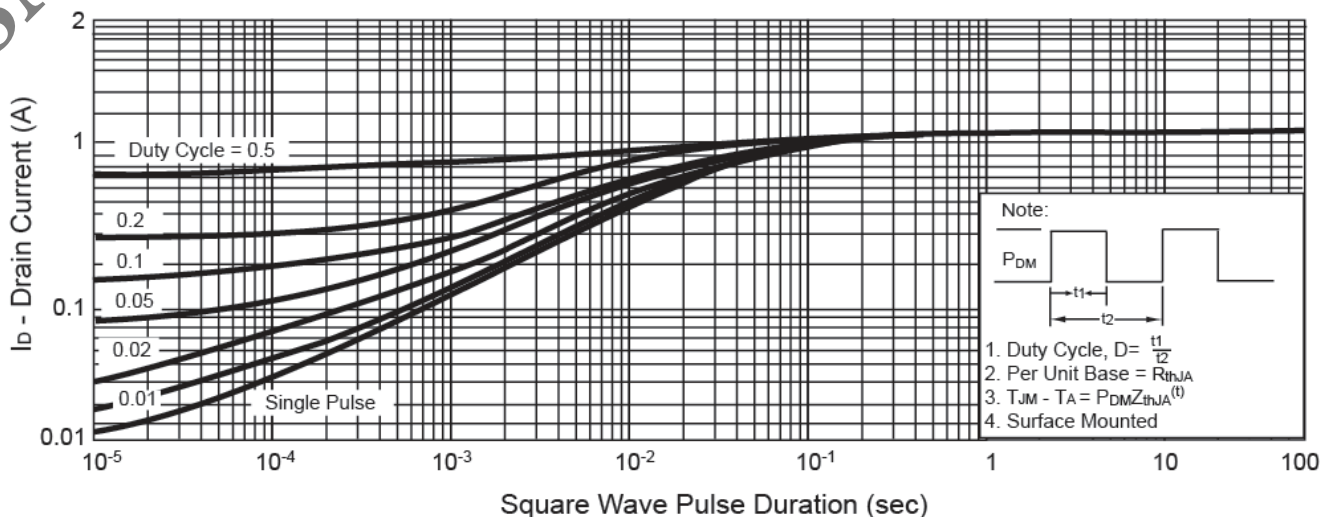
Threshold Voltage



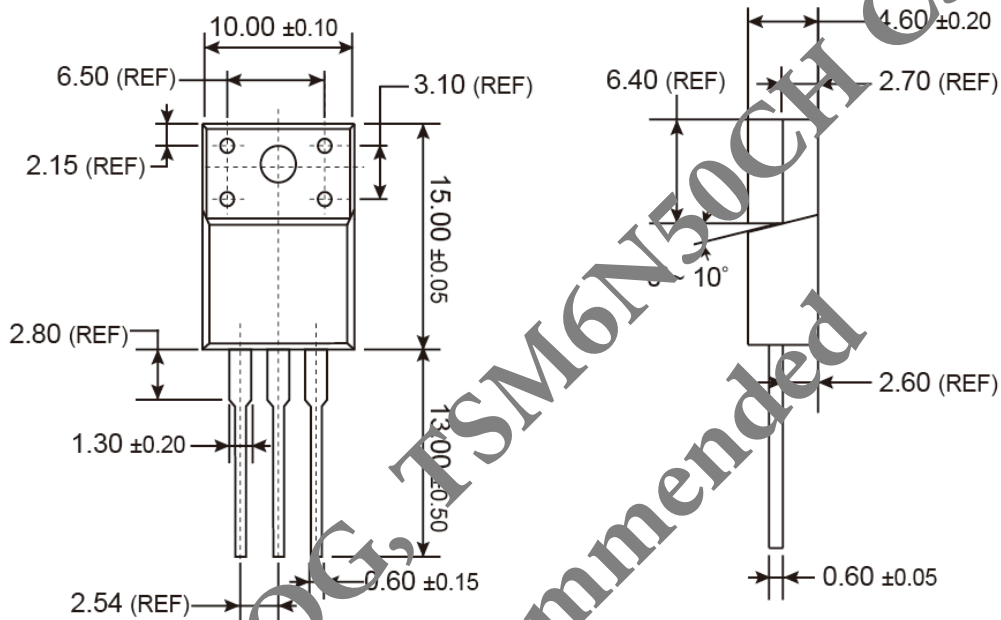
Maximum Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient

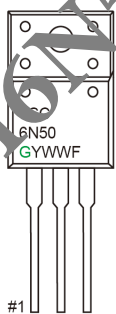


ITO-220 Mechanical Drawing



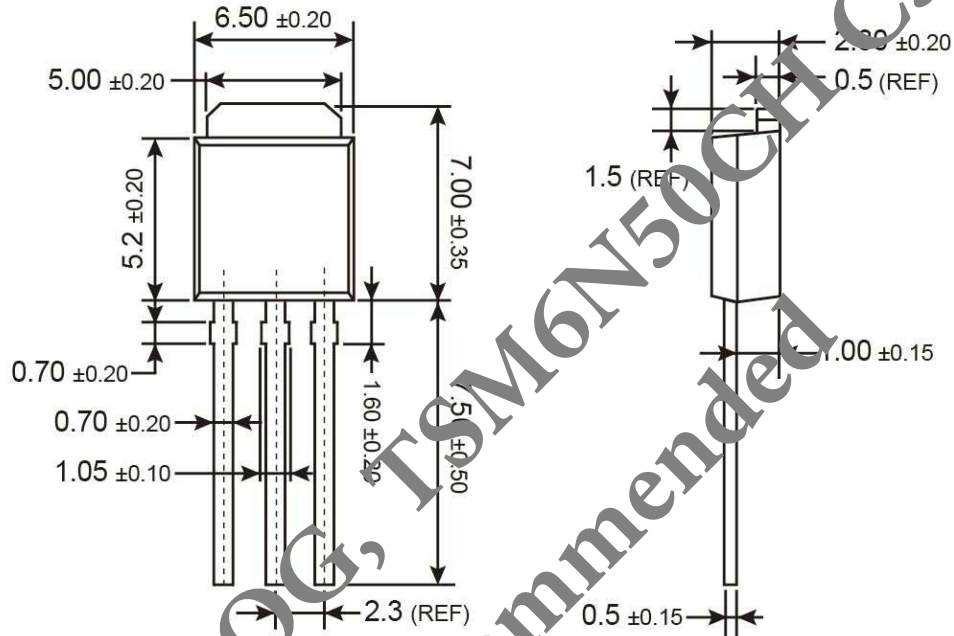
Unit: Millimeters

Marking Diagram



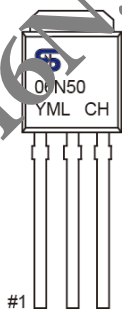
- G** = Halogen Free
- Y** = Year Code
- WW** = Week Code (01~52)
- F** = Factory Code

TO-251 (IPAK) Mechanical Drawing



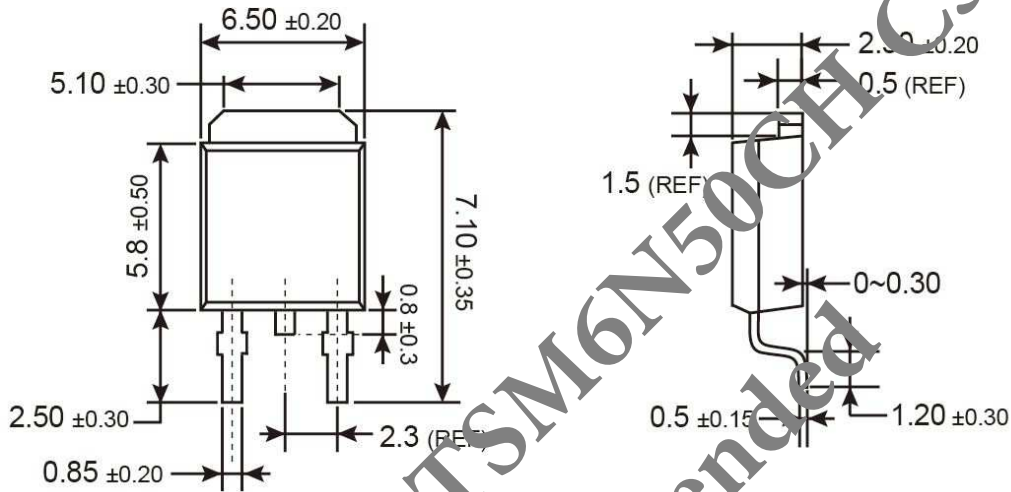
Unit: Millimeters

Marking Diagram



- Y = Year Code
- M = Month Code for Halogen Free Product
 - O =Jan P =Feb Q =Mar R =Apr
 - S =May T =Jun U =Jul V =Aug
 - W =Sep X =Oct Y =Nov Z =Dec
- L = Lot Code

TO-252 Mechanical Drawing



Unit: Millimeters

Marking Diagram



- Y** = Year Code
- M** = Month Code for Halogen Free Product
- O** = Jan **P** = Feb **Q** = Mar **R** = Apr
- S** = May **T** = Jun **U** = Jul **V** = Aug
- W** = Sep **X** = Oct **Y** = Nov **Z** = Dec
- L** = Lot Code

TSM6N50CP ROG, TSM6N50CH C5G
Not Recommended

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