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# **TSM900N06** 60V N-Channel Power MOSFET





TO-251S (IPAK SL)





TO-252

#### Pin Definition:

- 1. Gate
- 2. Drain 3. Source

#### **Key Parameter Performance**

Parameter		Value	Unit	
V <sub>D</sub>	S	60	V	
R <sub>DS(on)</sub> (max)	V <sub>GS</sub> = 10V	90	mΩ	
	$V_{GS} = 4.5V$	100		
$Q_g$		9.3	nC	

#### **SOT-223**

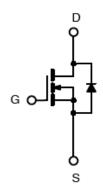


### **Ordering Information**

Part No.	Package	Packing
TSM900N06CH X0G	TO-251S	75pcs / Tube
TSM900N06CP ROG	TO-252	2.5kpcs / 13" Reel
TSM900N06CW RPG	SOT-223	2.5kpcs / 13" Reel
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**Note:** "G" denotes for Halogen- and Antimony-free as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds

#### **Block Diagram**



N-Channel MOSFET

#### **Absolute Maximum Ratings** (Tc = 25°C unless otherwise noted)

Parameter		Symbol				
			IPAK	DPAK	SOT-223	Unit
Drain-Source Voltage		V <sub>DS</sub>		V		
Gate-Source Voltage		$V_{GS}$		V		
Continuo Dunin Commant (Note 1	Tc=25°C	I <sub>D</sub>		Α		
Continuous Drain Current (Note 1)	Tc=100°C			Α		
Pulsed Drain Current (Note 1)		I <sub>DM</sub>		Α		
Single Pulse Avalanche Energy (Note 3)		E <sub>AS</sub>		mJ		
Single Pulse Avalanche Current (Note 3)		I <sub>AS</sub>	7			Α
Total Power @ T <sub>C</sub> =2	@ T <sub>C</sub> =25°C		25	25	4.17	W
Dissipation Derate above T <sub>C</sub> =25°C		$P_{D}$	0.2	0.2	0.014	W/°C
Operating Junction Temperature		TJ	150			°C
Storage Temperature Range		T <sub>STG</sub>	-55 to +150			°C







#### **Thermal Performance**

Peremeter	Symbol		Unit		
Parameter		IPAK	DPAK	SOT-223	Offic
Thermal Resistance - Junction to Case	R <sub>eJC</sub>	5	5	30	°C/W
Thermal Resistance - Junction to Ambient	$R_{\Theta JA}$	62	62	70	°C/W

**Electrical Specifications** (T<sub>C</sub> = 25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Тур	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	BV <sub>DSS</sub>	60			V
Drain Saurea On State Registeres	$V_{GS} = 10V, I_D = 6A$	0		76	90	mΩ
Drain-Source On-State Resistance	$V_{GS} = 4.5V, I_D = 3A$	$R_{DS(ON)}$		87	100	
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu A$	$V_{GS(TH)}$	1.2	1.8	2.5	V
7 0	$V_{DS} = 60V, V_{GS} = 0V$				1	μА
Zero Gate Voltage Drain Current	$V_{DS} = 48V, T_{J} = 125^{\circ}C$	I <sub>DSS</sub>			10	
Gate Body Leakage	$V_{GS} = \pm 20V, V_{DS} = 0V$	I <sub>GSS</sub>			±100	μΑ
Forward Transconductance	$V_{DS} = 10V, I_{D} = 3A$	g <sub>fs</sub>		4		S
Dynamic						
Total Gate Charge (Note 4,5)	V 40V 1 0A	$Q_g$		9.3		nC
Gate-Source Charge <sup>(Note 4,5)</sup>	$V_{DS} = 48V, I_D = 6A,$	$Q_{gs}$		2.1		
Gate-Drain Charge <sup>(Note 4,5)</sup>	$V_{GS} = 10V$	$Q_{gd}$		1.8		
Input Capacitance	\/ 45\/ \/ O\/	C <sub>iss</sub>		500		
Output Capacitance	$V_{DS} = 15V, V_{GS} = 0V,$ f = 1MHz	C <sub>oss</sub>		45		pF
Reverse Transfer Capacitance	I = IIVIMZ	$C_{rss}$		16		
Gate Resistance	$V_{GS}$ =0V, $V_{DS}$ =0V, f=1MHz	$R_{g}$		2		Ω
Switching						
Turn-On Delay Time <sup>(Note 4,5)</sup>		t <sub>d(on)</sub>		2.9		
Turn-On Rise Time <sup>(Note 4,5)</sup>	$V_{DD} = 30V , V_{GS} = 10V ,$	t <sub>r</sub>		9.5		
Turn-Off Delay Time <sup>(Note 4,5)</sup>	$R_G = 3.3\Omega$ , $I_D = -1A$	t <sub>d(off)</sub>		18.4		ns
Turn-Off Fall Time <sup>(Note 4,5)</sup>		t <sub>f</sub>		5.3		
Source-Drain Diode Ratings and 0	Characteristic					
Continuous Drain-Source Diode	V V 0V 5 0	I <sub>S</sub>			11	Α
Pulse Drain-Source Diode	$V_G = V_D = 0V$ , Force Current	I <sub>SM</sub>			44	Α
Diode-Source Forward Voltage	$V_{GS} = 0V, I_{S} = 1A$	$V_{SD}$			1.2	V
Reverse Recovery Time <sup>(Note 4)</sup>	$V_{GS} = 30V, I_{S} = 1A$	t <sub>rr</sub>		23.2		ns
Reverse Recovery Charge <sup>(Note 4)</sup>	$dI_F/dt = 100A/\mu s$	Q <sub>rr</sub>		14.3		nC

#### Note:

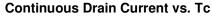
- 1. Limited by maximum junction temperature.
- 2. Repetitive Rating : Pulsed width limited by maximum junction temperature.
- 3.  $V_{DD}$ =25V, $V_{GS}$ =10V,L=1mH, $I_{AS}$ =7A., $R_G$ =25 $\Omega$ ,Starting  $T_J$ = 25°C
- 4. Pulse test: pulse width  $\leq 300 \mu s$ , duty cycle  $\leq 2\%$
- 5. Essentially independent of operating temperature.

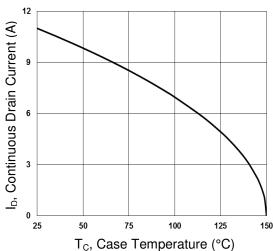


### 60V N-Channel Power MOSFET

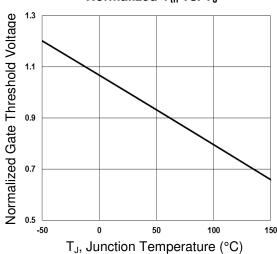
# Pb ROHS COMPLIANT

#### **Electrical Characteristics Curve**

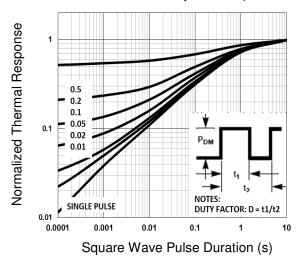




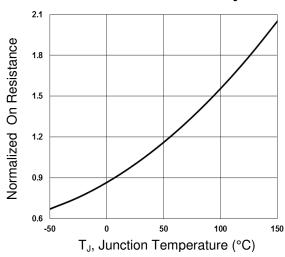
#### Normalized V<sub>th</sub> vs. T<sub>J</sub>



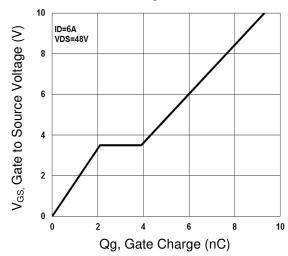
**Normalized Transient Impedance (TO-251S)** 



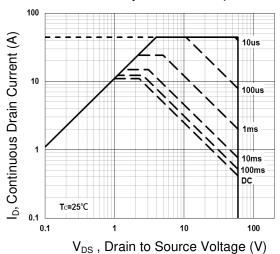
Normalized RDSON vs. T<sub>J</sub>



**Gate Charge Waveform** 



**Maximum Safe Operation Area (TO-251S)** 



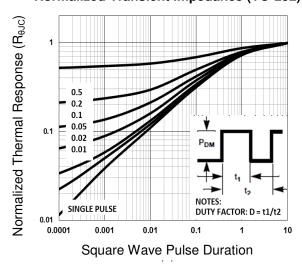


### 60V N-Channel Power MOSFET

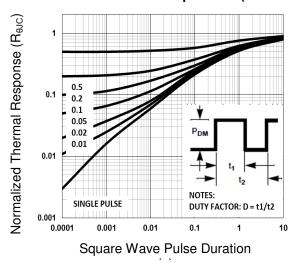


#### Electrical Characteristics Curve (Tc=25°C, unless otherwise noted)

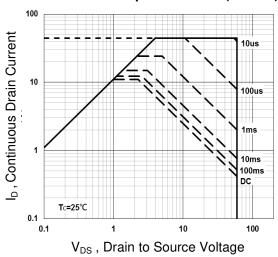
#### **Normalized Transient Impedance (TO-252)**



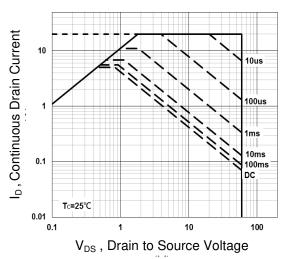
#### **Normalized Transient Impedance (SOT-223)**



#### Maximum Safe Operation Area (TO-252)



#### **Maximum Safe Operation Area (SOT-223)**

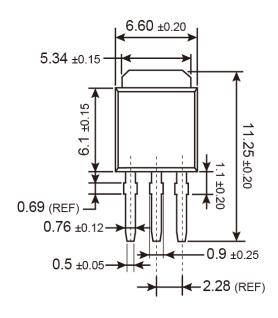


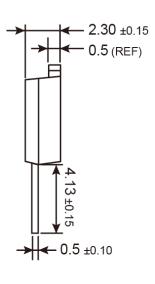


### 60V N-Channel Power MOSFET



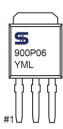
# **TO-251S Mechanical Drawing**





Unit: Millimeters

### **Marking Diagram**



Y = Year Code

M = Month Code for Halogen Free Product (O=Jan, P=Feb, Q=Mar, R=Apl, S=May, T=Jun, U=Jul, V=Aug, W=Sep, X=Oct, Y=Nov, Z=Dec)

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L = Lot Code

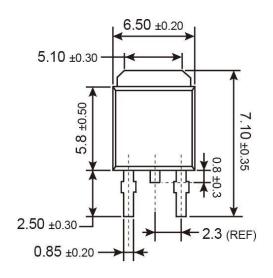
Version: C1612

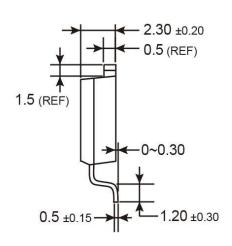


### 60V N-Channel Power MOSFET



# **TO-252 Mechanical Drawing**





Unit: Millimeters

### **Marking Diagram**



Y = Year Code

 $\label{eq:market} \begin{array}{ll} \textbf{M} &= \text{Month Code for Halogen Free Product} \\ &\quad (\textbf{O}=\text{Jan, P}=\text{Feb, Q}=\text{Mar, R}=\text{Apl, S}=\text{May, T}=\text{Jun, U}=\text{Jul, V}=\text{Aug, W}=\text{Sep,} \\ &\quad \textbf{X}=\text{Oct, Y}=\text{Nov, Z}=\text{Dec)} \end{array}$ 

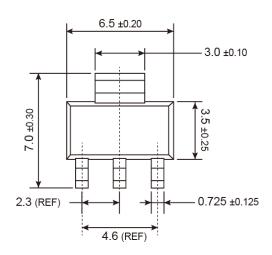
L = Lot Code

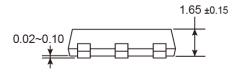


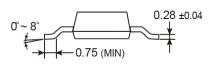
### 60V N-Channel Power MOSFET



# **SOT-223 Mechanical Drawing**







Unit: Millimeters

### **Marking Diagram**



Y = Year Code

M = Month Code for Halogen Free Product (O=Jan, P=Feb, Q=Mar, R=Apl, S=May, T=Jun, U=Jul, V=Aug, W=Sep, X=Oct, Y=Nov, Z=Dec)

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L = Lot Code

Version: C1612



# TSM900N06 60V N-Channel Power MOSFET

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