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With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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## P-Channel Power MOSFET

-30V, -5.3A, 60mΩ

### FEATURES

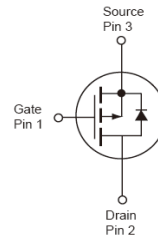
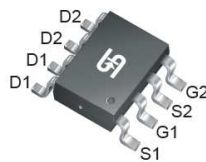
- Advance Trench Process Technology
- High Density Cell Design for Ultra Low On-resistance
- Compliant to RoHS Directive 2011/65/EU and WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21

### APPLICATION

- Load Switch
- PA Switch

### KEY PERFORMANCE PARAMETERS

PARAMETER	VALUE	UNIT
$V_{DS}$	-30	V
$R_{DS(on)}$ (max)	$V_{GS} = -10V$	60
	$V_{GS} = -4.5V$	90
$Q_g$	9.52	nC


**SOP-8**

**Notes:** MSL 3 (Moisture Sensitivity Level) per J-STD-020

### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current, $V_{GS} @ 4.5V$ (Note 1)	$I_D$	$T_C = 25^\circ\text{C}$	A
		$T_C = 100^\circ\text{C}$	
Pulsed Drain Current, $V_{GS} @ 4.5V$ (Note 2)	$I_{DM}$	-20	A
Total Power Dissipation @ $T_C = 25^\circ\text{C}$	$P_{DTOT}$	2.5	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	- 55 to +150	$^\circ\text{C}$

### THERMAL PERFORMANCE

PARAMETER	SYMBOL	LIMIT	UNIT
Junction to Case Thermal Resistance	$R_{\theta JC}$	30	$^\circ\text{C/W}$
Junction to Ambient Thermal Resistance	$R_{\theta JA}$	50	$^\circ\text{C/W}$

**Notes:**  $R_{\theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistances. The case thermal reference is defined at the solder mounting surface of the drain pins.  $R_{\theta JA}$  is guaranteed by design while  $R_{\theta CA}$  is determined by the user's board design.  $R_{\theta JA}$  shown below for single device operation on FR-4 PCB in still air.

**ELECTRICAL SPECIFICATIONS** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
Static <sup>(Note 3)</sup>						
Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA	BV <sub>DSS</sub>	-30	--	--	V
Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA	V <sub>GS(TH)</sub>	-1.0	-1.5	-3.0	V
Gate Body Leakage	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V	I <sub>GSS</sub>	--	--	±100	nA
Zero Gate Voltage Drain Current	V <sub>DS</sub> = -24V, V <sub>GS</sub> = 0V	I <sub>DSS</sub>	--	--	-1	μA
Drain-Source On-State Resistance	V <sub>GS</sub> = -10V, I <sub>D</sub> = -5.3A	R <sub>DS(ON)</sub>	--	50	60	mΩ
	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -4.2A		--	75	90	
Dynamic <sup>(Note 4)</sup>						
Total Gate Charge	V <sub>DS</sub> = -15V, I <sub>D</sub> = -5.3A, V <sub>GS</sub> = -10V	Q <sub>g</sub>	--	9.52	--	nC
Gate-Source Charge		Q <sub>gs</sub>	--	3.43	--	
Gate-Drain Charge		Q <sub>gd</sub>	--	1.71	--	
Input Capacitance	V <sub>DS</sub> = -15V, V <sub>GS</sub> = 0V, f = 1.0MHz	C <sub>iss</sub>	--	551.57	--	pF
Output Capacitance		C <sub>oss</sub>	--	90.96	--	
Reverse Transfer Capacitance		C <sub>rss</sub>	--	60.79	--	
Switching <sup>(Note 5)</sup>						
Turn-On Delay Time	V <sub>DD</sub> = -15V, R <sub>GEN</sub> = 6Ω, I <sub>D</sub> = -1A, V <sub>GS</sub> = -10V,	t <sub>d(on)</sub>	--	10.8	--	ns
Turn-On Rise Time		t <sub>r</sub>	--	2.33	--	
Turn-Off Delay Time		t <sub>d(off)</sub>	--	22.53	--	
Turn-Off Fall Time		t <sub>f</sub>	--	3.87	--	
Source-Drain Diode <sup>(Note 3)</sup>						
Forward On Voltage	I <sub>S</sub> = -1.9A, V <sub>GS</sub> = 0V	V <sub>SD</sub>	--	--	-1.3	V

**Notes:**

1. Current limited by package
2. Pulse width limited by the maximum junction temperature
3. Pulse test:  $PW \leq 300\mu s$ , duty cycle  $\leq 2\%$
4. For DESIGN AID ONLY, not subject to production testing.
5. Switching time is essentially independent of operating temperature.

**ORDERING INFORMATION**

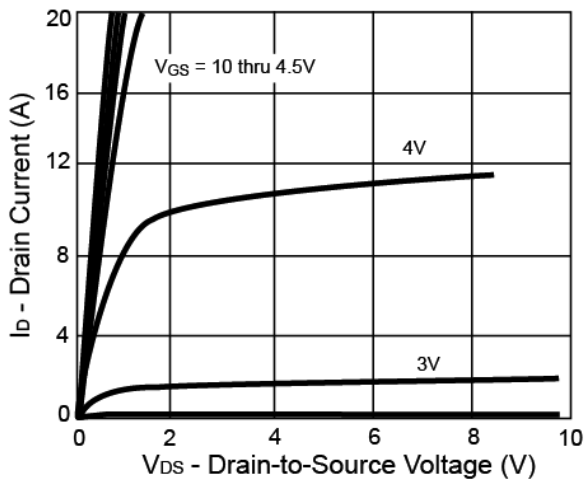
PART NO.	PACKAGE	PACKING
TSM9435CS RLG	SOP-8	2,500pcs / 13" Reel



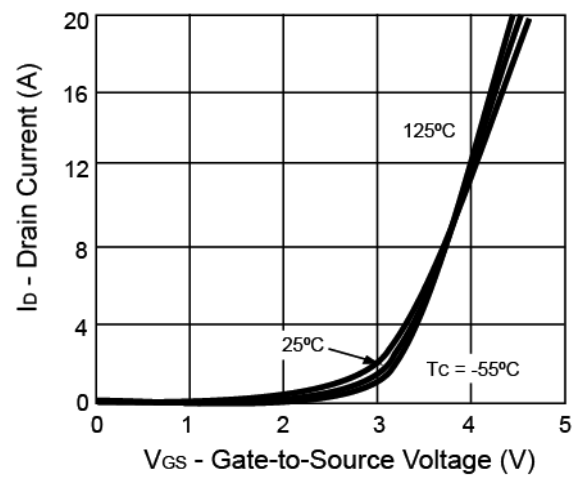
## CHARACTERISTICS CURVES

( $T_A = 25^\circ\text{C}$  unless otherwise noted)

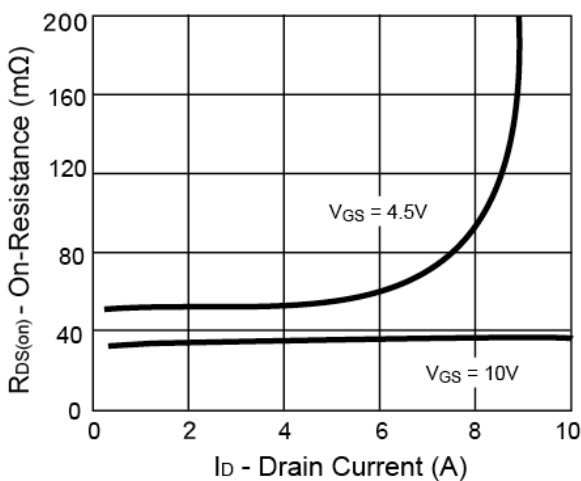
**Output Characteristics**



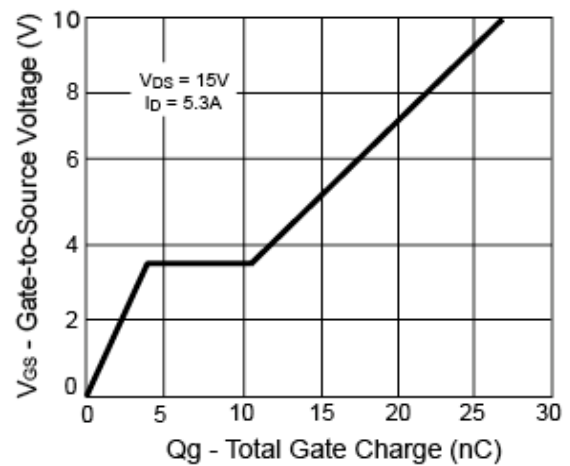
**Transfer Characteristics**



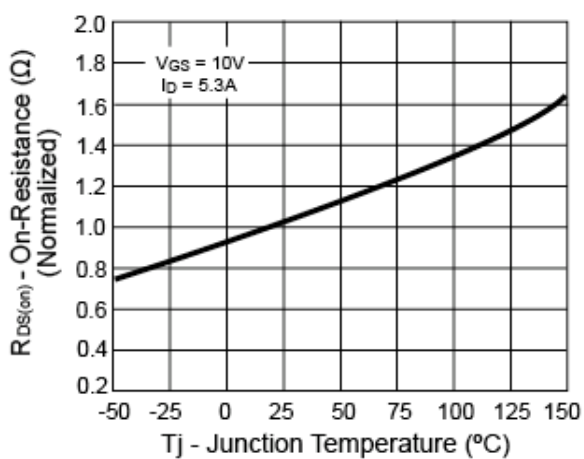
**On-Resistance vs. Drain Current**



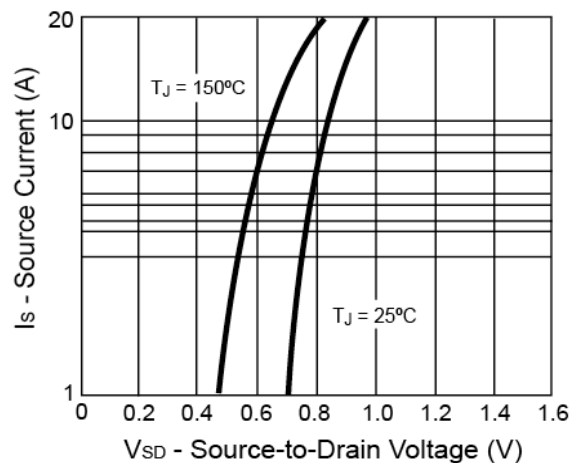
**Gate Charge**



**On-Resistance vs. Junction Temperature**



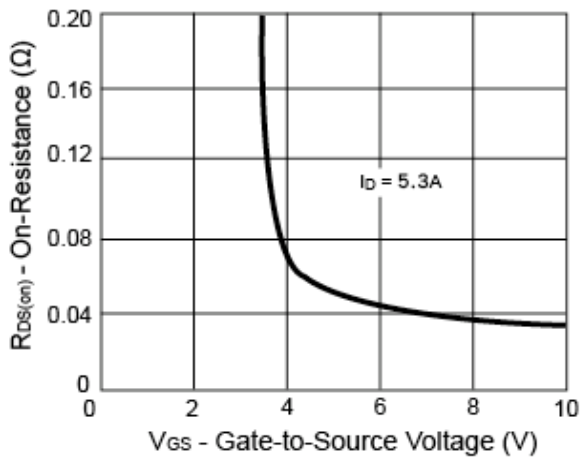
**Source-Drain Diode Forward Voltage**



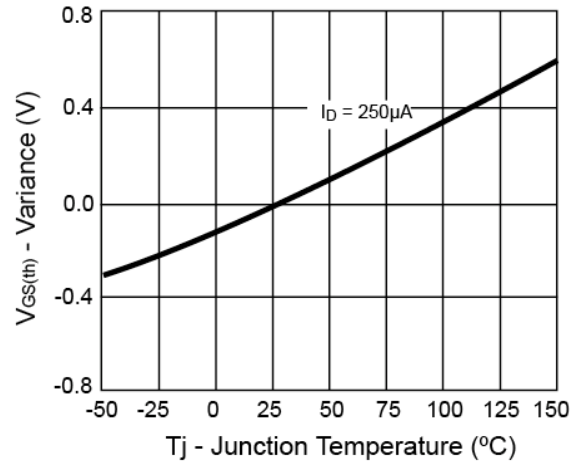
## CHARACTERISTICS CURVES

( $T_A = 25^\circ\text{C}$  unless otherwise noted)

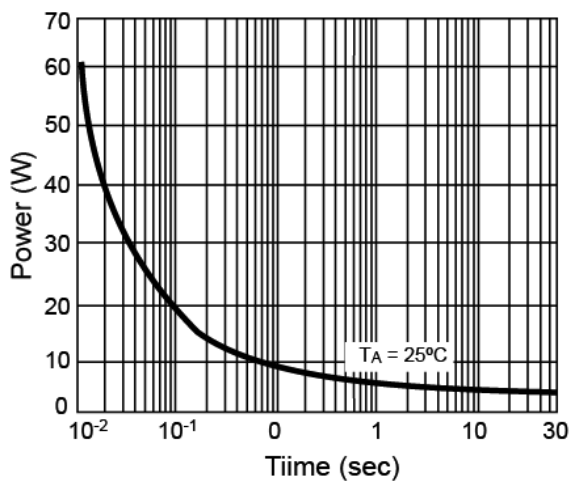
**On-Resistance vs. Gate-Source Voltage**



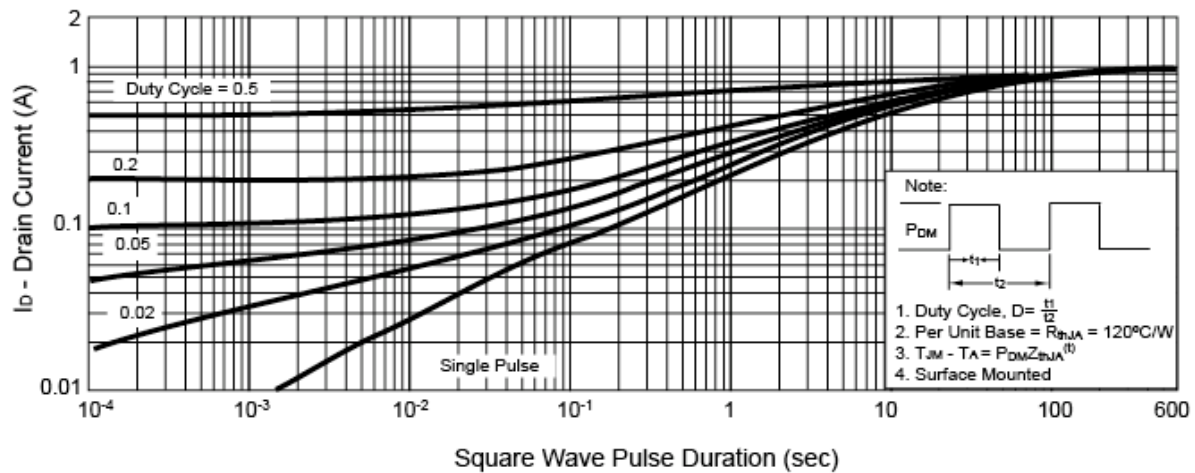
**Threshold Voltage**



**Single Pulse Power**

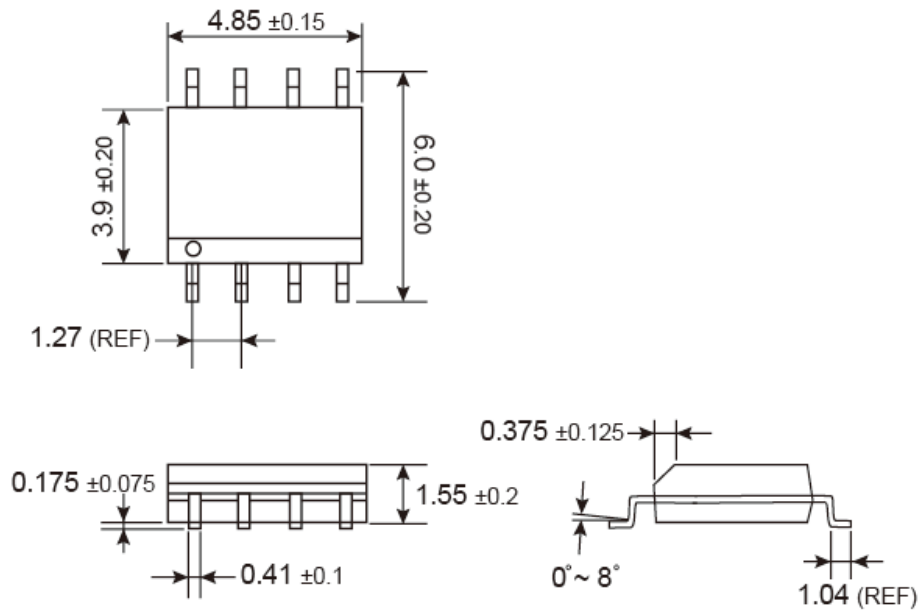


**Normalized Thermal Transient Impedance, Junction-to-Ambient**

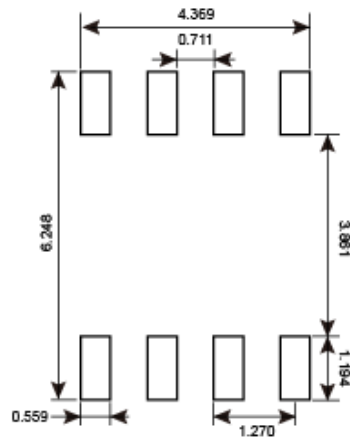


**PACKAGE OUTLINE DIMENSIONS** (Unit: Millimeters)

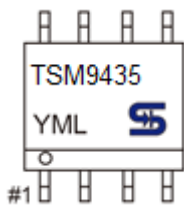
**SOP-8**



**SUGGESTED PAD LAYOUT** (Unit: Millimeters)



**MARKING DIAGRAM**



**Y** = Year Code

**M** = Month Code for Halogen Free Product

**O** =Jan **P** =Feb **Q** =Mar **R** =Apr

**S** =May **T** =Jun **U** =Jul **V** =Aug

**W** =Sep **X** =Oct **Y** =Nov **Z** =Dec

**L** = Lot Code (1~9, A~Z)

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