imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

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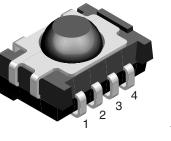


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TSOP352.., TSOP354..

Vishay Semiconductors

IR Receiver Modules for Remote Control Systems



16797

MECHANICAL DATA

Pinning: 1 = GND, 2 = N.C., 3 = V_S, 4 = OUT

FEATURES

- Very low supply current
- Photo detector and preamplifier in one package
- Internal filter for PCM frequency
- Supply voltage range: 2.5 V to 5.5 V
- Improved immunity against modulated light sources
- Insensitive to supply voltage ripple and noise
- Taping available for topview and sideview

HALOGEN FREE GREEN (5-2008)

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RoHS

COMPLIANT

 Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

assembly

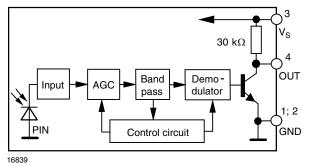
The TSOP352.., TSOP354.. series are miniaturized SMD IR receiver modules for infrared remote control systems. PIN diode and preamplifier are assembled on lead frame, the epoxy package is designed as IR filter.

The demodulated output signal can directly be decoded by a microprocessor. The TSOP352.. is compatible with all common IR remote control data formats. The TSOP354.. is optimized to suppress almost all spurious pulses from energy saving lamps but will also suppress some data signals.

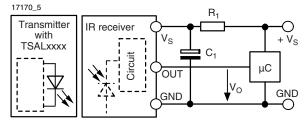
This component has not been qualified according to automotive specifications.

PARTS TABLE		
CARRIER FREQUENCY	STANDARD APPLICATIONS (AGC2/AGC8)	VERY NOISY ENVIRONMENTS (AGC4)
30 kHz	TSOP35230	TSOP35430
33 kHz	TSOP35233	TSOP35433
36 kHz	TSOP35236	TSOP35436
38 kHz	TSOP35238	TSOP35438
40 kHz	TSOP35240	TSOP35440
56 kHz	TSOP35256	TSOP35456

BLOCK DIAGRAM



APPLICATION CIRCUIT



 R_1 and C_1 are recommended for protection against EOS. Components should be in the range of 33 Ω < R_1 < 1 k Ω , C_1 > 0.1 $\mu F.$

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ABSOLUTE MAXIMUM RATINGS				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Supply voltage (pin 3)		V _S	- 0.3 to + 6	V
Supply current (pin 3)		I _S	3	mA
Output voltage (pin 4)		Vo	- 0.3 to (V _S + 0.3)	V
Output current (pin 4)		Ι _Ο	5	mA
Junction temperature		Tj	100	°C
Storage temperature range		T _{stg}	- 40 to + 100	°C
Operating temperature range		T _{amb}	- 30 to + 85	°C
Power consumption	$T_{amb} \le 85 \ ^{\circ}C$	P _{tot}	10	mW

Note

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only
and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification
is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability.

ELECTRICAL AND OPTICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply current (pin 3)	$E_v = 0, V_S = 3.3 V$	I _{SD}	0.27	0.35	0.45	mA
	E _v = 40 klx, sunlight	I _{SH}		0.45		mA
Supply voltage		VS	2.5		5.5	V
Transmission distance	$E_v = 0$, test signal see fig. 1, IR diode TSAL6200, $I_F = 250 \text{ mA}$	d		45		m
Output voltage low (pin 4)	$I_{OSL} = 0.5 \text{ mA}, E_e = 0.7 \text{ mW/m}^2,$ test signal see fig. 1	V _{OSL}			100	mV
Minimum irradiance	Pulse width tolerance: t _{pi} - 5/f _o < t _{po} < t _{pi} + 6/f _o , test signal see fig. 1	E _{e min.}		0.15	0.35	mW/m ²
Maximum irradiance	$\begin{array}{c} t_{pi} \text{ - } 5/f_o < t_{po} < t_{pi} + 6/f_o, \\ \text{test signal see fig. 1} \end{array}$	E _{e max.}	30			W/m ²
Directivity	Angle of half transmission distance	φ1/2		± 50		deg

TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

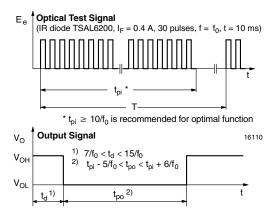
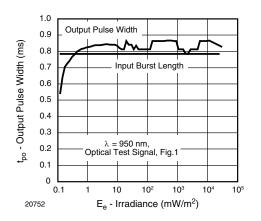


Fig. 1 - Output Active Low







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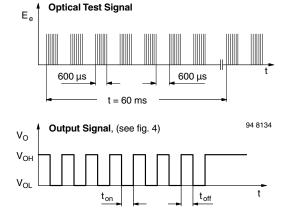


Fig. 3 - Output Function

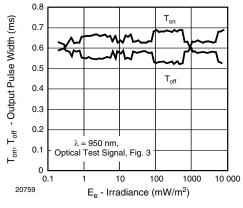


Fig. 4 - Output Pulse Diagram

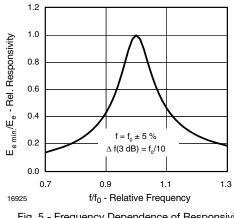


Fig. 5 - Frequency Dependence of Responsivity

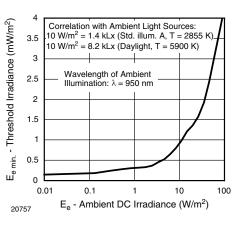
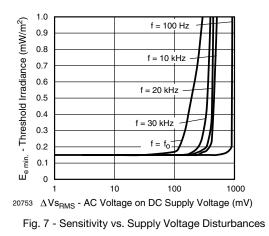
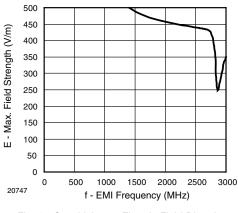


Fig. 6 - Sensitivity in Bright Ambient







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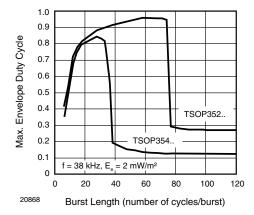


Fig. 9 - Maximum Envelope Duty Cycle vs. Burst Length

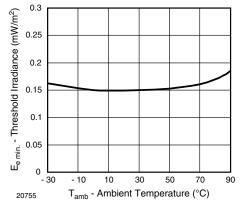


Fig. 10 - Sensitivity vs. Ambient Temperature

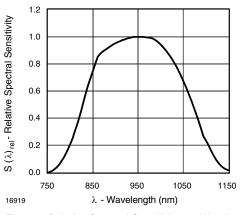
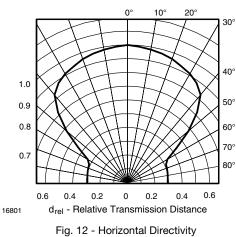
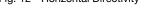


Fig. 11 - Relative Spectral Sensitivity vs. Wavelength





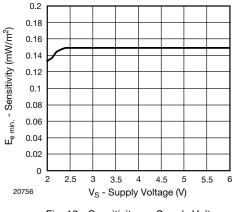


Fig. 13 - Sensitivity vs. Supply Voltage

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SUITABLE DATA FORMAT

The TSOP352.., TSOP354.. series is designed to suppress spurious output pulses due to noise or disturbance signals. Data and disturbance signals can be distinguished by the devices according to carrier frequency, burst length and envelope duty cycle. The data signal should be close to the band-pass center frequency (e.g. 38 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the TSOP352.., TSOP354.. in the presence of a disturbance signal, the sensitivity of the receiver is reduced to insure that no spurious pulses are present at the output. Some examples of disturbance signals which are suppressed are

- DC light (e.g. from tungsten bulb or sunlight)
- · Continuous signals at any frequency
- Strongly or weakly modulated noise from fluorescent lamps with electronic ballasts (see figure 14 or figure 15)

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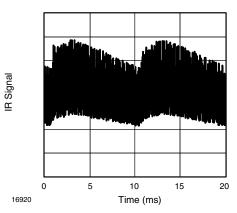


Fig. 14 - IR Signal from Fluorescent Lamp with Low Modulation

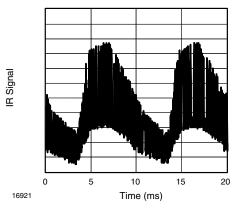


Fig. 15 - IR Signal from Fluorescent Lamp with High Modulation

	TSOP352	TSOP354
Minimum burst length	10 cycles/burst	10 cycles/burst
After each burst of length a minimum gap time is required of	10 to 70 cycles ≥ 10 cycles	10 to 35 cycles ≥ 10 cycles
For bursts greater than a minimum gap time in the data stream is needed of	70 cycles > 4 x burst length	35 cycles > 10 x burst length
Maximum number of continuous short bursts/second	1800	1500
Recommended for NEC code	yes	yes
Recommended for RC5/RC6 code	yes	yes
Recommended for Sony code	yes	no
Recommended for Thomson 56 kHz code	yes	yes
Recommended for Mitsubishi code (38 kHz, preburst 8 ms, 16 bit)	yes	no
Recommended for Sharp code	yes	yes
Suppression of interference from fluorescent lamps	Most common disturbance signals are suppressed	Even extreme disturbance signals are suppressed

Note

For data formats with short bursts please see the datasheet for TSOP351.., TSOP353..

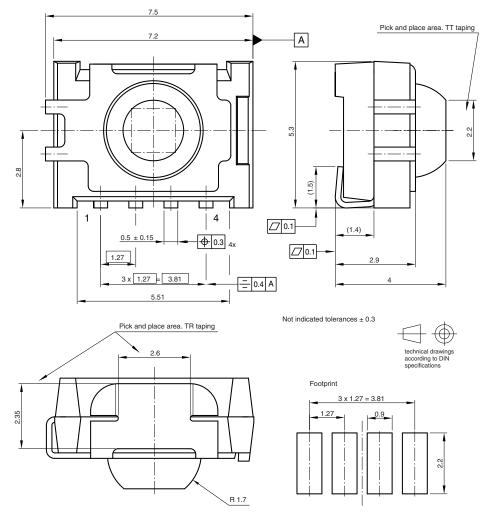


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PACKAGE DIMENSIONS in millimeters



Drawing-No.: 6.544-5341.01-4 Issue: 8; 02.09.09

ASSEMBLY INSTRUCTIONS

Reflow Soldering

- Reflow soldering must be done within 72 h while stored under a max. temperature of 30 °C, 60 % RH after opening the dry pack envelope
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown in the diagram. Excercise extreme care to keep the maximum temperature below 260 °C. The temperature shown in the profile means the temperature at the device surface. Since there is a temperature difference between the component and the circuit board, it should be verified that the temperature of the device is accurately being measured
- Handling after reflow should be done only after the work surface has been cooled off

Manual Soldering

• Use a soldering iron of 25 W or less. Adjust the

· Handle products only after the temperature has cooled off

temperature of the soldering iron below 300 °C

• Finish soldering within 3 s

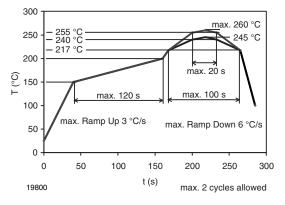
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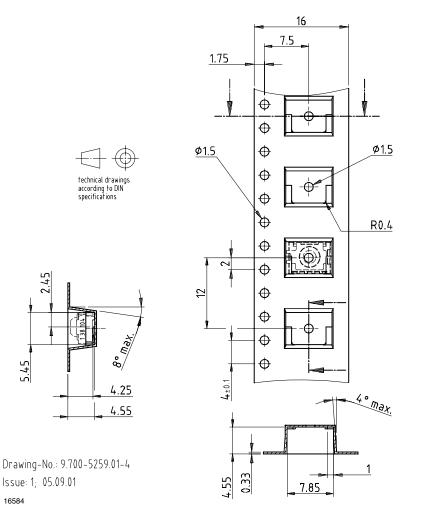
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VISHAY LEAD (Pb)-FREE REFLOW SOLDER PROFILE



TAPING VERSION TSOP..TT DIMENSIONS in millimeters



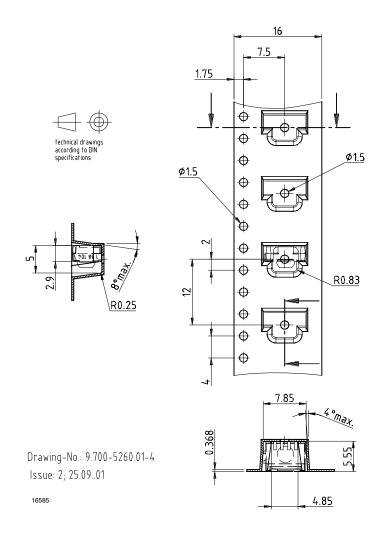


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TAPING VERSION TSOP..TR DIMENSIONS in millimeters



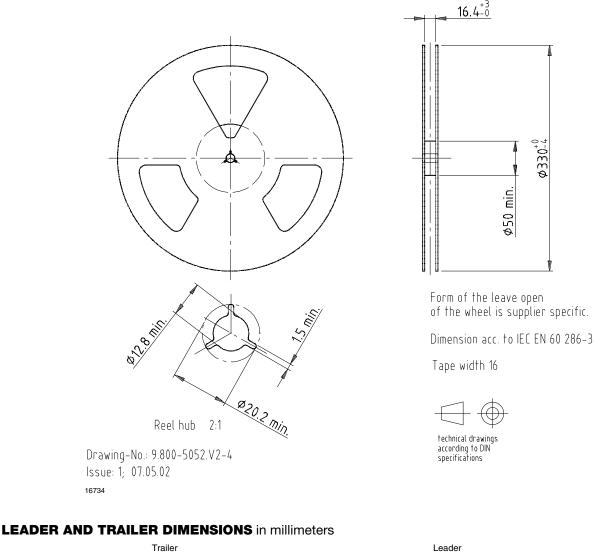


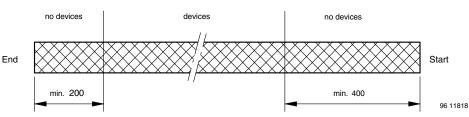
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REEL DIMENSIONS in millimeters





COVER TAPE PEEL STRENGTH

According to DIN EN 60286-3 0.1 N to 1.3 N 300 mm/min. ± 10 mm/min. 165° to 180° peel angle

LABEL

Standard bar code labels for finished goods

The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.



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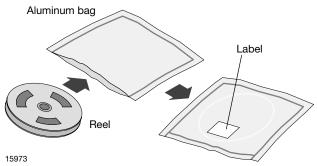
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VISHAY SEMICONDUCTOR Gr	nbH STANDARD BAR CODE PRO	DUCT LABEL (finished goods)
PLAIN WRITTING	ABBREVIATION	LENGTH
Item-description	-	18
Item-number	INO	8
Selection-code	SEL	3
LOT-/serial-number	BATCH	10
Data-code	COD	3 (YWW)
Plant-code	PTC	2
Quantity	QTY	8
Accepted by	ACC	-
Packed by	PCK	-
Mixed code indicator	MIXED CODE	-
Origin	XXXXXXX+	Company logo
LONG BAR CODE TOP	ТҮРЕ	LENGTH
Item-number	Ν	8
Plant-code	Ν	2
Sequence-number	Х	3
Quantity	Ν	8
Total length	-	21
SHORT BAR CODE BOTTOM	ТҮРЕ	LENGTH
Selection-code	Х	3
Data-code	Ν	3
Batch-number	Х	10
Filter	-	1
Total length	-	17

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity \leq 60 % RH max.

After more than 72 h under these conditions moisture content will be too high for reflow soldering.

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In case of moisture absorption, the devices will recover to the former condition by drying under the following condition: 192 h at 40 °C + 5 °C/- 0 °C and < 5 % RH (dry air/nitrogen) or

96 h at 60 $^\circ\text{C}$ + 5 $^\circ\text{C}$ and < 5 % RH for all device containers or

24 h at 125 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC[®] standard JSTD-020 level 4 label is included on all dry bags.

CAUTION This bag contains MOISTURE-SENSITIVE DEVICES
1. Shelf life in sealed bag: 12 months at < 40 $^\circ\text{C}$ and < 90 $\%$ relative humidity (RH)
 After this bag is opened, devices that will be subjected to soldering reflow or equivalent processing (peak package body temp. 260 °C) must be Mounted within 72 hours at factory condition of < 30 °C/60 % RH or 2b. Stored at < 5 % RH
3. Devices require baking befor mounting if: Humidity Indicator Card is > 10 % when read at 23 $^\circ$ C \pm 5 $^\circ$ C or 2a. or 2b. are not met.
4. If baking is required, devices may be baked for: 192 hours at 40 °C + 5 °C/- 0 °C and < 5 % RH (dry air/nitrogen) or 96 hours at 60 °C ± 5 °C and < 5 % RH for all device containers or 24 hours at 125 °C ± 5 °C not suitable for reels or tubes
Bag Seal Date:
(If blank, see barcode label)
Note: Level and body temperature defined by EIA JEDEC Standard JSTD-020
2522

EIA JEDEC standard JSTD-020 level 4 label is included on all dry bags

Document Number: 81781



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ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electro-static sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.





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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.