imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



Vishay Semiconductors

IR Receiver Modules for Remote Control Systems



- Very low supply current
- Photo detectors and preamplifier in one package
- Internal filter for PCM frequency
- Supply voltage: 2.5 V to 5.5 V
- Improved immunity against ambient light
- Insensitive to supply voltage ripple and noise
- Material categorization:

for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

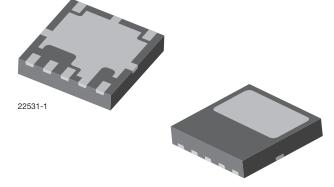
The TSOP373.., TSOP375.. series are miniaturized receiver modules for infrared remote control systems. A PIN diode and a preamplifier are assembled on a PCB, the epoxy package contains an IR filter. The demodulated output signal can be directly connected to a microprocessor for decoding.

The TSOP373.. series devices are optimized to suppress almost all spurious pulses from energy saving lamps like CFLs. AGC3 may also suppress some data signals if continuously transmitted.

New designs should prefer the TSOP373.. series containing the newer AGC3. The TSOP375.. series contain a very robust AGC5. This series should only be used for critically noisy environments.

These components have not been qualified according to automotive specifications.

PARTS T	ABLE			
AGC		NOISY ENVIRONMENTS AND SHORT BURSTS (AGC3)	VERY NOISY ENVIRONMENTS AND SHORT BURSTS (AGC5)	
	36 kHz	TSOP37336 ⁽¹⁾	TSOP37536	
Carrier	38 kHz	TSOP37338 ⁽²⁾⁽³⁾⁽⁴⁾⁽⁵⁾	TSOP37538	
frequency	40 kHz	TSOP37340	TSOP37540	
	56 kHz	TSOP37356	TSOP37556	
Package		Belo	bog	
Pinning		1 = OUT, 2, 3, 6, 7, 8 = GND, 4, 5 = V _S		
Dimensions (mm)		3.95 W x 3.95 H x 0.8 D		
Mounting		SMD		
Application		Remote control		
Best remote control code		⁽¹⁾ MCIR ⁽²⁾ Mitsubishi ⁽³⁾ RECS-80 Code ⁽⁴⁾ r-map ⁽⁵⁾ XMP-1, XMP-2		



ORDERING CODE

Taping:

TSOP37...TT1 - top view taped TSOP37...TT2 - top view taped



RoHS

COMPLIANT

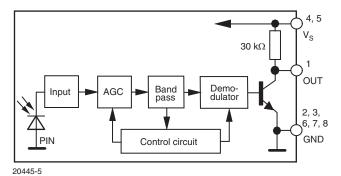
HALOGEN



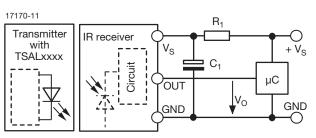


Vishay Semiconductors

BLOCK DIAGRAM



APPLICATION CIRCUIT



 $R_{\rm 1}$ and $C_{\rm 1}$ recommended to reduce supply ripple for $V_{\rm S}$ < 2.8 V

ABSOLUTE MAXIMUM R	DLUTE MAXIMUM RATINGS			
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Supply voltage		V _S	-0.3 to +6	V
Supply current		I _S	3	mA
Output voltage		Vo	-0.3 to (V _S + 0.3)	V
Output current		Ι _Ο	5	mA
Junction temperature		Tj	100	°C
Storage temperature range		T _{stg}	-25 to +85	°C
Operating temperature range		T _{amb}	-25 to +85	°C
Power consumption	$T_{amb} \le 85 \ ^{\circ}C$	P _{tot}	10	mW

Note

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only
and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification
is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability.

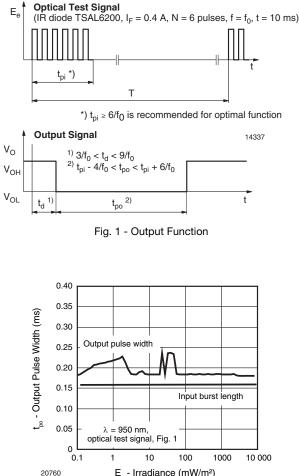
ELECTRICAL AND OPT	ICAL CHARACTERISTICS	(T _{amb} = 25 °	°C, unless o	otherwise s	oecified)	
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply voltage		Vs	2.5	-	5.5	V
Supply current	$V_{S} = 3.3 V, E_{v} = 0$	I _{SD}	0.27	0.35	0.45	mA
Supply current	E _v = 40 klx, sunlight	I _{SH}	-	0.45	-	mA
Transmission distance	$E_v = 0$, IR diode TSAL6200, $I_F = 200$ mA, test signal see Fig. 1	d	-	45	-	m
Output voltage low	$I_{OSL} = 0.5 \text{ mA}, E_e = 0.7 \text{ mW/m}^2,$ test signal see Fig. 1	V _{OSL}	-	-	100	mV
Minimum irradiance	Pulse width tolerance: $ t_{pi} - 5/f_o < t_{po} < t_{pi} + 6/f_{o,} $ test signal see Fig. 1	E _{e min.}	-	0.12	0.25	mW/m ²
Maximum irradiance	t _{pi} - 5/f _o < t _{po} < t _{pi} + 6/f _o , test signal see Fig. 1	E _{e max.}	30	-	-	W/m ²
Directivity	Angle of half transmission distance	φ1/2	-	± 75	-	deg

TSOP373.., TSOP375..



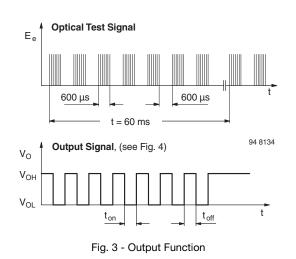


TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)



E - Irradiance (mW/m²)

Fig. 2 - Output Pulse Width vs. Irradiance



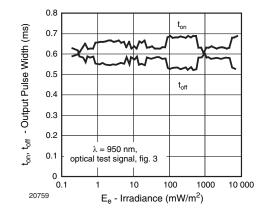


Fig. 4 - Output Pulse Diagram

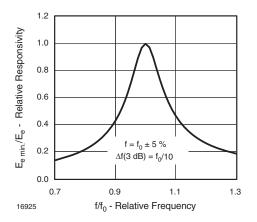


Fig. 5 - Frequency Dependance of Responsivity

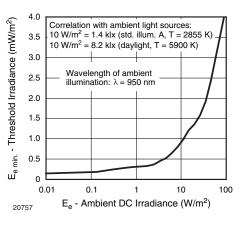


Fig. 6 - Sensitivity in Bright Ambient

Rev. 1.8, 12-Dec-16

3



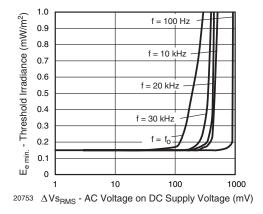


Fig. 7 - Sensitivity vs. Supply Voltage Disturbances

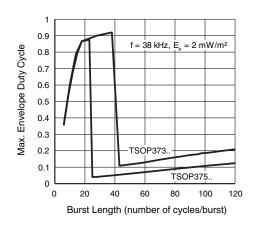


Fig. 8 - Max. Envelope Duty Cycle vs. Burst Length

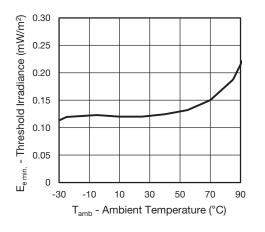


Fig. 9 - Sensitivity vs. Ambient Temperature

Vishay Semiconductors

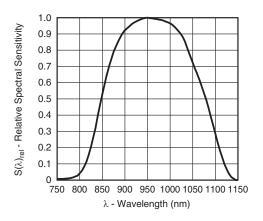


Fig. 10 - Relative Spectral Sensitivity vs. Wavelength

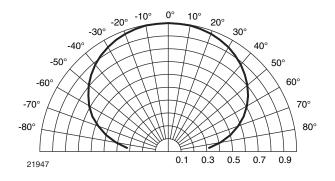


Fig. 11 - Directivity

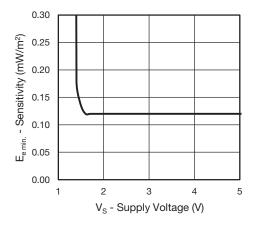


Fig. 12 - Sensitivity vs. Supply Voltage

Rev. 1.8, 12-Dec-16

4



SUITABLE DATA FORMAT

The TSOP373.., TSOP375.. series is designed to suppress spurious output pulses due to noise or disturbance signals. The devices can distinguish data signals from noise due to differences in frequency, burst length, and envelope duty cycle. The data signal should be close to the device's band-pass center frequency (e.g. 38 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the TSOP373.., TSOP375.. in the presence of a disturbance, the sensitivity of the receiver is automatically reduced by the AGC to insure that no spurious pulses are present at the receiver's output. Some examples which are suppressed are:

- DC light (e.g. from tungsten bulbs sunlight)
- · Continuous signals at any frequency
- Strongly or weakly modulated patterns from fluorescent lamps with electronic ballasts (see Fig. 13 or Fig. 14)

Vishay Semiconductors

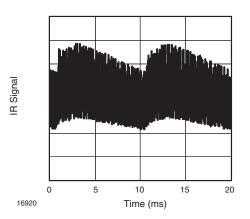


Fig. 13 - IR Signal from Fluorescent Lamp with Low Modulation

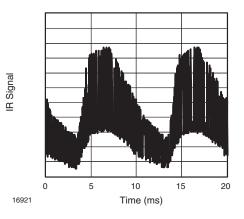


Fig. 14 - IR Signal from Fluorescent Lamp with High Modulation

	TSOP373	TSOP375
Minimum burst length	6 cycles/burst	6 cycles/burst
After each burst of length a minimum gap time is required of	6 to 35 cycles ≥ 10 cycles	6 to 24 cycles ≥ 10 cycles
For bursts greater than a minimum gap time in the data stream is needed of	35 cycles > 6 x burst length	24 cycles > 25 ms
Maximum number of continuous short bursts/second	2000	2000
MCIR code	Preferred	Yes
RCMM code	Preferred	Yes
XMP-1, XMP-2 code	Preferred	Yes
Suppression of interference from fluorescent lamps	Mild and complex disturbance patterns are suppressed (example: signal pattern of Fig. 13 and 14)	Critical disturbance patterns are suppressed, e.g. highly dimmed LCDs

Note

• For data formats with long bursts (more than 10 carrier cycles) please see the datasheet for TSOP372.., TSOP374...



PACKAGE DIMENSIONS in millimeters

(0.3)

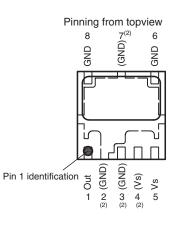
 0.75 ± 0.05

TSOP373.., TSOP375..

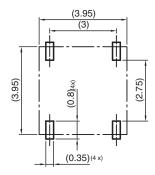
Vishay Semiconductors

Not indicated tolerances ± 0.1

technical drawings according to DIN specifications



Proposed pad layout from component side (dim. for reference only)

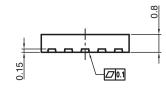


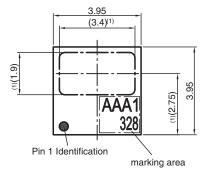
(0.95) 0.55 tt (1.8) (0.4) $\overset{(8 \times)}{0.35 \pm 0.05}$ 0.55 1.5 2

 $4 \times 0.75 = 3$

(0.3)

0.475



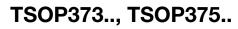


Drawing-No.: 6.550-5315.01-4 Issue: 2; 12.02.14

Notes

⁽¹⁾ Optically effective area

⁽²⁾ Pins connected internally. It is not necessary to connect externally.



• Use a soldering iron of 25 W or less. Adjust the

· Handle products only after the temperature has cooled off

temperature of the soldering iron below 300 °C

Vishay Semiconductors



ASSEMBLY INSTRUCTIONS

Reflow Soldering

- Reflow soldering must be done within 168 h while stored under a max. temperature of 30 °C, 60 % RH after opening the dry pack envelope
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown in the diagram. Exercise extreme care to keep the maximum temperature below 260 °C. The temperature shown in the profile means the temperature at the device surface. Since there is a temperature difference between the component and the circuit board, it should be verified that the temperature of the device is accurately being measured
- Handling after reflow should be done only after the work surface has been cooled off

VISHAY LEAD (Pb)-FREE REFLOW SOLDER PROFILE

300 <u>M</u>ax. 260 °C 255 240 250 245 °C ٥Č 217 °C 200 Max 20 s T (°C) 150 <u>Max. 1</u>00 s Max. 120 s 100 Max. ramp up 3 °C/s Max. ramp down 6 °C/s 50 0 100 0 50 150 200 250 300 t (s) 19800 Max. 2 cycles allowed

Manual Soldering

• Finish soldering within 3 s

ORDERING INFORMATION			
ORDERING CODE	PACKAGING	VOLUME ⁽¹⁾	REMARKS
TSOP37TT1	Tapa and real	MOQ: 1800 pcs	3.95 mm x 3.95 mm x 0.75 mm
TSOP37TT2	Tape and reel	MOQ: 7000 pcs	3.95 mm x 3.95 mm x 0.75 mm

Note

(1) MOQ: minimum order quantity

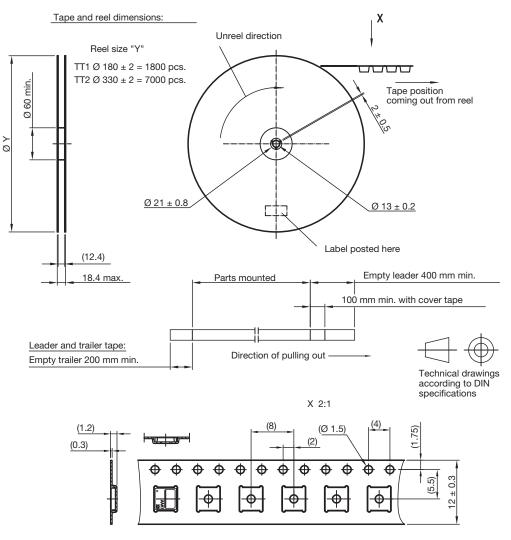
TSOP373.., TSOP375..



TAPING VERSION TSOP37... DIMENSIONS in millimeters

www.vishay.com

VISHAY



Drawing-No.: 9.700-5347.01-4 Issue: 1; 14.11.11 Not indicated tolerances ± 0.1

TSOP373.., TSOP375..





LABEL

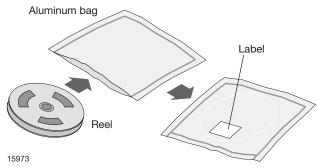
Standard bar code labels for finished goods

The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

PLAIN WRITING		LENGTH
	ABBREVIATION	
Item-description	-	18
Item-number	INO	8
Selection-code	SEL	3
LOT-/serial-number	BATCH	10
Data-code	COD	3 (YWW)
Plant-code	PTC	2
Quantity	QTY	8
Accepted by	ACC	-
Packed by	РСК	-
Mixed code indicator	MIXED CODE	-
Origin	XXXXXXX+	Company logo
Long bar code top	Туре	Length
Item-number	Ν	8
Plant-code	Ν	2
Sequence-number	Х	3
Quantity	Ν	8
Total length	-	21
Short bar code bottom	Туре	Length
Selection-code	Х	3
Data-code	Ν	3
Batch-number	Х	10
Filter	-	1
Total length	-	17

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity \leq 60 % RH max.

After more than 168 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition: 192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen)

or 96 h at 60 $^{\circ}\text{C}$ + 5 $^{\circ}\text{C}$ and < 5 % RH for all device containers or

24 h at 125 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC[®] standard J-STD-020 level 3 label is included on all dry bags.

Rev. 1.8, 12-Dec-16

9



Caution

This bag contains MOISTURE-SENSITIVE DEVICES

3. After bag is opened, devices that will be subjected to reflow solder or other high temperature process must be

a) Humidity Indicator Card reads > 10% for level 2a - 5a devices or >60% for

a) Mounted within: $\frac{168}{^{1 f blank, see adjacent bar code label}} hours of factory conditions} \leq 30^{\circ} C/60\% \ RH, or$

5. If baking is required, refer to IPC/JEDEC J-STD-033 for bake procedure

If blank, see adjacent bar code label Note: Level and body temperature defined by IPC/JEDEC J-STD-020

260

°C

1. Calculated shelf life in sealed bag: 12 months at <40°C and <90%

relative humidity (RH)

b) Stored per J-STD-0334. Devices require bake, before mounting, if:

b) 3a or 3b are not met

Bag Seal Date:

vel 2 devices when read at 23±5°C

2. Peak package body temperature: 20

3

Vishay Semiconductors

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

BAR CODE PRODUCT LABEL (example)



22178

22650

EIA JEDC standard J-STD-020 level 3 label is included on all dry bags

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.



Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.