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RoHS

COMPLIANT

GREEN

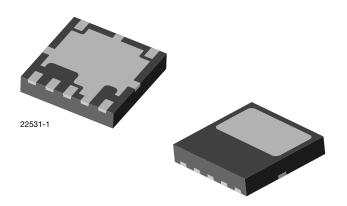
(5-2008)



www.vishay.com

Vishay Semiconductors

IR Receiver Modules for Remote Control Systems



FEATURES

- · Height of 0.8 mm
- ± 75° half angle sensitivity
- · Low supply current
- Photo detectors and preamplifier in one package
- Internal filter for PCM frequency
- Supply voltage: 2.5 V to 5.5 V, typically even 2.0 V to 5.5 V is possible
- Improved immunity against ambient light
- · Insensitive to supply voltage ripple and noise
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

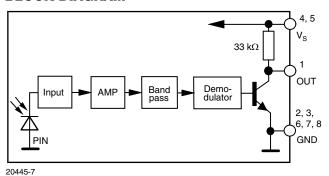
DESCRIPTION

The TSSP57038 is a compact IR receiver for sensor applications. It has a high gain for IR signals at 38 kHz. The detection level does not change when ambient light or strong IR signals are applied. It can receive continuous 38 kHz signals or 38 kHz bursts.

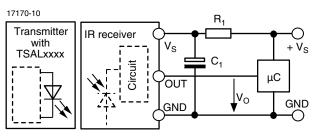
This component has not been qualified according to automotive specifications.

PARTS TABLE	
CARRIER FREQUENCY	SENSOR APPLICATIONS
38 kHz	TSSP57038

BLOCK DIAGRAM



APPLICATION CIRCUIT



The external components R_1 and C_1 are optional to improve the robustness against electrical overstress (typical values are R_1 = 100 Ω , C_1 = 0.1 μ F).



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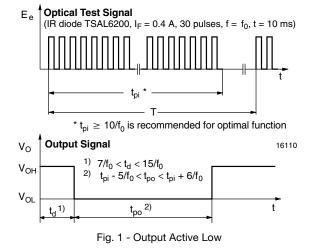
ABSOLUTE MAXIMUM R	ATINGS			
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Supply voltage		Vs	- 0.3 to + 6	V
Supply current		I _S	5	mA
Output voltage		Vo	- 0.3 to (V _S + 0.3)	V
Output current		Io	5	mA
Junction temperature		T _j	100	°C
Storage temperature range		T _{stg}	- 25 to + 85	°C
Operating temperature range		T _{amb}	- 25 to + 85	°C
Power consumption	T _{amb} ≤ 85 °C	P _{tot}	10	mW

Note

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only
and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification
is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability.

ELECTRICAL AND OPT	ICAL CHARACTERISTICS	$(T_{amb} = 25)^{\circ}$	°C, unless	otherwise s	pecified)	
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Cumply assument (nin 2)	$E_{V} = 0, V_{S} = 5 V$	I _{SD}	0.55	0.7	0.9	mA
Supply current (pin 3)	$E_v = 40 \text{ klx, sunlight}$	I _{SH}		0.8		mA
Supply voltage		V _S	2.5		5.5	V
Transmission distance	$E_V = 0$, test signal see fig. 1, IR diode TSAL6200, $I_F = 400 \text{ mA}$	d		25		m
Output voltage low (pin 1)	I _{OSL} = 0.5 mA, E _e = 2 mW/m ² , test signal see fig. 1	V _{OSL}			100	mV
Minimum irradiance	Pulse width tolerance: $t_{pi} - 5/f_o < t_{po} < t_{pi} + 6/f_o,$ test signal see fig. 1	E _{e min.}		0.7	1.2	mW/m²
Maximum irradiance	t_{pi} - 5/f _o < t_{po} < t_{pi} + 6/f _o , test signal see fig. 1	E _{e max.}	50			W/m ²
Directivity	Angle of half transmission distance	Ψ1/2		± 75		deg

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)



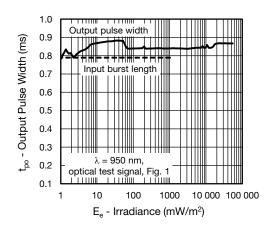


Fig. 2 - Pulse Length and Sensitivity in Dark Ambient

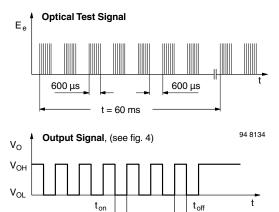


Fig. 3 - Output Function

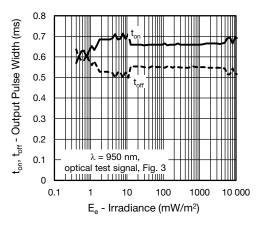


Fig. 4 - Output Pulse Diagram

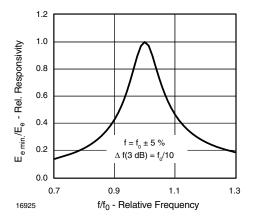


Fig. 5 - Frequency Dependence of Responsivity

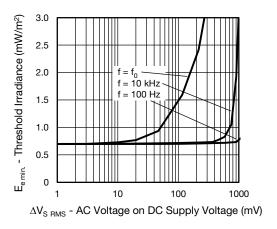


Fig. 6 - Sensitivity vs. Supply Voltage Disturbances

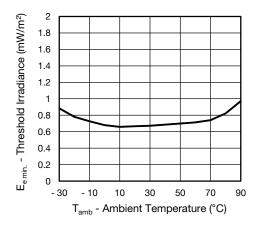


Fig. 7 - Sensitivity vs. Ambient Temperature

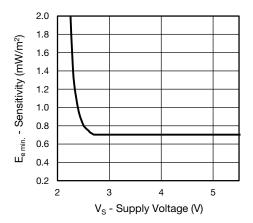


Fig. 8 - Sensitivity vs. Supply Voltage



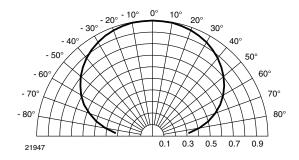
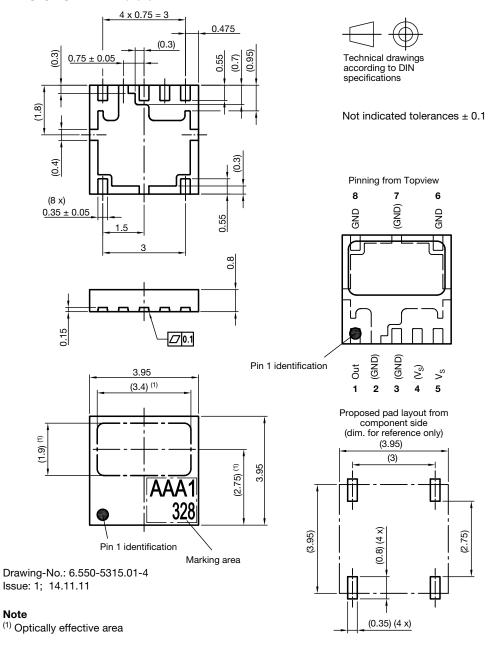


Fig. 9 - Horizontal Directivity

PACKAGE DIMENSIONS in millimeters





ASSEMBLY INSTRUCTIONS

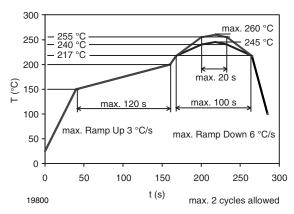
Reflow Soldering

- Reflow soldering must be done within 168 h while stored under a max. temperature of 30 °C, 60 % RH after opening the dry pack envelope
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown in the diagram. Excercise extreme care to keep the maximum temperature below 260 °C. The temperature shown in the profile means the temperature at the device surface. Since there is a temperature difference between the component and the circuit board, it should be verified that the temperature of the device is accurately being measured
- Handling after reflow should be done only after the work surface has been cooled off

Manual Soldering

- Use a soldering iron of 25 W or less. Adjust the temperature of the soldering iron below 300 °C
- Finish soldering within 3 s
- Handle products only after the temperature has cooled off

VISHAY LEAD (Pb)-FREE REFLOW SOLDER PROFILE



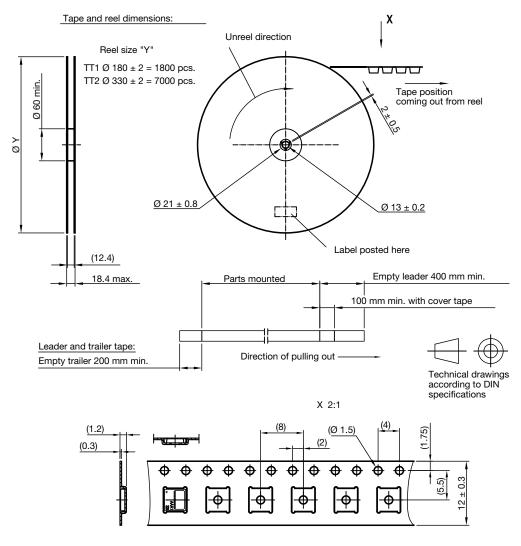
ORDERING INFORMATION			
ORDERING CODE	PACKAGING	VOLUME (1)	REMARKS
TSSP57038TT1	Tana and roal	MOQ: 1800 pcs	3.95 mm x 3.95 mm x 0.75 mm
TSSP57038TT2	Tape and reel	MOQ: 7000 pcs	5.95 Hill X 5.95 Hill X 0.75 Hill

Note

(1) MOQ: minimum order quantity



TAPING VERSION TSSP57038 DIMENSIONS in millimeters



Drawing-No.: 9.700-5347.01-4

Issue: 1; 14.11.11

Not indicated tolerances ± 0.1



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LABEL

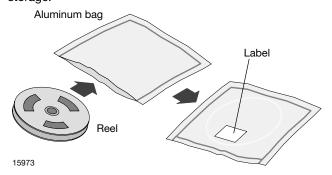
Standard bar code labels for finished goods

The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

PLAIN WRITING	ABBREVIATION	LENGTH
Item-description	-	18
Item-number	INO	8
Selection-code	SEL	3
LOT-/serial-number	BATCH	10
Data-code	COD	3 (YWW)
Plant-code	PTC	2
Quantity	QTY	8
Accepted by	ACC	-
Packed by	PCK	-
Mixed code indicator	MIXED CODE	-
Origin	xxxxxx+	Company logo
Long bar code top	Туре	Length
Item-number	N	8
Plant-code	N	2
Sequence-number	X	3
Quantity	N	8
Total length	-	21
Short bar code bottom	Туре	Length
Selection-code	X	3
Data-code	N	3
Batch-number	X	10
Filter	-	1
Total length	-	17

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 168 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition: 192 h at 40 °C + 5 °C/- 0 °C and < 5 % RH (dry air/nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 125 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JSTD-020 level 3 label is included on all dry bags.



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Caution This bag contains MOISTURE-SENSITIVE DEVICES 1. Calculated shelf life in sealed bag: 12 months at $<\!40^{\circ}\mathrm{C}$ and $<\!90\%$ relative humidity (RH) 260 2. Peak package body temperature: $\underline{260}_{\text{If blank, see adjacent bar code label}} ^{\circ}\text{C}$ 3. After bag is opened, devices that will be subjected to reflow solder or other high temperature process must be ≤30°C/60% RH, or b) Stored per J-STD-033 4. Devices require bake, before mounting, if: a) Humidity Indicator Card reads > 10% for level 2a - 5a devices or >60% for level 2 devices when read at $23\pm5\,^{\circ}\mathrm{C}$ b) 3a or 3b are not met 5. If baking is required, refer to IPC/JEDEC J-STD-033 for bake procedure If blank, see adjacent bar code label Note: Level and body temperature defined by IPC/JEDEC J-STD-020

EIA JEDEC standard JSTD-020 level 3 label is included on all dry bags

ESD PRECAUTION

22650

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electro-static sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

BAR CODE PRODUCT LABEL (example)



2217



Legal Disclaimer Notice

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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.

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