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Features

- 2-kV ESD Protection
- Two Comparators with Common Reference
- Tight Threshold Tolerance
- Threshold Matched to PTC Characteristic of Incandescent Lamps
- Temperature Compensated
- NPN Output
- Interference and Damage-protection According to VDE 0839
- EMI Protection
- Reversal Polarity Protection
- Load-dump Protection

1. Description

The monolithic integrated bipolar circuit, U479B, is designed as a monitor for lamp failure in automobiles. The comparator threshold is matched to the PTC characteristic of incandescent lamps. The threshold is tied to $V_{4,6} = V_S - V_T$ where $V_T = 8$ mV.

If the voltage drop across the shunt resistor, R_{sh} , exceeds 8 mV, the output is turned off, otherwise, the output is turned on. Without supply voltage or open input pin 8, the output is turned off. A comparator input, which is not used, must be connected to pin 7.

V_s V_{Batt} 2 7 R_{sh} Z 5 OUT1 0.6 V_s ||R_{sh} 8 Ĭ 3 OUT2 6 R. **150** Ω Lamps

Figure 1-1. Schematic and Application Circuit



Automotive Lamp-outage Monitor IC

U479B

Rev. 4775B-AUTO-09/05





2. Pin Configuration

Figure 2-1. Pinning DIP8/SO8

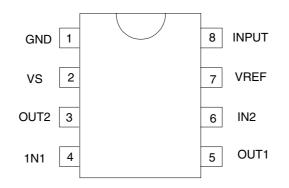


Table 2-1.Pin Description

Pin	Symbol	Function
1	GND	Reference point, ground
2	VS	Supply voltage
3	OUT2	Output 2
4	IN1	Input 1
5	OUT1	Output 1
6	IN2	Input 2
7	VREF	Reference voltage
8	INPUT	Input switch

3. Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

- ·					
Parameters	Pin	Symbol	Value	Unit	
Supply voltage	2, 7	Vs	16.5	V	
Current consumption, t = 2 ms	1	I ₁	1.5	A	
Output current	3, 5	I _{3,5}	20	mA	
Input voltage Reference point pin 7	4, 6	-V _{4,6}	6 V		
Power dissipation T _{amb} = 95°C DIP8 SO8		P _{tot} P _{tot}	420 360	mW mW	
T _{amb} = 60°C DIP8 SO8		P _{tot} P _{tot}	690 560	mW mW	
Ambient temperature range		T _{amb}	-40 to +95	°C	
Storage temperature range		T _{stg}	-55 to +125	°C	
Junction temperature		Tj	150	°C	

4. Thermal Resistance

Parameters		Symbol	Value	Unit
Junction ambient	DIP8	R _{thJA}	110	K/W
	SO8	R _{thJA}	160	K/W



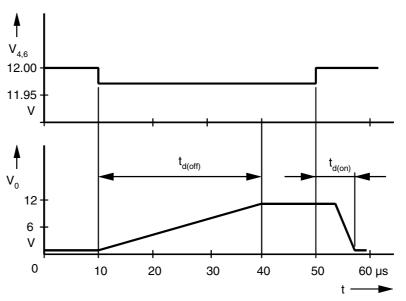
R

5. Electrical Characteristics

 $V_{S} = 9V$ to 15V, $T_{amb} = -40$ to +95°C, Figure 1-1 on page 1, unless otherwise specified.

Parameters	Test Conditions	Pin	Symbol	Min.	Тур.	Max.	Unit
Supply voltage		2, 7	Vs	9		15	V
Internal Z-diode Z ₂		2	Vz	20			V
Current consumption	V _S = 12V	1	l ₁		4.5	6	mA
Output saturation voltage	$V_{S} = 9V, I_{3,5} = 10 \text{ mA}$ $T_{amb} = 25^{\circ}\text{C}$	3, 5	V _{sat}			0.5	v
Control signal threshold	Reference point V _{Pin 7} $I_{3,5} = 3 \text{ mA}$ $V_S = 12V$ $V_S = 15V$	4, 6	-V _T -V _T	6.5 7.8	8 9.3	9.5 10.8	mV mV
Voltage drift	$\Delta V = \frac{V_{T(15 V)} - V_{T(12 V)}}{15 V - 12 V}$		ΔV		0.45		mV/V
Threshold voltage	Switch identification	8	V ₈		0.6 V _S		V
lanut aumanta	Input 1/input 2	4, 6	I _I		100		nA
Input currents	Input switch	8	l _l		5		μA
Delay time	Switch-on, high to low	3, 5	t _{d(on)}		6		μs
Delay time	Switch-off, low to high		t _{d(off)}		30		μs

Figure 5-1. Delay Times

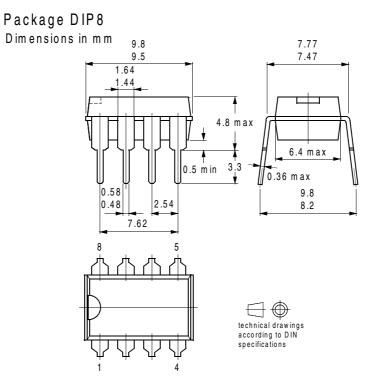


U479B

6. Ordering Information

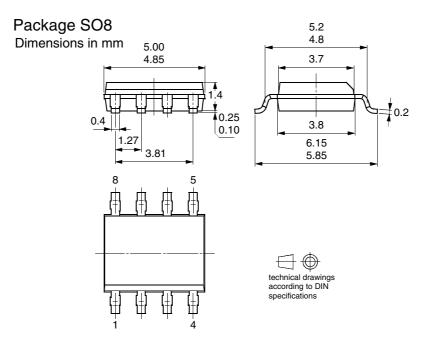
Extended Type Number	Package	Remarks
U479B-MY	DIP8	Pb-free
U479B-MFPY	SO8	Tubed, Pb-free
U479B-MFPG3Y	SO8	Taped and reeled, Pb-free

7. Package Information









8. Revision History

Please note that the following page numbers referred to in this section refer to the specific revision mentioned, not to this document.

Revision No.	History
	Put datasheet in a new template
4775B-AUTO-09/05	Pb-free logo on page 1 added
	 Ordering Information on page 5 changed

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