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With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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uClamp0502T Low Profile µClamp™ 2-Line ESD protection

PROTECTION PRODUCTS - MicroClamp™

Description

The $\mu Clamp^{TM}$ series of TVS arrays are designed to protect sensitive electronics from damage or latch-up due to ESD. It is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and other portable electronics. It features large cross-sectional area junctions for conducting high transient currents. They offer desirable characteristics for board level protection including fast response time, low operating and clamping voltage, and no device degradation.

The $\mu\text{Clamp}^{\text{TM}}\text{O}502\text{T}$ is in a 3-pin, RoHS/WEEE compliant, SLP1006P3T package. It measures 1.0 x 0.6 mm with a nominal height of only 0.4mm. The leads are spaced at a pitch of 0.65mm and are finished with lead-free NiPdAu. Each device will protect two lines operating at 5 volts. It gives the designer the flexibility to protect single lines in applications where arrays are not practical. They may be used to meet the ESD immunity requirements of IEC 61000-4-2, Level 4 (±15kV air, ±8kV contact discharge). The combination of small size and high ESD surge capability makes them ideal for use in portable applications such as cellular phones, digital cameras, and MP3 players.

Features

- ◆ Transient protection for data lines to IEC 61000-4-2 (ESD) ±15kV (air), ±8kV (contact) IEC 61000-4-4 (EFT) 40A (tp = 5/50ns) Cable Discharge Event (CDE)
- Ultra-small package (1.0 x 0.6 x 0.4mm)
- Protects two data lines
- Low clamping voltage
- Working voltage: 5V
- Low leakage current
- Solid-state silicon-avalanche technology

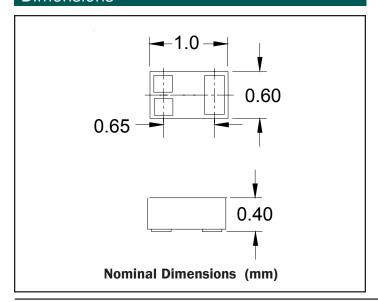
Mechanical Characteristics

- ◆ SLP1006P3T package
- ◆ RoHS/WEEE Compliant
- ◆ Nominal Dimensions: 1.0 x 0.6 x 0.4 mm
- Lead Finish: NiPdAu
- Molding compound flammability rating: UL 94V-0
- Marking: Marking code, cathode band
- Packaging : Tape and Reel

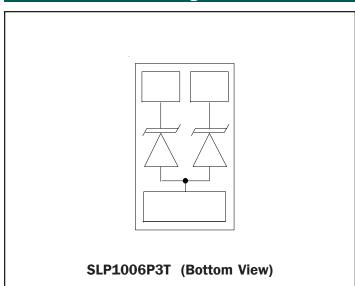
Applications

- Cellular Handsets & Accessories
- Personal Digital Assistants (PDAs)
- Notebooks & Handhelds
- Portable Instrumentation
- Digital Cameras
- Peripherals
- MP3 Players

Dimensions



Schematic & PIN Configuration





Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power (tp = 8/20μs)	P _{pk}	25	Watts
Maximum Peak Pulse Current (tp = 8/20μs)	I _{pp}	2	Amps
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V _{ESD}	+/- 20 +/- 15	kV
Operating Temperature	T _J	-55 to +125	°C
Storage Temperature	T _{STG}	-55 to +150	°C

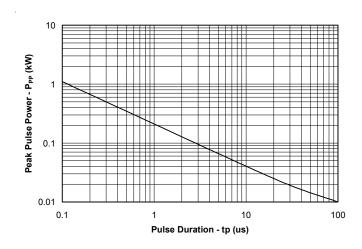
Electrical Characteristics (T=25°C)

Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V _{RWM}				5	V
Reverse Breakdown Voltage	V _{BR}	I _t = 1mA	6			V
Reverse Leakage Current	I _R	V _{RWM} = 5V, T=25°C			0.25	μΑ
Forward Voltage	V _F	I _F = 10mA		1	1.2	V
Clamping Voltage	V _c	$I_{pp} = 2A, t_p = 8/20\mu s$			12.5	V
Junction Capacitance	C _j	V _R = OV, f = 1MHz			10	pF
Junction Capacitance	C _j	V _R = 3.3V, f = 1MHz		4.5		pF

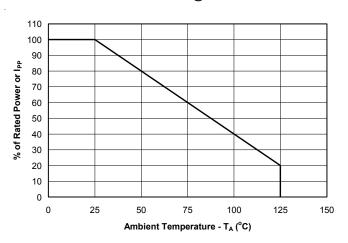


Typical Characteristics

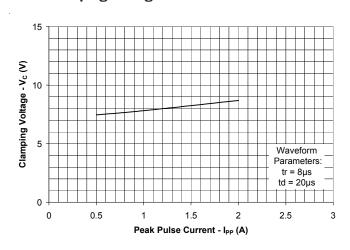
Non-Repetitive Peak Pulse Power vs. Pulse Time



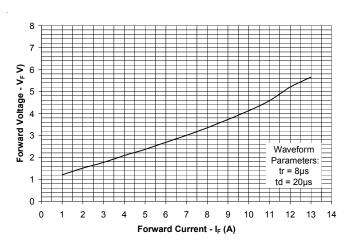
Power Derating Curve



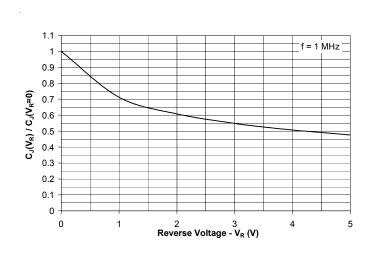
Clamping Voltage vs. Peak Pulse Current



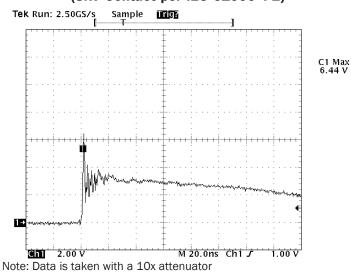
Forward Voltage vs. Forward Current



Junction Capacitance vs. Reverse Voltage



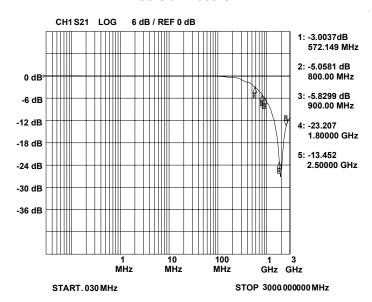
ESD Clamping (8kV Contact per IEC 61000-4-2)



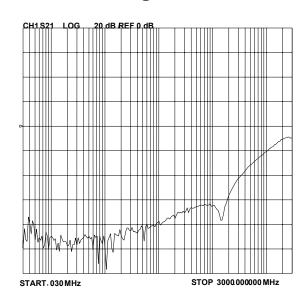


Typical Characteristics

Insertion Loss S21



Analog Crosstalk





Applications Information

Device Connection Options

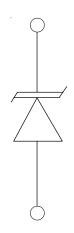
This device is designed to protect two data lines. The device is unidirectional and may be used on lines where the signal polarity is above ground. The cathode band should be placed towards the line that is to be protected.

Circuit Board Layout Recommendations for Suppression of ESD.

Good circuit board layout is critical for the suppression of ESD induced transients. The following guidelines are recommended:

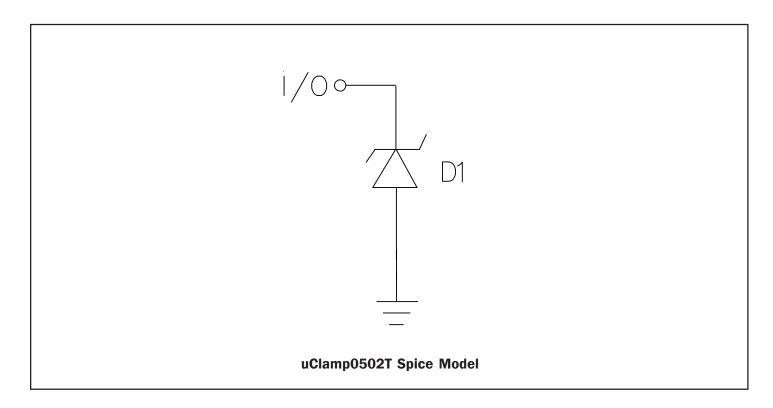
- Place the TVS near the input terminals or connectors to restrict transient coupling.
- Minimize the path length between the TVS and the protected line.
- Minimize all conductive loops including power and ground loops.
- The ESD transient return path to ground should be kept as short as possible.
- Never run critical signals near board edges.
- Use ground planes whenever possible.

Circuit Diagram





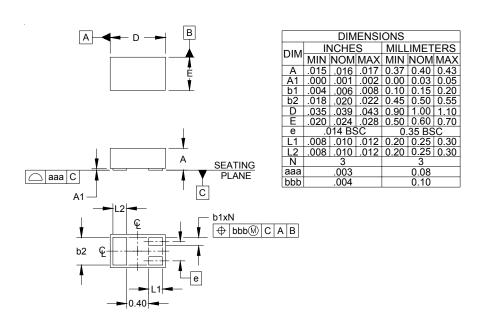
Applications Information - Spice Model



uClamp0502T Spice Parameters						
Parameter	Unit	D1 (TVS)				
IS	Amp	2.05e-15				
BV	Volt	7.0				
VJ	Volt	0.80				
RS	Ohm	0.75				
IBV	Amp	1.0E-3				
CJO	Farad	9e-12				
TT	sec	2.541E-9				
M		0.25				
N		1.1				
EG	eV	1.11				



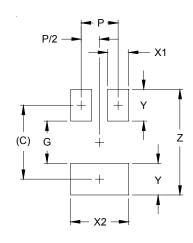
Outline Drawing - SLP1006P3T



NOTES:

1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).

Land Pattern - SLP1006P3T



DIMENSIONS						
DIM	INCHES	MILLIMETERS				
С	(.028)	(0.70)				
G	.016	0.40				
Р	.014	0.35				
X1	.008	0.20				
X2	.022	0.55				
Υ	.012	0.30				
Z	.039	1.00				

NOTES:

- 1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
- 2. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.



Marking Code



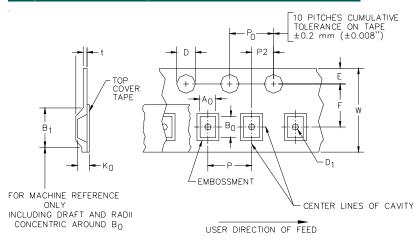
Ordering Information

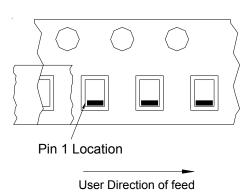
Part Number	Working	Qty per	Reel	
	Voltage	Reel	Size	
uClamp0502T.TCT	5V	3,000	7 Inch	

Notes:

1) This is a lead-free, RoHS/WEEE compliant product MicroClamp, uClamp and μClamp are marks of Semtech Corporation

Tape and Reel Specification





Device Orientation in Tape

A0	во	ко		
0.69 +/-0.10 mm	1.19 +/-0.10 mm	0.66 +/-0.10 mm		

Tape Width	B, (Max)	D	D1	E	F	Р	PO	P2	Т	W
8 mm	4.2 mm (.165)	1.5 + 0.1 mm - 0.0 mm (0.59 +.005 000)	0.4 mm ±0.25 (.031)	1.750±.10 mm (.069±.004)	3.5±0.05 mm (.138±.002)	4.0±0.10 mm (.157±.00- 4)	4.0±0.1 mm (.157±.00- 4)	2.0±0.05 mm (.079±.002)	0.254±0.02 mm (.016)	8.0 mm + 0.3 mm - 0.1 mm (.312±.012)

Contact Information

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