# imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

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### Contact us

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#### RF Power MOSFET Transistor 20W, 100-500 MHz, 28V

#### Features

- N-channel enhancement mode device
- DMOS structure
- Lower capacitances for broadband operation
- Common source configuration
- Lower noise floor

#### ABSOLUTE MAXIMUM RATINGS AT 25° C

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V <sub>DS</sub>	65	V
Gate-Source Voltage	$V_{GS}$	20	V
Drain-Source Current	I <sub>DS</sub>	2.8	А
Power Dissipation	PD	53	W
Junction Temperature	TJ	200	°C
Storage Temperature	T <sub>STG</sub>	-55 to 150	°C
Thermal Resistance	$\theta_{\rm JC}$	3.3	°C/W

#### TYPICAL DEVICE IMPEDANCES

F (MHz)	Z <sub>IN</sub> (Ω)	Z <sub>LOAD</sub> (Ω)			
100	9.5-j60.0	4.0+j68.0			
300	5.0-j35.0	40.0+j48.0			
500	2.0-j22.0	36.0+j34.0			
V <sub>DD</sub> =28V, I <sub>DQ</sub> =200 mA, P <sub>OUT</sub> =20.0 W					

 $Z_{\mbox{\scriptsize IN}}$  is the series equivalent input impedance of the device from gate to source.

 $Z_{\text{LOAD}}$  is the optimum series equivalent load impedance as measured from drain to ground.

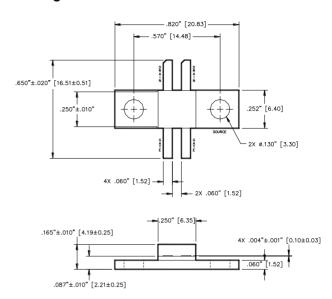
#### **ELECTRICAL CHARACTERISTICS AT 25°C**

Parameter	Symbol	Min	Max	Units	Test Conditions
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	65	-	V	$V_{GS} = 0.0 \text{ V}$ , $I_{DS} = 4.0 \text{ mA}$
Drain-Source Leakage Current	I <sub>DSS</sub>	-	2.0	mA	$V_{GS} = 28.0 \text{ V}$ , $V_{GS} = 0.0 \text{ V}$
Gate-Source Leakage Current	I <sub>GSS</sub>	-	2.0	μA	V <sub>GS</sub> = 20.0 V , V <sub>DS</sub> = 0.0 V
Gate Threshold Voltage	V <sub>GS(TH)</sub>	2.0	6.0	V	V <sub>DS</sub> = 10.0 V , I <sub>DS</sub> = 200.0 mA
Forward Transconductance	Gм	.160	-	S	$V_{\text{DS}}$ = 10.0 V , $I_{\text{DS}}$ 200.0 mA , $\Delta$ $V_{\text{GS}}$ = 1.0V, 80 $\mu s$ Pulse
Input Capacitance	C <sub>ISS</sub>	-	14	pF	V <sub>DS</sub> = 28.0 V , F = 1.0 MHz
Output Capacitance	C <sub>oss</sub>	-	10	pF	V <sub>DS</sub> = 28.0 V , F = 1.0 MHz
Reverse Capacitance	C <sub>RSS</sub>	-	4.8	pF	V <sub>DS</sub> = 28.0 V , F = 1.0 MHz
Power Gain	G <sub>P</sub>	10	-	dB	$V_{DD}$ = 28.0 V, $I_{DQ}$ = 200.0 mA, $P_{OUT}$ = 20.0 W F =500 MHz
Drain Efficiency	ŋ <sub>D</sub>	50	-	%	V <sub>DD</sub> = 28.0 V, I <sub>DQ</sub> = 200.0 mA, P <sub>OUT</sub> = 20.0 W F =500 MHz
Load Mismatch Tolerance	VSWR-T	-	20:1	-	V <sub>DD</sub> = 28.0 V, I <sub>DQ</sub> = 200.0 mA, P <sub>OUT</sub> = 20.0 W F =500 MHz

<sup>1</sup> 

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#### Package Outline



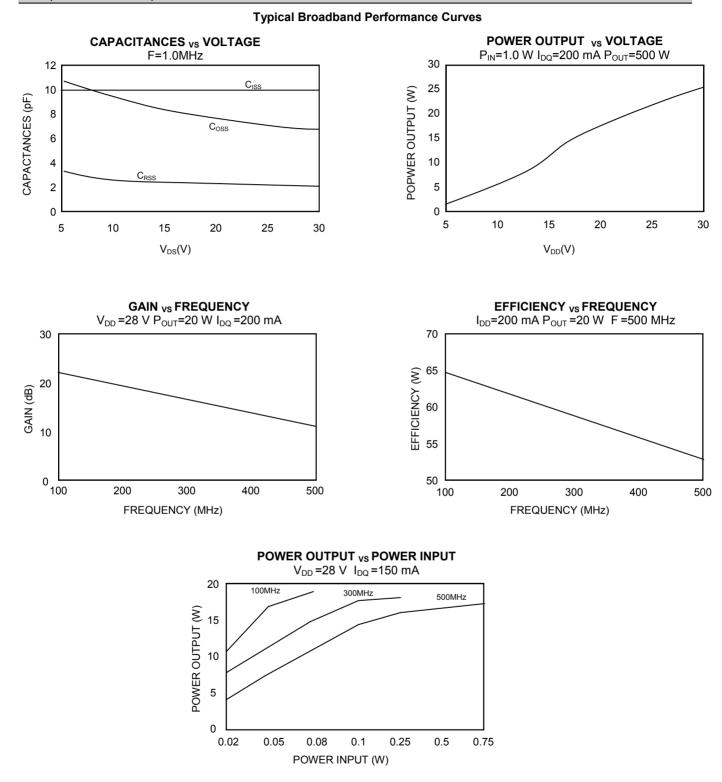
UNLESS OTHERWISE NOTED, TOLERANCES ARE INCHES  $\pm .005"$  [MILLIMETERS  $\pm 0.13$ mm]





### RF Power MOSFET Transistor 20W, 100-500 MHz, 28V

Rev. V1

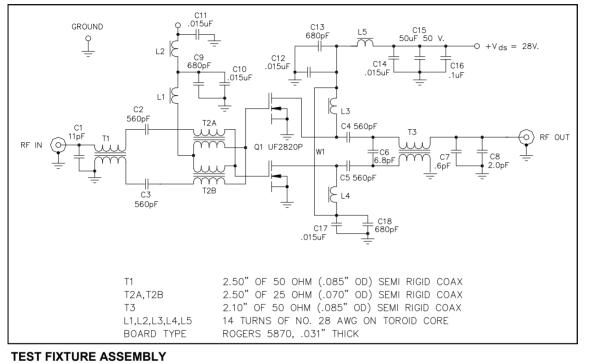


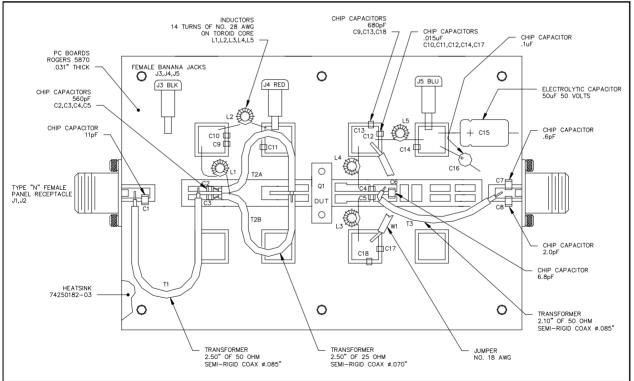
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#### TEST FIXTURE SCHEMATIC





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